

Contemporary Logic Design (2nd Edition)



BOOK DETAIL

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Book Description

BASIC APPROACH In the past ten years there has been a revolution in the practice of hardware design. Professionals now rely on CAD software, rapid prototyping, and programmable logic devices to streamline the design process. Contemporary Logic Design is the first text to address these changes — and to offer a truly modern introduction to logic design. Throughout, the author complements his presentation of logic design theory with discussions of current design technologies. Approximately 60% of the book presents new material; the remainder has been re-organized and partially re-written to correspond to the organizational changes.

Contemporary Logic Design 2nd Edition Solution

sir George Christopher T. Bartley



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Contemporary Logic Design Randy H. Katz,1994 This text demonstrates state of the art technologies for the design of modern logic circuits including CAD tools rapid prototyping and programmable logic devices It provides practice in traditional techniques of logic design and includes examples of implementations from many CAD tools FPGA Prototyping by VHDL Examples Pong P. Chu,2011-09-20 This book uses a learn by doing approach to introduce the concepts and techniques of VHDL and FPGA to designers through a series of hands on experiments FPGA Prototyping by VHDL Examples provides a collection of clear easy to follow templates for quick code development a large number of practical examples to illustrate and reinforce the concepts and design techniques realistic projects that can be implemented and tested on a Xilinx prototyping board and a thorough exploration of the Xilinx PicoBlaze soft core microcontroller **Contemporary Logic Design** Randy H. Katz,Gaetano Borriello,2006 Embedded SoPC Design with Nios II Processor and Verilog Examples Pong P. Chu,2012-05-14 Explores the unique hardware programmability of FPGA based embedded systems using a learn by doing approach to introduce the concepts and techniques for embedded SoPC design with Verilog An SoPC system on a programmable chip integrates a processor memory modules I O peripherals and custom hardware accelerators into a single FPGA field programmable gate array device In addition to the customized software customized hardware can be developed and incorporated into the embedded system as well allowing us to configure the soft core processor create tailored I O interfaces and develop specialized hardware accelerators for computation intensive tasks Utilizing an Altera FPGA prototyping board and its Nios II soft core processor Embedded SoPC Design with Nios II Processor and Verilog Examples takes a learn by doing approach to illustrate the hardware and software design and development process by including realistic projects that can be implemented and tested on the board Emphasizing hardware design and integration throughout the book is divided into four major parts Part I covers HDL and synthesis of custom hardware Part II introduces the Nios II processor and provides an overview of embedded software development Part III demonstrates the design and development of hardware and software of several complex I O peripherals including a PS2 keyboard and mouse a graphic video controller an audio codec and an SD secure digital card Part IV provides several case studies of the integration of hardware accelerators including a custom GCD greatest common divisor circuit a Mandelbrot set fractal circuit and an audio synthesizer based on DDFS direct digital frequency synthesis methodology While designing and developing an embedded SoPC can be rewarding the learning can be a long and winding journey This book shows the trail ahead and guides readers through the initial steps to exploit the full potential of this emerging methodology **Embedded SoPC Design with Nios II Processor and VHDL Examples** Pong P. Chu,2011-09-26 The book is divided into four major parts Part I covers HDL constructs and synthesis of basic digital circuits Part II provides an overview of embedded software development with the emphasis on low level I O access and drivers Part III demonstrates the design and development of hardware and software for several complex I O

peripherals including PS2 keyboard and mouse a graphic video controller an audio codec and an SD secure digital card Part IV provides three case studies of the integration of hardware accelerators including a custom GCD greatest common divisor circuit a Mandelbrot set fractal circuit and an audio synthesizer based on DDFS direct digital frequency synthesis methodology The book utilizes FPGA devices Nios II soft core processor and development platform from Altera Co which is one of the two main FPGA manufactures Altera has a generous university program that provides free software and discounted prototyping boards for educational institutions details at www.altera.com/university The two main educational prototyping boards are known as DE1 99 and DE2 269 All experiments can be implemented and tested with these boards A board combined with this book becomes a turn key solution for the SoPC design experiments and projects Most HDL and C codes in the book are device independent and can be adapted by other prototyping boards as long as a board has similar I O configuration

Contemporary Logic Design Randy H. Katz,1994 *Subject Guide to Books in Print* ,1996 Electronic Experiences in a Virtual Lab Roberto Gastaldi,Giovanni Campardo,2020-05-11 This book presents a collection of lessons on various topics commonly encountered in electronic circuit design including some basic circuits and some complex electronic circuits which it uses as vehicles to explain the basic circuits they are composed of The circuits considered include a linear amplifier oscillators counters a digital clock power supplies a heartbeat detector a sound equalizer an audio power amplifier and a radio The theoretical analysis has been deliberately kept to a minimum in order to dedicate more time to a learning by doing approach which after a brief review of the theory readers are encouraged to use directly with a simulator tool to examine the operation of circuits in a virtual laboratory Though the book is not a theory textbook readers should be familiar with the basic principles of electronic design and with spice like simulation tools To help with the latter aspect one chapter is dedicated to the basic functions and commands of the OrCad P spice simulator used for the experiments described in the book

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