

FEATURES

64x decimation of a stereo pulse density modulation (PDM)
 bit stream to pulse code modulation (PCM) audio data
 Slave I²S or time division multiplexed (TDM) output interface
 Configurable TDM slots
 I/O supply operation: 1.62 V to 3.6 V
 64x output sample rate PDM clock
 64x/128x/192x/256x/384x/512x output sample rate BCLK
 Automatic BCLK ratio detection
 Output sample rate: 4 kHz to 96 kHz
 Automatic PDM CLK drive at 64x the sample rate
 Automatic power down with BCLK removal
 0.67 mA operating current at 48 kHz and 1.8 V IOVDD supply
 Shutdown current: <1 μ A
 8-ball, 1.56 mm $\times$ 0.76 mm, 0.4 mm pitch WLCSP
 Power-on reset

APPLICATIONS

Mobile computing
 Portable electronics
 Consumer electronics

GENERAL DESCRIPTION

The **ADAU7002** converts a stereo PDM bit stream into a PCM output. The source for the PDM data can be two microphones or other PDM sources. The PCM audio data is output on a serial audio interface port in either I²S or TDM format.

The **ADAU7002** is specified over the commercial temperature range (-40°C to $+85^{\circ}\text{C}$). It is available in a halide-free, 8-ball, 1.56 mm $\times$ 0.76 mm, wafer level chip scale package (WLCSP).

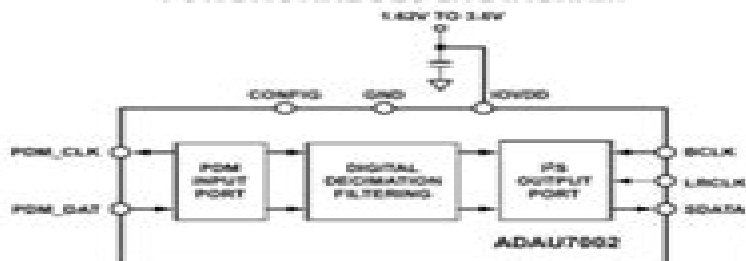
FUNCTIONAL BLOCK DIAGRAM


Figure 1.

Rev. A

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Stereo Pdm To I Tdm Conversion Ic Data Sheet Adau7002

Ying-Ying Zheng



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