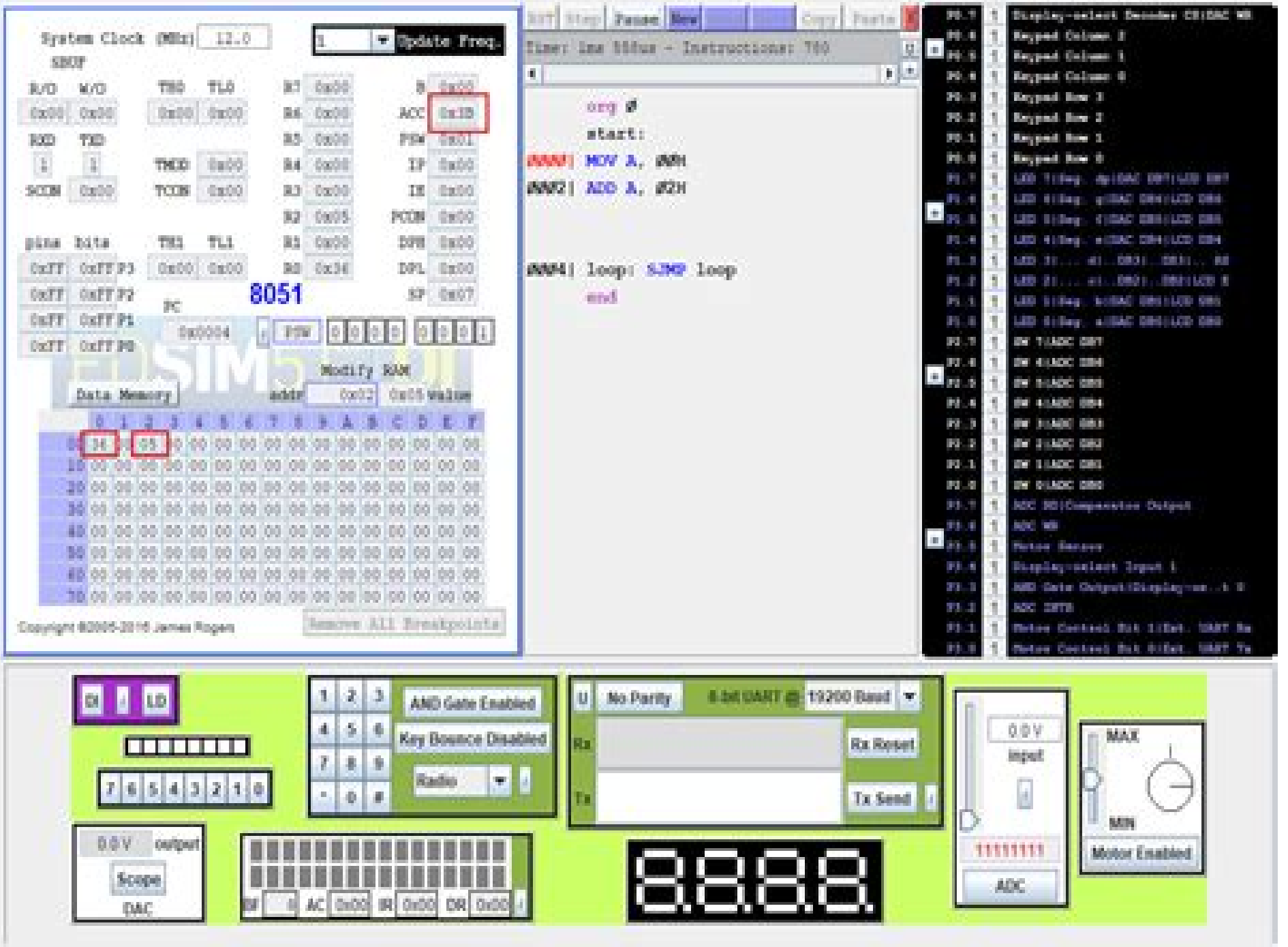
System Clock (MHz) 12.0
Update Freq.
SBUF
R/O W/O TH0 TL0
RXD TXD
SCON
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pins bits TH1 TL1
PC
8051
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org 0
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MOV A, 0H
ADD A, 2H
loop: SJMP loop
end
Keyped Column 2
Keyped Column 1
Keyped Column 0
Keyped Row 3
Keyped Row 2
Keyped Row 1
Keyped Row 0
Motor Sensor
AND Gate Enabled
Key Bounce Disabled
Radio
No Parity
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