



XADC Header

User SMA Clock

User SMA

User LEDs

PMBus Connector

SFP/SFP+ Connector

RAM (MPC, 4x GTX)

RAM (LPC, 1X GTX)

SD Card Interface

Power Switch

Linear SPI Flash Memory (128MB)

Config Mode/Upper Linear Flash Address Switch

12V Power

USB 1540 Interface

User DIP Switch

Status LEDs

FPGA Prog Push Button

10/100/1000 MHz Ethernet PHY

User Push Buttons

HDMI Output

GTX SMA Transceiver and Clock

User Rotary Switch (located under LCD)

LCD Character Display

PCI Express Connector (8-lane)

Xilinx XC7K325T-2FFG800C FPGA

DDR3 SODIMM Memory (1GB)

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Kathleen Armour



Kintex 7 Fpga Embedded Targeted Reference Design:

Whispering the Techniques of Language: An Psychological Journey through **Kintex 7 Fpga Embedded Targeted Reference Design**

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