

# NPCT42x Trusted Platform Module (TPM)

## General Description

The NPCT42x single-chip Trusted Platform Module (TPM) is a family of high-performance, NuvoTon SecureChip™ technology devices. The devices implement the Trusted Computing Group (TCG) version 1.2 specifications for PC-Client TPM.

The NPCT42x devices are designed as feature-rich system boot time and trusted OS loading devices. They provide a solution for PC security for a wide range of PC applications.

The NPCT42x family of devices are Microsoft® Windows™ compliant and are supported by Linux kernel v2.6.18 and higher.

## Features

### General

- PC-Client TPM solution
  - TPM 1.2 compliant
  - Supports legacy BIOS as required
  - Supports TCG version 1.2 specifications
  - Supports TPM interface
- Host Interface
  - TPM 1.2 standard interface
  - Supports legacy facility by using TIS protocol
  - I2C mapped registers
- Secure General Purpose I/O (GPIO)
  - 4x8-bit GPIO pins
  - I/O pins individually configured as input or output
  - Configurable internal pull-up resistor
  - TCG 1.2-defined interface
  - Dedicated Physical Presence (PP) pin with configurable pull-up or pull-down resistor
- Clock Generator

### Bus Interface

- LPC Bus Interface
  - Based on Intel's LPC Interface Specification, Rev. 4.0 (v4.0), August 2000
  - TPM 1.2 interface (TIS)

### Clocking and Supply

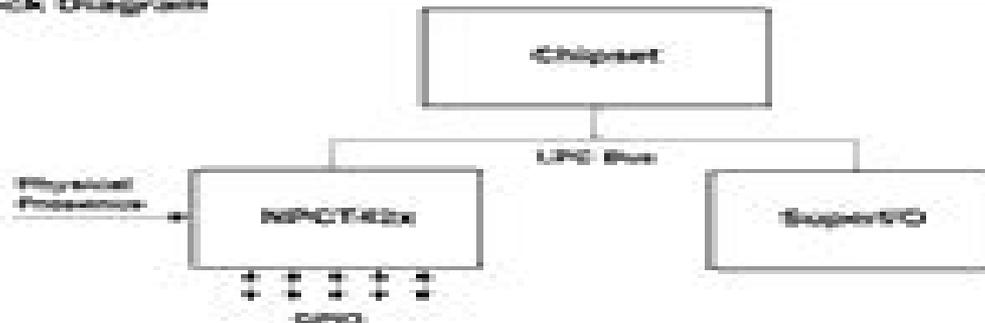
- On-Chip-Clock Generator
- Power Supply
  - 3.3V supply operation
  - Separate pins for main (VDD) and standby (VDD) power supplies
  - Low standby power consumption

### Software

- TPM BIOS, Secure Memory Access (SMA) and Memory Protect (MP)
- TPM Device Driver for Microsoft Windows
- TPM Cryptosystems (supported by Secure Intel® BIOS, Intel® BIOS, and BIOS (CTD))
- TPM Device Driver for Linux (TPM) or Embedded Linux (TPM)



## System Block Diagram



# Nuvoton Datasheet

**Scorpion King Jr**



## **Nuvoton Datasheet:**

*Microcontroller Exploits* Travis Goodspeed,2024-09-10 *Microcontroller Exploits* is a deep dive into advanced hardware hacking with detailed examples of real world techniques and a comprehensive survey of vulnerabilities In this advanced guide to hardware hacking you ll learn how to read the software out of single chip computers especially when they are configured not to allow the firmware to be extracted This book documents a very wide variety of microchip hacking techniques it s not a beginner s first introduction You ll start off by exploring detailed techniques for hacking real world chips such as how the STM32F0 allows for one word to be dumped after every reset You ll see how the STM32F1 s exception handling can slowly leak the firmware out over an hour and how the Texas Instruments MSP430 firmware can be extracted by a camera flash For each exploit you ll learn how to reproduce the results dumping a chip in your own lab In the second half of the book you ll find an encyclopedic survey of vulnerabilities indexed and cross referenced for use in practicing hardware security

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