

On Chip Transformer Design And Modeling For Fully

RS Peters



On Chip Transformer Design And Modeling For Fully:

On-chip Transformer Design and Modeling for Fully Integrated Isolated DC/DC Converters Yao Zhao,2014

Isolated DC DC converters are used to provide electrical isolation between two supply domain systems A fully integrated isolated DC DC converter having no board level components and fabricated using standard integrated circuits IC process is highly desirable in order to increase the system reliability and reduce costs The isolation between the low voltage side and high voltage side of the converter is realized by a transformer that transfers energy while blocking the DC loop The resonant mode power oscillator is used to enable high efficiency power transfer The on chip transformer is expected to have high coil inductance high quality factors and high coupling coefficient to reduce the loss in the oscillation The performance of a transformer is highly dependent on the vertical structure horizontal geometry and other indispensable structures that make it compatible with the IC process such as metal fills and patterned ground shield PGS With the help of three dimensional 3 D electro magnetic EM simulation software the 3 D transformer model is simulated and the simulation result is got with high accuracy In this thesis an on chip transformer for a fully integrated DC DC converter using standard IC process is developed Different types of transformers are modeled and simulated in HFSS The performances are compared to select the optimum design The effects of the additional structures including PGS and metal fills are also simulated The transformer is tested with a network analyzer and the testing results show a good consistency with the simulation results when taking the chip traces printed circuit board PCB traces bond wires and SMA connectors into account

Radio Design in Nanometer Technologies Mohammed Ismail,Delia R. de Llera González,2007-06-16 Radio Design in Nanometer Technologies addresses current trends and future directions in radio design for wireless applications As radio transceivers constitute the major bottleneck in a wireless chipset in terms of power consumption and die size the radio must be designed in the context of the entire system end to end Therefore the book will address wireless systems as well as the DSP parts before it gets into coverage of radio design issues As such the book is the first volume that looks at the integrated radio design problem as a piece of a big puzzle namely the entire chipset or single chip that builds an entire wireless system This is the only way to successfully design radios to meet the stringent demands of today s increasingly complex wireless systems

Linear CMOS RF Power Amplifiers for Wireless Applications Paulo Augusto Dal Fabbro, Maher Kayal,2010-06-22 Advances in electronics have pushed mankind to create devices ranging from credible gadgets to medical equipment to spacecraft instruments More than that modern society is getting used to if not dependent on the comfort solutions and astonishing amount of information brought by these devices One eld that has continuously benefited from those advances is the radio frequency integrated circuit RFIC design which in its turn has promoted countless benefits to the mankind as a payback Wireless communications is one prominent example of what the advances in electronics have enabled and their consequences to our daily life How could anyone back in the eighties think of the possibilities opened by the wireless local area networks WLANs that can be found today in a

host of places such as public libraries coffee shops trains to name just a few How can a youngster who lives this true WLAN experience nowadays imagine a world without it This book deals with the design of linear CMOS RF Power Amplifiers PAs The RF PA is a very important part of the RF transceiver the device that enables wireless communications Two important aspects that are key to keep the advances in RF PA design at an accelerated pace are treated efficiency enhancement and frequency tunable capability For this purpose the design of two different integrated circuits realized in a 0.11 μm technology is presented each one addressing a different aspect With respect to efficiency enhancement the design of a dynamic supply RF power amplifier is treated making up the material of Chaps 2 to 4

Proceedings of the ... Midwest Symposium on Circuits and Systems, 2003

RF and mm-Wave Power Generation in Silicon Hua Wang, Kaushik Sengupta, 2015-12-10 RF and mm Wave Power Generation in Silicon presents the challenges and solutions of designing power amplifiers at RF and mm Wave frequencies in a silicon based process technology It covers practical power amplifier design methodologies energy and spectrum efficient power amplifier design examples in the RF frequency for cellular and wireless connectivity applications and power amplifier and power generation designs for enabling new communication and sensing applications in the mm Wave and THz frequencies With this book you will learn Power amplifier design fundamentals and methodologies Latest advances in silicon based RF power amplifier architectures and designs and their integration in wireless communication systems State of the art mm Wave THz power amplifier and power generation circuits and systems in silicon Extensive coverage from fundamentals to advanced design topics focusing on various layers of abstraction from device modeling and circuit design strategy to advanced digital and mixed signal architectures for highly efficient and linear power amplifiers New architectures for power amplifiers in the cellular and wireless connectivity covering detailed design methodologies and state of the art performances Detailed design techniques trade off analysis and design examples for efficiency enhancement at power back off and linear amplification for spectrally efficient non constant envelope modulations Extensive coverage of mm Wave power generation techniques from the early days of the 60 GHz research to current state of the art reconfigurable digital mm Wave PA architectures Detailed analysis of power generation challenges in the higher mm Wave and THz frequencies and novel technical solutions for a wide range of potential applications including ultrafast wireless communication to sensing imaging and spectroscopy Contributions from the world class experts from both academia and industry

Transformers and Inductors for Power Electronics W.G. Hurley, W.H. Wölfle, 2013-04-29 Based on the fundamentals of electromagnetics this clear and concise text explains basic and applied principles of transformer and inductor design for power electronic applications It details both the theory and practice of inductors and transformers employed to filter currents store electromagnetic energy provide physical isolation between circuits and perform stepping up and down of DC and AC voltages The authors present a broad range of applications from modern power conversion systems They provide rigorous design guidelines based on a robust methodology for inductor and transformer design They offer real

design examples informed by proven and working field examples Key features include emphasis on high frequency design including optimisation of the winding layout and treatment of non sinusoidal waveforms a chapter on planar magnetic with analytical models and descriptions of the processing technologies analysis of the role of variable inductors and their applications for power factor correction and solar power unique coverage on the measurements of inductance and transformer capacitance as well as tests for core losses at high frequency worked examples in MATLAB end of chapter problems and an accompanying website containing solutions a full set of instructors presentations and copies of all the figures Covering the basics of the magnetic components of power electronic converters this book is a comprehensive reference for students and professional engineers dealing with specialised inductor and transformer design It is especially useful for senior undergraduate and graduate students in electrical engineering and electrical energy systems and engineers working with power supplies and energy conversion systems who want to update their knowledge on a field that has progressed considerably in recent years

Microwave Journal ,2001 **Analysis and Modeling of Monolithic On-chip Transformers on Silicon Substrates** Yevgeniy Mayevskiy,2005 Passive components including spiral inductors and transformers fabricated on silicon based substrates are placing an increasing demand on radio frequency integrated circuit RFIC design Performance of the RFIC suffers from several non ideal effects that must be taken into account in order to create a successful design In particular monolithic transformers can be a major obstacle for Low Noise Amplifiers LNAs mixers Voltage Controlled Oscillators VCOs and other RFIC circuits For designers to correctly analyze their designs that include monolithic transformers lumped element models suitable for circuit simulation and design optimization are required This thesis work is mainly concerned with the analysis and the methodology of developing a wide band compact equivalent circuit model for monolithic transformers fabricated on silicon substrates A new wide band compact equivalent circuit model for monolithic transformers fabricated on silicon substrates is presented The model achieves high wide band accuracy through the use of the newly developed Frequency Dependent Transformer FDT cell which effectively takes into account the frequency dependent conductor losses associated with the transformer s windings A fast extraction technique that allows extraction of the circuit parameters from simulated or measured data is also described Results are presented for various RFIC transformer configurations including stacked and interleaved topologies This research provides a complete measurement or simulation based modeling methodology for four port transformers on lossy silicon substrates

IEEE International Conference on Electronics, Circuits and Systems ,2003 **On-chip High Voltage Generation Using Mechanical Oscillators** Kabir J. Udeshi,2006 **Scientific and Technical Aerospace Reports** ,1982 Integrated Inductors and Transformers Egidio Ragonese,Angelo Scuderi,Tonio Biondi,Giuseppe Palmisano,2010-11-15 With the ability to improve performance reduce fabrication costs and increase integration levels of both RX and TX sections of the RF mm wave front end passive inductive components have experienced extraordinary growth in ICs Therefore a fundamental

understanding of monolithic inductors and transformers has become essential for all process eng **IEEE Circuits & Devices** ,1998 The Design, Modeling and Optimization of On-chip Inductor and Transformer Circuits Sunderarajan S. Mohan,1999 **VLSI Analog Circuits: Algorithms, Architecture, Modeling, and Circuit Implementation** Hongjiang Song,2016-09-16 VLSI Signal Processing Principles Practices and Applications This comprehensive resource shows how very large scale integration VLSI technology can be effectively deployed in real world electronics to meet cost power function and reliability requirements VLSI Analog Circuits Algorithm Architecture Modeling and Circuit Implementation Second Edition is a textbook for advanced electrical engineering courses that shows step by step how to analyze and solve practical design problems using VLSI You will get up to date discussions on VLSI passive active RC MOS C Gm C CTI SC and SI analog filter circuits Mixed mode configurations VLSI RF signal processing and circuit tuning techniques are explained in full detail Coverage includes VLSI continuous time signal processing fundamentals VLSI active RC MOS C and VLSI Gm C circuits VLSI continuous time current mode filters VLSI discrete time signal processing systems VLSI switched capacitor and switched current circuits Frequency scaling and transformation techniques Mixed mode VLSI analog signal processing Component and ladder simulation based VLSI design Practical design aspects of VLSI analog filters VLSI RF signal processing circuits Digital based analog signal processing circuits **Electrical & Electronics Abstracts** ,1997 **Science Abstracts** ,1995 Electronic Design ,1996 **IEEE ... Microwave and Millimeter-Wave Monolithic Circuits Symposium Digest of Papers** , **Technical Proceedings** ,1996 The papers presented at CIEP aim at achieving lower costs through efficient use of power in industrial and business applications Approaches to designing and building these techniques into operations are presented Lower power demands through efficiencies in consumer products are described leading to portable product designs

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