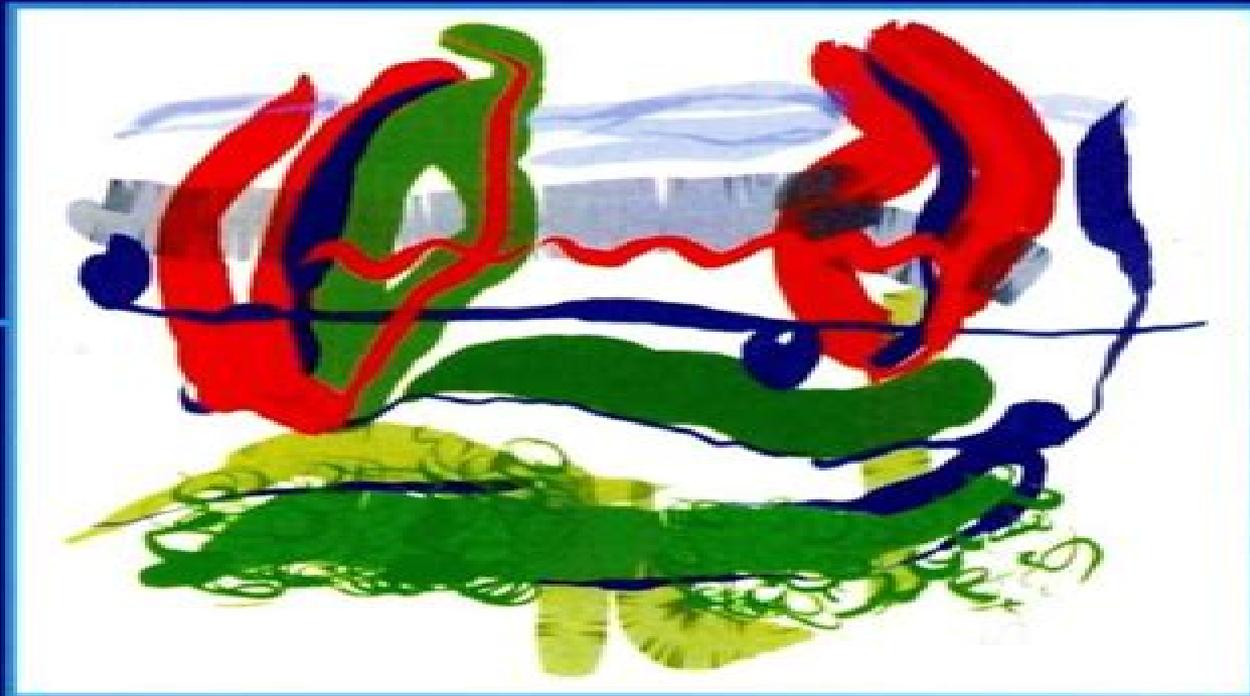


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AN INTRODUCTION TO DIGITAL COMPUTER DESIGN, FIFTH EDITION V. RAJARAMAN,T.

RADHAKRISHNAN,2008-03-01 This highly acclaimed well established book now in its fifth edition is intended for an introductory course in digital computer design for B Sc students of computer science B Tech students of computer science and engineering and BCA MCA students of computer applications A knowledge of programming in C or Java would be useful to give the student a proper perspective to appreciate the development of the subject The first part of the book presents the basic tools and develops procedures suitable for the design of digital circuits and small digital systems It equips students with a firm understanding of logic principles before they study the intricacies of logic organization and architecture of computers in the second part Besides discussing data representation arithmetic operations Boolean algebra and its application in designing combinatorial and sequential switching circuits the book introduces the Algorithmic State Machines which are used to develop a hardware description language for the design of digital systems The organization of a small hypothetical computer is described to illustrate how instruction sets are evolved Real computers namely Pentium and MIPS machines are described and compared with the hypothetical computer After discussing the features of a CPU I O devices and I O organization cache and virtual memory the book concludes with a new chapter on the use of parallelism to enhance the speed of computers Besides the fifth edition has new material in CMOS gates MSI ALU and Pentium5 architecture The chapter on Cache and Virtual Memory has been rewritten *Computer Organization, Design, and Architecture, Fifth Edition* Sajjan G. Shiva,2013-12-20 Suitable for a one or two semester undergraduate or beginning graduate course in computer science and computer engineering Computer Organization Design and Architecture Fifth Edition presents the operating principles capabilities and limitations of digital computers to enable development of complex yet efficient systems With 50 percent updated material 11 new sections and four revised sections this edition takes students through a solid up to date exploration of single and multiple processor systems embedded architectures and performance evaluation

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Table of Contents Computer Organization 5th Edition

1. Understanding the eBook Computer Organization 5th Edition
 - The Rise of Digital Reading Computer Organization 5th Edition
 - Advantages of eBooks Over Traditional Books
2. Identifying Computer Organization 5th Edition
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Computer Organization 5th Edition
 - User-Friendly Interface
4. Exploring eBook Recommendations from Computer Organization 5th Edition
 - Personalized Recommendations
 - Computer Organization 5th Edition User Reviews and Ratings

- Computer Organization 5th Edition and Bestseller Lists
- 5. Accessing Computer Organization 5th Edition Free and Paid eBooks
 - Computer Organization 5th Edition Public Domain eBooks
 - Computer Organization 5th Edition eBook Subscription Services
 - Computer Organization 5th Edition Budget-Friendly Options
- 6. Navigating Computer Organization 5th Edition eBook Formats
 - ePub, PDF, MOBI, and More
 - Computer Organization 5th Edition Compatibility with Devices
 - Computer Organization 5th Edition Enhanced eBook Features
- 7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Computer Organization 5th Edition
 - Highlighting and Note-Taking Computer Organization 5th Edition
 - Interactive Elements Computer Organization 5th Edition
- 8. Staying Engaged with Computer Organization 5th Edition
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Computer Organization 5th Edition
- 9. Balancing eBooks and Physical Books Computer Organization 5th Edition
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Computer Organization 5th Edition
- 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
- 11. Cultivating a Reading Routine Computer Organization 5th Edition
 - Setting Reading Goals Computer Organization 5th Edition
 - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Computer Organization 5th Edition
 - Fact-Checking eBook Content of Computer Organization 5th Edition
 - Distinguishing Credible Sources

13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
14. Embracing eBook Trends
 - Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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