AVR121: Enhancing ADC resolution by oversampling

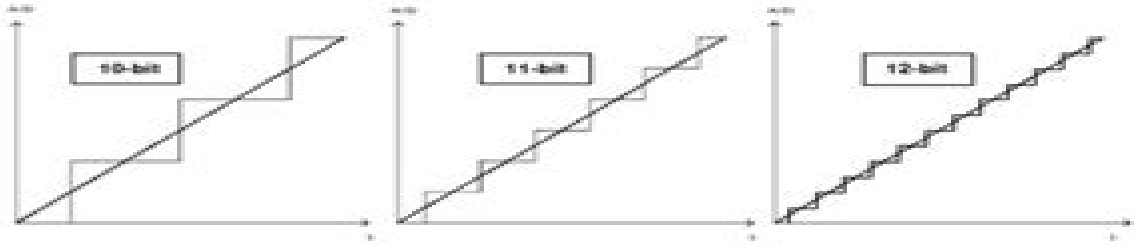
Features

- Increasing the resolution by oversampling
- Averaging and decimation
- Noise reduction by averaging samples

1 Introduction

Atmel's AVR controller offers an Analog to Digital Converter with 10-bit resolution. In most cases 10-bit resolution is sufficient, but in some cases higher accuracy is desired. Special signal processing techniques can be used to improve the resolution of the measurement. By using a method called 'Oversampling and Decimation' higher resolution might be achieved, without using an external ADC. This Application Note explains the method, and which conditions need to be fulfilled to make this method work properly.

Figure 1-1. Enhancing the resolution.



8-bit AVR Microcontrollers

Application Note

Rev. 8003A-AVR-09/05

Avr121 Enhancing Adc Resolution By Oversampling

Charles Borges De Lima E Marco V. M. Villaça

Avr121 Enhancing Adc Resolution By Oversampling:

Advanced Data Acquisition and Intelligent Data Processing Vladimir Haasz,Kurosh Madani,2022-09-01 DAQ and data processing is a basic part of all automated production systems diagnostic systems watching over quality of production energy distribution transport control or in various other areas Demands on the speed accuracy and reliability increase in general It is possible to achieve not only using superior but also more expensive hardware but also applying advanced data acquisition and intelligent data processing It deals e g optimal data fusion of a number of sensors new stochastic methods for accuracy increasing new algorithms for acceleration of data processing etc These are the grounds for publishing this book Advanced Data Acquisition and Intelligent Data Processing offers 10 up to date examples of different applications of advanced data acquisition and intelligent data processing used in monitoring measuring and diagnostics systems The book arose based on the most interesting papers from this area published at IDAACS 2013 conference However the indivudual chapters include not only designed solution in wider context but also relevant theoretical parts achieved results and possible future ways Technical topics discussed in this book include advanced methods of data acquisition in application that are not routine measured data fusion using up to date advanced data processing nonlinear dynamical systems identification multidimensional image processing Advanced Data Acquisition and Intelligent Data Processing is ideal for personnel of firms deals with advanced instrumentation energy consumption monitoring environment monitoring non descructive diagnostics robotics etc as well as academic staff and postgraduate students in electrical control and computer engineering Content 1 Introduction 2 Waveform acquisition with resolutions exceeding those of the ADC employed 3 Different Disaggregation Algorithms in Non Intrusive Home Energy Monitoring Systems 4 Design and testing of an electronic nose system sensitive to the aroma of truffles 5 DAQ System for Ultrasonic Transducer Evaluation under Spread Spectrum Excitation 6 Optimal Data Fusion in Decentralized Stochastic Unknown Input Observers 7 Odor Classification by Neural Networks 8 ANFIS Based Approach for Improved Multisensors Signal Processing 9 Neuro Fuzzy Sensor s Linearization Based FPGA 10 Interpolation Method of Nonlinear Dynamical Systems Identification Based on Volterra Model in Frequency Domain 11 Training Cellular Automata for Hyperspectral Image Segmentation **Handbook of Modern Sensors** Jacob Fraden,2015-10-15 This book presents a comprehensive and up to date account of the theory physical principles design and practical implementations of various sensors for scientific industrial and consumer applications This latest edition focuses on the sensing technologies driven by the expanding use of sensors in mobile devices These new miniature sensors will be described with an emphasis on smart sensors which have embedded processing systems The chapter on chemical sensors has also been expanded to present the latest developments Digital systems however complex and intelligent they may be must receive information from the outside world that is generally analog and not electrical Sensors are interface devices between various physical values and the electronic circuits that understand only a language of moving electrical charges In other words sensors are the eyes ears

and noses of silicon chips Unlike other books on sensors the Handbook of Modern Sensors is organized according to the measured variables temperature pressure position etc This book is a reference text for students researchers interested in modern instrumentation applied physicists and engineers sensor designers application engineers and technicians whose job it is to understand select and or design sensors for practical systems **AVR Programming** Elliot Williams,2014-01-27 Atmel s AVR microcontrollers are the chips that power Arduino and are the go to chip for many hobbyist and hardware hacking projects In this book you ll set aside the layers of abstraction provided by the Arduino environment and learn how to program AVR microcontrollers directly In doing so you ll get closer to the chip and you ll be able to squeeze more power and features out of it Each chapter of this book is centered around projects that incorporate that particular microcontroller topic Each project includes schematics code and illustrations of a working project Program a range of AVR chips Extend and re use other people s code and circuits Interface with USB I2C and SPI peripheral devices Learn to access the full range of power and speed of the microcontroller Build projects including Cylon Eyes a Square Wave Organ an AM Radio a Passive Light Sensor Alarm Temperature Logger and more Understand what s happening behind the scenes even when using the Arduino IDE Emerging Communication Technologies for E-Health and Medicine Rodrigues, Joel J.P.C.,2012-04-30 According to the World Health Organisation WHO e health is the combined use of electronic communication and information technology in the health sector and moreover it enables a safer higher quality more equitable and sustainable health system Emerging Communication Technologies for E Health and Medicine is a fundamental source for the advancement of knowledge application and practice in the interdisciplinary areas of healthcare e health m health u health sensors biomedical engineering and telemedicine Due to its grounding in research and theory evidence this book is designed for use in graduate courses in health management medicine nursing health professionals and medical informatics The book can help to e health contents applications and interesting experiences It is an important way to communicate e health concepts **Avr E Arduino: Técnicas De Projeto** Charles Borges De Lima E Marco V. M. Villaça,2012-05-30 Devido ao seu desempenho os microcontroladores AVR t m assumido um papel de destaque entre os microcontroladores de 8 bits Sua arquitetura moderna al m de permitir execu es mais r pidas dos programas permite uma maior densidade de c digo comparado s outras tecnologias de 8 bits A plataforma Arduino tornou populares os microcontroladores AVR possibilitando que esses alcancem um n mero cada vez maior de pessoas A plataforma Arduino associa principalmente a facilidade de programa o com a disponibilidade de in meros perif ricos na forma de m dulos ferramentas de programa o gratuitas e amplo suporte t cnico Ao abordar a tecnologia AVR voltada ao emprego da plataforma Arduino com o ATmega328 esta obra inclui in meras t cnicas para o projeto de sistemas microcontrolados raramente encontradas em um nico livro tais como o uso de displays LCD alfanum ricos e gr ficos gera o de m sicas curtas leitura de teclado matriz e cubo de LEDs t cnicas de multiplexa o gera o de formas de onda comunica o serial com um computador comunica o sem fio cart es de mem ria sonar leitura de sensores

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High-performance Oversampling A/D Converter Design Techniques in Scaled CMOS Technologies Shaolan Li,2018 Oversampling analog to digital converters ADCs are specialists in digitizing real word signals in high resolution They have been crucial building blocks in many modern electronic systems and are expected to have a growing significance in the booming era of Internet of Things IoT However many classic oversample ADC techniques are power hungry and scaling unfriendly making them deficient for many emerging applications To address this challenge this thesis explores solution from two directions voltage controlled oscillator VCO based ADC and hybrid ADC The first work presents a VCO based ADC featuring a novel phase extended quantizer PEQ Compared to prior VCO based ADCs the proposed phase extension technique enabled this work to achieve 1 bit higher resolution with very little power and circuit overhead On top of resolution boosting A tri level resistor digital to analog converter DAC is also introduced as complementary to the new quantizer enabling high DR while creating a dynamic power saving mechanism for the proposed design Fabricated in 130 nm CMOS the prototype ADC achieved peak Schreier Figure of Merits FoM of 174 3 dB marking 2 5x improvement over prior arts The second project extends the achievement of the first and develops techniques to push the performance frontier of high order VCO based ADC Existing high order VCO based ADCs still rely on OTA based active loop filter which deemphasizes the merits of the VCO based quantizer To address their limitation a hybrid passive VCO architecture is proposed where we made the front end loop filter fully passive and zero power and used the VCO to support the entire loop gain This technique innovatively utilizes the parasitic effect of the VCO hence also obviates the need for parasitic cancellation A prototype fabricated in 40 nm CMOS measured 19 fJ conversion Walden FoM achieving 5x improvement over existing high order VCO based ADC Finally this thesis also presents the design of a noise shaping SAR NS SAR ADC Unlike prior works that all rely on the cascaded integrator feed forward CIFF structure this work distinctively employs the error feedback EF structure It is the first in its kind to achieves second order NTF with complex zero optimization using all low

power scaling friendly building blocks such as passive finite impulse response FIR filter and dynamic amplifier D Amp A background calibration scheme is also developed to improve robustness over process voltage temperature PVT variation The prototype chip fabricated in 40nm CMOS measured the state of the art performance of 178 dB Schreier FoM This works demonstrated a NS SAR solution that combines optimized NTF power efficiency and PVT robustness **Oversampling A/D Converters with Improved Signal Transfer Functions** Bupesh Pandita,2011-09-15 This book describes techniques for designing complex discrete time ADCs with signal transfer functions that significantly filter interfering signals The book provides an understanding of theory issues and implementation of discrete complex ADCs The concepts developed in each chapter are further explained by applying them to a target application of ADCs in DTV receivers **Faster Scaling for Oversampling ADC's** Bashar H. Atiyat,2003 *Binary Dithered Oversampling Analog-to-digital Converters* Diego Pienovi,2009 In today s world analog to digital converters ADC play a major role Our modern society requires and depends on electronic devices that process the analog input data in the digital domain such as cellphones audio and video systems and even domestic appliances Therefore the development of fast and accurate ADCs have become a key issue for the industry In general a good ADC is the one that achieves high resolution low quantization noise with low complexity One of the most popular techniques to decrease the quantization noise is to digitalize the incoming signal with a sampling frequency many times higher than twice the signal bandwith This technique is called oversampling Probably the most popular oversampling converters are the Sigma Delta SD modulators which use a very high sampling frequency and a binary quantizer for first order SD In SD the quantizer is embedded into a feedback loop in such a way that the quantization noise is not only spread over the spectrum because of oversampling but it is also shaped to the upper frequencies this is called noise shaping The big problem with SD is that the quantization noise spectrum presents undesired harmonics caused by the nonlinear nature of the quantizer To avoid this the solution is to add an independent signal before the quantization stage called dither It was proven by Lipshitz and Vanderkooy in 1 that dithering a first order SD modulator is ineffective as it turns the modulator in constant overload In addition to this problem because of the feedback loop higher order SD modulators can be unstable That being said the aim of this work is to present a simple oversampling ADC without feedback capable of generating the minimum uncorrelated quantization noise that yields the maximumpossible SNR at the output For that purpose this work develops the statistical characteristics of the optimum dither that achieves the mentioned goal for different types of input signals

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