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SEVENTH EDITION

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A Quantitative Approach

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Parallel Programming Thomas Rauber, Gudula Runger, 2010-03-16 Innovations in hardware architecture like hyper threading or multicore processors mean that parallel computing resources are available for inexpensive desktop computers In only a few years many standard software products will be based on concepts of parallel programming implemented on such hardware and the range of applications will be much broader than that of scientific computing up to now the main application area for parallel computing Rauber and Runger take up these recent developments in processor architecture by giving detailed descriptions of parallel programming techniques that are necessary for developing efficient programs for multicore processors as well as for parallel cluster systems and supercomputers Their book is structured in three main parts covering all areas of parallel computing the architecture of parallel systems parallel programming models and environments and the implementation of efficient application algorithms The emphasis lies on parallel programming techniques needed for different architectures The main goal of the book is to present parallel programming techniques that can be used in many situations for many application areas and which enable the reader to develop correct and efficient parallel programs Many examples and exercises are provided to show how to apply the techniques The book can be used as both a textbook for students and a reference book for professionals The presented material has been used for courses in parallel programming at different universities for many years

Proceedings, 2003 VLSI-SoC: Research Trends in VLSI and Systems on Chip Giovanni De Micheli, Salvador Mir, Ricardo Reis, 2010-08-23 This book contains extended and revised versions of the best papers presented during the

fourteenth IFIP TC 10 WG 10 5 International Conference on Very Large Scale Integration This conference provides a forum to exchange ideas and show industrial and academic research results in microelectronics design The current trend toward increasing chip integration and technology process advancements brings about stimulating new challenges both at the physical and system design levels **VLSI Circuits and Systems** ,2005 Annual ACM Symposium on Parallel Algorithms and Architectures ,2003 Journal of Information Science and Engineering ,2006 **FCCM 2004** Jeffrey Arnold,Kenneth L. Pocek,2004 FCCM presents recent work on the use of reconfigurable logic as computing elements The proceedings focuses on topics such as device architecture system architecture compilation and programming tools run time environments nano technology and applications **Electrical Computer Engineering** University of Wisconsin--Madison. Department of Electrical and Computer Engineering,2003 **The Anatomy of a High-Performance Microprocessor** Bruce Shriver,Bennett Smith,1998-06-18 This work describes in detail the microarchitecture of a high performance microprocessor giving an integrated treatment of platform and systems issues relating to the design and implementation of microprocessor based systems This book is a reference for individuals building systems using microprocessors and readers looking for significant insights into fundamental design guidelines that transcend the design implementation and use of a specific microprocessor Practitioners academics and technical and product managers alike will benefit from this detailed overview of microprocessors platforms and systems for years in the future Combining Selective Cache Line Replacement and Active Management for Data Caching Yan Ting Betty Chen,2005 *The British National Bibliography* Arthur James Wells,2005 *Journal of Computer Resource Management* ,2006 *High Performance Computing* ,1999 Storage and File Systems for MEMS-based Storage Bo Hong,2005 Report ,2000

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