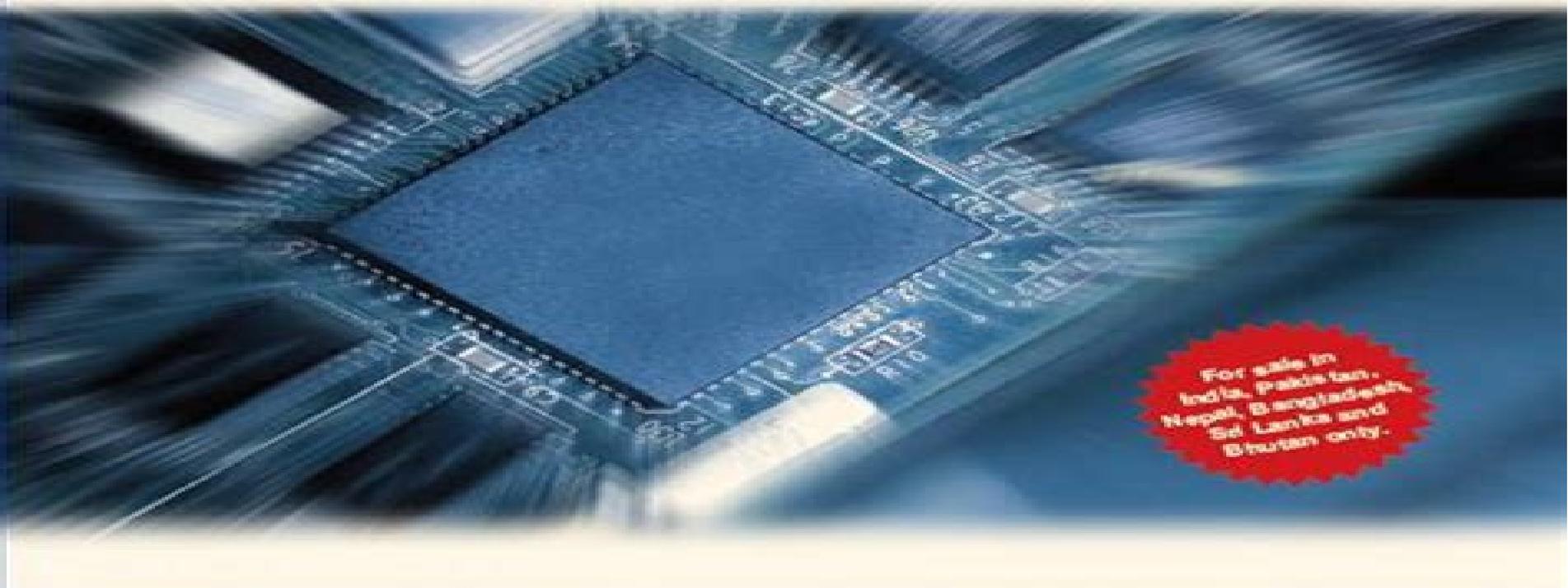


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performance of digital integrated circuits has revolutionized the way we live and work. The continued scaling down of MOS transistors has broadened the scope of use for circuit technology to the point that texts on the topic are generally lacking after a few years. The second edition of *Digital Integrated Circuits Analysis and Design* focuses on timeless principles with a modern interdisciplinary view that will serve integrated circuits engineers from all disciplines for years to come. Providing a revised instructional reference for engineers involved with Very Large Scale Integrated Circuit design and fabrication, this book delves into the dramatic advances in the field, including new applications and changes in the physics of operation made possible by relentless miniaturization. This book was conceived in the versatile spirit of the field to bridge a void that had existed between books on transistor electronics and those covering VLSI design and fabrication as a separate topic. Like the first edition, this volume is a crucial link for integrated circuit engineers and those studying the field, supplying the cross-disciplinary connections they require for guidance in more advanced work. For pedagogical reasons, the author uses SPICE level 1 computer simulation models but introduces BSIM models that are indispensable for VLSI design. This enables users to develop a strong and intuitive sense of device and circuit design by drawing direct connections between the hand analysis and the SPICE models. With four new chapters, more than 200 new illustrations, numerous worked examples, case studies, and support provided on a dynamic website, this text significantly expands concepts presented in the first edition.

CMOS Digital Integrated Circuits Sung-Mo Kang, Yusuf Leblebici, 1996. This text is the most comprehensive book on the market for CMOS circuits. Aimed at junior senior courses offered in electrical engineering and computer science, this book starts with CMOS processing and then covers MOS transistor models, basic CMOS gates, dynamic circuits, memory circuits, BiCMOS circuits, I/O circuits, VLSI design methodologies, design for manufacturability, and design for testability. This text provides rigorous treatment of basic design concepts with detailed examples. It addresses both design concepts and computer-aided analysis for most of the circuit examples. SPICE simulation results are provided for illustration.

Analysis and Design of Digital Integrated Circuits David A. Hodges, Horace G. Jackson, Resve A. Saleh, 2003. The third edition of Hodges and Jackson's *Analysis and Design of Digital Integrated Circuits* has been thoroughly revised and updated by a new co-author, Resve Saleh of the University of British Columbia. The new edition combines the approachability and concise nature of the Hodges and Jackson classic with a complete overhaul to bring the book into the 21st century. The new edition has replaced the emphasis on BiPolar with an emphasis on CMOS. The outdated MOS transistor model used throughout the book will be replaced with the now standard deep submicron model. The material on memory has been expanded and updated. As well, the book now includes more on SPICE simulation and new problems that reflect recent technologies. The emphasis of the book is on design, but it does not neglect analysis and has as a goal to provide enough information so that a student can carry out analysis as well as be able to design a circuit. This book provides an excellent and balanced introduction to digital circuit design for both students and professionals.

Digital Integrated Circuits Jan M. Rabaey, 2003. Contents: p pChapter 1 Introduction p pChapter

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knowledges to benefit enterprisers being interested in integrated circuit industry

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The Electrical Engineering Handbook - Six Volume Set Richard C. Dorf, 2018-12-14 In two editions spanning more than a decade The Electrical Engineering Handbook stands as the definitive reference to the multidisciplinary field of electrical engineering Our knowledge continues to grow and so does the Handbook For the third edition it has grown into a set of six books carefully focused on specialized areas or fields of study Each one represents a concise yet definitive collection of key concepts models

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Analysis and Design of Digital Integrated Circuits David A. Hodges, Horace G. Jackson, 1988 This is a state of the art treatment of the circuit design of digital integrated circuits It includes coverage of the basic concepts of static characteristics voltage transfer characteristics noise margins fanout power dissipation and dynamic characteristics propagation delay times and the interrelationships among these parameters The authors are regarded as leading authorities in integrated circuits and MOS technology

Compact Models and Performance Investigations for Subthreshold Interconnects Rohit Dhiman, Rajeevan Chandel, 2014-11-07 The book provides a detailed analysis of issues related to sub threshold interconnect performance from the perspective of analytical approach and design techniques Particular emphasis is laid on the performance analysis of coupling noise and variability issues in sub threshold domain to develop efficient compact models The proposed analytical approach gives physical insight of the parameters affecting the transient behavior of coupled interconnects Remedial design techniques are also suggested to mitigate the effect of coupling noise The effects of wire width spacing between the wires wire length are thoroughly investigated In addition the effect of parameters like driver strength on peak coupling noise has also been analyzed Process voltage and temperature variations are prominent factors affecting sub threshold design and have also been investigated The process variability analysis has been carried out using parametric analysis process corner analysis and Monte Carlo technique The book also provides a qualitative summary of the work reported in the literature by various researchers in the design of digital sub threshold circuits This book should be of interest for researchers and graduate students with deeper insights into sub threshold interconnect models in particular In this sense this book will best fit as a text book and or a reference book for students who are initiated in the area of research and advanced courses in nanotechnology interconnect design and modeling

International Conference on Intelligent Computing and Smart Communication 2019 Geetam Singh Tomar, Narendra S. Chaudhari, Jorge Luis V. Barbosa, Mahesh Kumar Aghwariya, 2020-01-07 This book gathers high quality research papers presented at the First International Conference ICSC 2019 organised by THDC Institute of Hydropower Engineering and Technology Tehri India from 20 to 21 April 2019 The book is divided into two major sections Intelligent Computing and Smart Communication Some of the areas covered are Parallel and Distributed Systems Web Services Databases and Data Mining

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