

Gpu Accelerator And Co Processor Capabilities Ansys

Yi Zou



Gpu Accelerator And Co Processor Capabilities Ansys:

Implementation and Performance Analysis of Many-body Quantum Chemical Methods on the Intel Xeon Phi Coprocessor and NVIDIA GPU Accelerator Bobo Shi, 2016 The tensor contraction are performed using BLAS DGEMM on coprocessor accelerator Then the result is post processed using a 6 dimensional loop For Intel Xeon Phi implementation OpenMP is used to bind threads to physical processing units on Xeon Phi coprocessors The OpenMP threads affinity are tuned for Intel Xeon Phi Coprocessor to obtain best performance For GPU a algorithm is designed to map the 6 dimensional loop post processing to CUDA threads gridDim and blockDim are tuned to reach best performance 4x and 9x 13x overall speedup is obtained for Intel Xeon Phi and GPU implementation respectively

Modern Accelerator Technologies for Geographic Information Science Xuan Shi, Volodymyr Kindratenko, Chaowei Yang, 2013-10-27 This book explores the impact of augmenting novel architectural designs with hardware based application accelerators The text covers comprehensive aspects of the applications in Geographic Information Science remote sensing and deploying Modern Accelerator Technologies MAT for geospatial simulations and spatiotemporal analytics MAT in GIS applications MAT in remotely sensed data processing and analysis heterogeneous processors many core and highly multi threaded processors and general purpose processors are also presented This book includes case studies and closes with a chapter on future trends Modern Accelerator Technologies for GIS is a reference book for practitioners and researchers working in geographical information systems and related fields Advanced level students in geography computational science computer science and engineering will also find this book useful

Coprocessor Acceleration for Domain-specific Computing Yi Zou, 2012 There is a growing trend to use coprocessors to offload and accelerate domain specific applications in order to obtain significant performance improvement and energy power reductions Two important coprocessor components in the heterogeneous system are the GPU and FPGA GPU graphics processing unit is increasingly used as a data parallel coprocessor for general computations The newest GPU has a much larger number of cores compared to CPU and very high peak FLOPS FPGA field programmable gate array on the other hand allows users to customize at fine grain level the computational data path and memory hierarchy according to the exact need of the applications FPGA excels in integer operations and bit level operations The thesis starts with several coprocessor acceleration examples for our focus application domains the first domain is on VLSICAD algorithms and the second is on computational medical imaging We detail application acceleration examples in the domains including lithography simulation for IC manufacturing medical image reconstruction using compressive sensing and medical image registration using fluid models Both GPU accelerated versions and FPGA accelerated versions have been implemented Based on these implementations we then analyze the performance and energy trade offs the interaction between the diverse application requirements and a spectrum of hardware systems and how those domain specific coprocessor acceleration case studies further bring us insights for domain specific architecture innovations In the end we showcase an example for collaborative

execution on the heterogeneous platform Different scheduling policies are needed to optimize performance or energy The thesis concludes as we present reusable architecture templates and realizations for futuristic accelerator rich CMPs

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