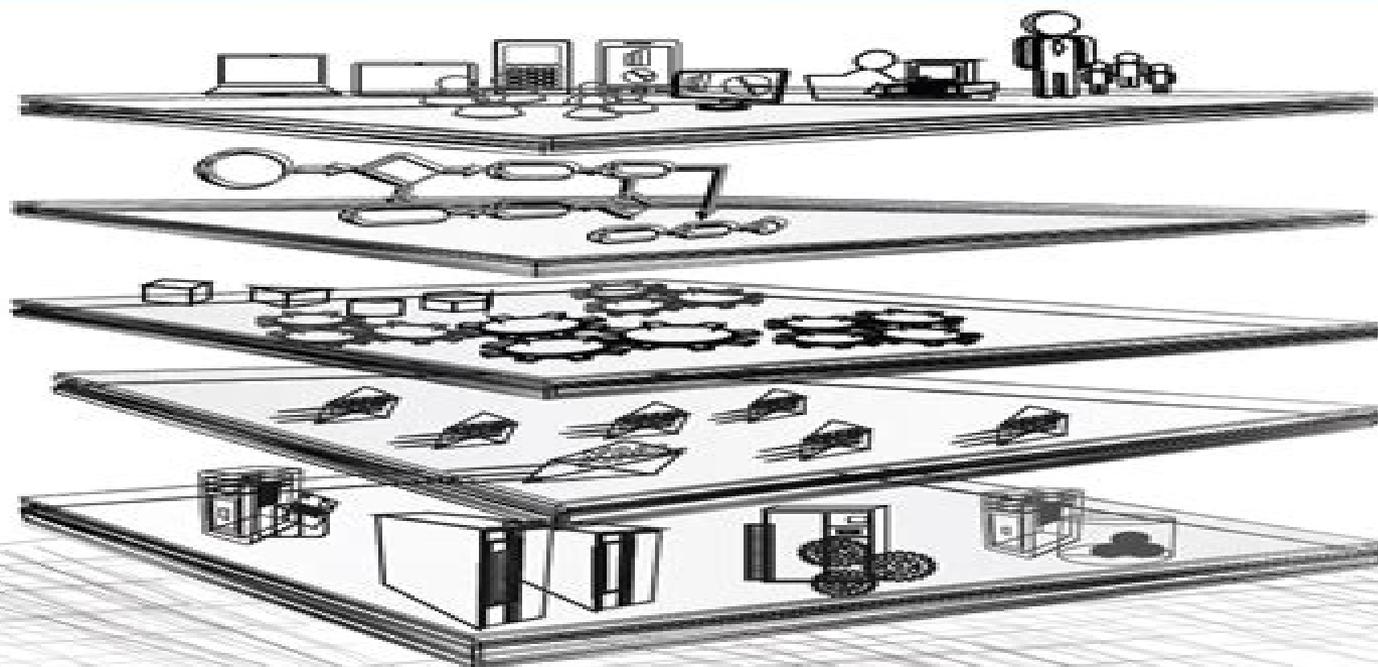


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*Computer System Architecture* Mr. A. Victor Benevent Raj, Lt. Dr. S. Kevin Andrews, 2023-01-30 Computer System Architecture is a comprehensive guide to the internal organisation and operation of computer systems offering an in depth treatment of core architectural concepts from data representation and basic computer organisation to instruction execution computer arithmetic memory organisation and multiprocessor characteristics Structured into five units the book progresses systematically through 1 data representation and register transfer concepts 2 memory reference instructions input output interrupts and programming the basic computer 3 CPU organisation instruction formats and addressing modes including RISC 4 computer arithmetic and I O interface mechanisms such as DMA and priority interrupts and 5 memory hierarchy topics such as cache and virtual memory concluding with an overview of multiprocessor characteristics It is intended for students and professionals in Computer Science and Engineering and it is designed to serve as both a learning text and a reference for understanding how computers work internally Book details for Google Books fields if needed Title Computer System Architecture Authors Mr A Victor Benevent Raj Lt Dr S Kevin Andrews First published January 2023 Publisher

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