



GPU Accelerator and co-processor Capabilities * Release 20.2

* Based on support of the GPU to process certain calculations and flow solver computations for better performance during a solution. Acceleration can be used for both selected requests parallel processing (before solving) and distributed memory parallel processing (distributed memory). Acceleration is available for both Windows and Linux.

Support by Applications

ANSYS Mechanical APDL supports all NVIDIA Tesla series cards and the following Quadro series cards: 4000Q, 5000Q, 5100Q, 6000Q, 6100Q, 7000Q, 7100Q, 8000Q, 8100Q, 9000Q, 9100Q. **ANSYS Mechanical APDL** supports the following Intel Xeon Phi coprocessor cards: 6110, 6120 and 7120. When using the coprocessor an equivalent based on the system solver with NVIDIA cards additional considerations apply (please consult the ANSYS installation guide for details).

ANSYS Fluent supports NVIDIA GeForce GTX series and Quadro series workstation and server cards.

ANSYS Fluent supports all NVIDIA Tesla series cards and the following Quadro series cards: 4000Q, 5000Q, 6000Q, 6100Q, 7000Q, 7100Q, 8000Q, 8100Q, 9000Q, 9100Q.

ANSYS Fluent supports NVIDIA Tesla P series, Tesla E series, Quadro P series, GeForce GTX series and GeForce GTX series.

ANSYS Fluent supports NVIDIA Tesla P series, T40 series, Tesla P series, Quadro P series (P5000 and above).

ANSYS Fluent supports NVIDIA GeForce GTX series and Quadro series workstation and server cards.

ANSYS Fluent supports NVIDIA Tesla P series, T40 series, Tesla P series, Quadro P series (P5000 and above).

ANSYS Fluent supports NVIDIA Tesla P series, Tesla E series, Quadro P series, GeForce GTX series and GeForce GTX series.

Application	Manufacturer	Product Series	Card / GPU	Tested Platform	Tested Operating System Version
ANSYS Mechanical APDL	NVIDIA	Quadro	4000Q	Linux 64	Windows Server 2012
			5000Q	Linux 64	Red Hat 6.8
			6000Q	Linux 64	Windows 7
	Tesla	6100	Linux 64	Red Hat 6.8	
		7100	Linux 64	Windows 7	
		8100	Linux 64	Red Hat 6.8	
ANSYS Fluent	NVIDIA	Quadro	4000Q	Linux 64	Red Hat 7.0
			5000Q	Windows 64	Windows 7
			6000Q	Windows 64	Windows 7
			7000Q	Windows 64	Windows 10
			8000Q	Linux 64	Red Hat 6.8
	Tesla	6100	Linux 64	Red Hat 7.0	
		7100	Windows 64	Windows 7	
		8100	Linux 64	Red Hat 6.8	
		9100	Linux 64	Red Hat 6.8	
		P5000	Linux 64	Red Hat 6.8	

Application	Manufacturer	Product Series	Card / GPU	Tested Platform	Tested Operating System Version
ANSYS Fluent	NVIDIA	Quadro	4000	Linux 64	Red Hat 7.0
			5000	Linux 64	Red Hat 6.8
			6000	Linux 64	Windows 7.0
			7000	Linux 64	Windows 7.0
			8000 (Quadro)	Windows 64	Windows 10
			9000	Linux 64	Red Hat 7.0
ANSYS Fluent	NVIDIA	Quadro	4000	Windows 64	Windows 10
			5000	Linux 64	Red Hat 6.8
			6000	Linux 64	Red Hat 6.8
	Tesla	6100	Windows 64	Windows 7	
		7100	Windows 64	Windows 7	
		P5000	Windows Server 2012 R2	Windows 10	
ANSYS APDL	NVIDIA	Quadro	4000	Windows 64	Windows 6.0
			5000	Windows 64	Windows 7
			6000	Windows 64	Windows 7
			7000	Windows 64	Windows 7
			8000	Linux 64	Red Hat 6.7
			9000	Windows 64	Windows 7
	Tesla	6100	Windows 64	Windows 6.0	
		7100	Linux 64	Red Hat 6.7	
		8100	Windows 64	Windows 6.0	
		9100	Linux 64	Red Hat 6.7	
		P5000	Windows 64	Windows 6.0	
		P6000	Windows 64	Windows 7	
ANSYS Fluent	NVIDIA	Quadro	4000	Windows 64	Windows 10
			5000	Windows 64	Windows 10
			6000	Windows 64	Windows 10
	Tesla	6100	Windows 64	Windows 6.0	
		7100	Linux 64	Red Hat 7.0	
		P5000	Linux 64	Red Hat 6.8	

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Bobo Shi



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Implementation and Performance Analysis of Many-body Quantum Chemical Methods on the Intel Xeon Phi Coprocessor and NVIDIA GPU Accelerator Bobo Shi, 2016 The tensor contraction are performed using BLAS DGEMM on coprocessor accelerator Then the result is post processed using a 6 dimensional loop For Intel Xeon Phi implementation OpenMP is used to bind threads to physical processing units on Xeon Phi coprocessors The OpenMP threads affinity are tuned for Intel Xeon Phi Coprocessor to obtain best performance For GPU a algorithm is designed to map the 6 dimensional loop post processing to CUDA threads gridDim and blockDim are tuned to reach best performance 4x and 9x 13x overall speedup is obtained for Intel Xeon Phi and GPU implementation respectively *Modern Accelerator Technologies for Geographic Information Science* Xuan Shi, Volodymyr Kindratenko, Chaowei Yang, 2013-10-27 This book explores the impact of augmenting novel architectural designs with hardware based application accelerators The text covers comprehensive aspects of the applications in Geographic Information Science remote sensing and deploying Modern Accelerator Technologies MAT for geospatial simulations and spatiotemporal analytics MAT in GIS applications MAT in remotely sensed data processing and analysis heterogeneous processors many core and highly multi threaded processors and general purpose processors are also presented This book includes case studies and closes with a chapter on future trends Modern Accelerator Technologies for GIS is a reference book for practitioners and researchers working in geographical information systems and related fields Advanced level students in geography computational science computer science and engineering will also find this book useful Coprocessor Acceleration for Domain-specific Computing Yi Zou, 2012 There is a growing trend to use coprocessors to offload and accelerate domain specific applications in order to obtain significant performance improvement and energy power reductions Two important coprocessor components in the heterogeneous system are the GPU and FPGA GPU graphics processing unit is increasingly used as a data parallel coprocessor for general computations The newest GPU has a much larger number of cores compared to CPU and very high peak FLOPS FPGA field programmable gate array on the other hand allows users to customize at fine grain level the computational data path and memory hierarchy according to the exact need of the applications FPGA excels in integer operations and bit level operations The thesis starts with several coprocessor acceleration examples for our focus application domains the first domain is on VLSICAD algorithms and the second is on computational medical imaging We detail application acceleration examples in the domains including lithography simulation for IC manufacturing medical image reconstruction using compressive sensing and medical image registration using fluid models Both GPU accelerated versions and FPGA accelerated versions have been implemented Based on these implementations we then analyze the performance and energy trade offs the interaction between the diverse application requirements and a spectrum of hardware systems and how those domain specific coprocessor acceleration case studies further bring us insights for domain specific architecture innovations In the end we showcase an example for

collaborative execution on the heterogeneous platform Different scheduling policies are needed to optimize performance or energy The thesis concludes as we present reusable architecture templates and realizations for futuristic accelerator rich CMPs

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