

# NPCT42x Trusted Platform Module (TPM)

## General Description

The NPCT42x single-chip Trusted Platform Module (TPM) is a family of high-performance, NuvoTon ChipKeeper™ technology devices. The devices implement the Trusted Computing Group (TCG) version 1.2 specifications for PC-Client TPM.

The NPCT42x devices are designed as feature-rich system boot time and trusted OS loading devices. They provide a solution for PC security for a wide range of PC applications.

The NPCT42x family of devices are Microsoft® Windows™ compliant and are supported by Linux kernel v2.6.18 and higher.

## Features

### General

- Single-chip TPM solution
- TPM 1.2-compliant as required
- Supports TCG version 1.2 TPM interface
- Host Interface
  - TPM 1.2 standard interface
  - Supports legacy flexibility by using TIS protocol I/O-mapped registers
- Secure General Purpose I/O (GPIO)
  - 4x8-bit GPIO pins
  - I/O pins individually configured as input or output
  - Configurable internal pull-up resistor
  - TCG 1.2-defined interface
  - Dedicated Physical Presence (PP) pin with configurable pull-up or pull-down resistor
- Clock Generator

### Bus Interface

- LPC Bus Interface
  - Based on Intel's LPC Interface Specification, Rev. 4.0 (v4.0), August 2000
  - TPM 1.2 interface (TIS)

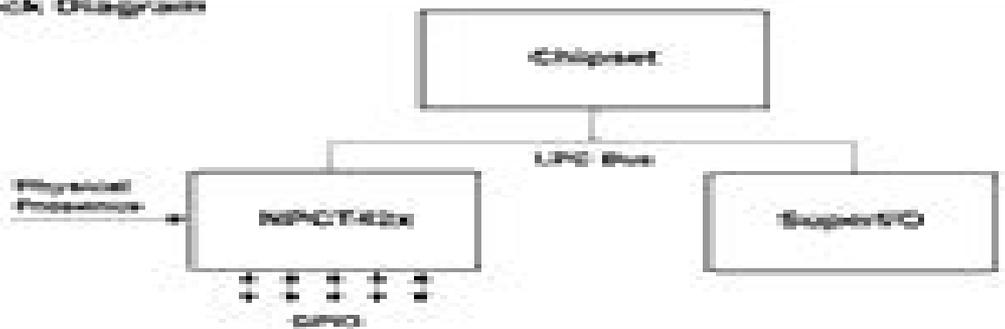
### Clocking and Supply

- On-Chip Clock Generator
- Power Supply
  - 3.3V supply operation
  - Separate pins for main (VDD) and standby (VDD) power supplies
  - Low standby power consumption

### Software

- TPM BIOS, drivers, Memory Access (MA) and Memory Protect (MP)
- TPM Device Driver for Microsoft Windows
- TPM Cryptosystems (supported by Security Intel®)
  - Intel® Key Storage (KST)
  - Intel® Key Management (KMG)
  - Intel® Key Protection (KPP)
  - Intel® Key Management (KMP)
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## System Block Diagram



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# Nuvoton Datasheet

**Travis Goodspeed**



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