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**Dan Heller, Paula M. Ferguson, David  
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*Programming Manual* Dan Heller, Paula M. Ferguson, David Brennan, 1994 Describes how to write applications using the Motif toolkit from the Open Software Foundation OSF going into detail on every Motif widget class with useful examples to help programmers develop their own code Tips on programming in general are also included The authors assume competence with C and familiarity with fundamental X Window System concepts Chapters are marked by uncut tabs for easy location The manual is updated for Motif 1.2 but still usable for Motif 1.1 Annotation copyright by Book News Inc Portland OR

*Buck's Step-by-Step Medical Coding, 2020 Edition E-Book* Elsevier, 2019-11-28 NEW Coding updates include the latest information available promoting accurate coding and success on the job

**The Business Of Book Publishing** Elizabeth Geiser, Arnold Dolin, Gladys Topkis, 2019-06-04 When the first University of Denver Publishing Institute came to a close in August 1976 all of us involved in its launching knew that we had a real success on our hands And we knew it was due in great measure to an outstanding faculty of more than forty top publishing executives who had come to Denver during those four weeks to teach our students How regrettable it seemed that their knowledge and expertise were available only to the eighty students handpicked for that first class Fred Praeger publisher of Westview Press suggested a solution Do a book he invited and let Westview publish the curriculum for others to share

**USBC (Universal Standard Book Code)** Frederick Henry Ayers, 1984

*SIMD Programming Manual for Linux and Windows* Paul Cockshott, Kenneth Renfrew, 2013-03-09 A number of widely used contemporary processors have instruction set extensions for improved performance in multi media applications The aim is to allow operations to proceed on multiple pixels each clock cycle Such instruction sets have been incorporated both in specialist DSP chips such as the Texas C62xx Texas Instruments 1998 and in general purpose CPU chips like the Intel IA32 Intel 2000 or the AMD K6 Advanced Micro Devices 1999 These instruction set extensions are typically based on the Single Instruction stream Multiple Data stream SIMD model in which a single instruction causes the same mathematical operation to be carried out on several operands or pairs of operands at the same time The level of parallelism supported ranges from two floating point operations at a time on the AMD K6 architecture to 16 byte operations at a time on the Intel P4 architecture Whereas processor architectures are moving towards greater levels of parallelism the most widely used programming languages such as C Java and Delphi are structured around a model of computation in which operations take place on a single value at a time This was appropriate when processors worked this way but has become an impediment to programmers seeking to make use of the performance offered by multi media instruction sets The introduction of SIMD instruction sets Peleg et al

**Coding Exam Success** Jacqueline Thelian, 2011-09-16 Learn the ins and outs of coding and how to successfully navigate the CPC and CCS P exams This comprehensive straightforward review takes the complicated process of coding and makes it easy to understand With a comprehensive review of CPT ICD 9 CM and HCPCS and helpful test taking strategies this is the best way to prepare for the coding certification exams It is also the perfect reference for professional coders looking to stay sharp

*3-2-1 Code It! (Book Only)* Michelle A.

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