

# 64-Bit z/OS Assembler Coding

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# 64 Bit Z Os Assembler Coding Tachyon Soft

**Daniel Kusswurm**



## 64 Bit Z Os Assembler Coding Tachyon Soft:

*x64 Assembly Language Step-by-Step* Jeff Duntemann, 2023-09-21 The long awaited x64 edition of the bestselling introduction to Intel assembly language In the newly revised fourth edition of *x64 Assembly Language Step by Step Programming with Linux* author Jeff Duntemann delivers an extensively rewritten introduction to assembly language with a strong focus on 64 bit long mode Linux assembler The book offers a lighthearted robust and accessible approach to a challenging technical discipline giving you a step by step path to learning assembly code that s engaging and easy to read *x64 Assembly Language Step by Step* makes quick work of programmable computing basics the concepts of binary and hexadecimal number systems the Intel x86 x64 computer architecture and the process of Linux software development to dive deep into the x64 instruction set memory addressing procedures macros and interface to the C language code libraries on which Linux is built You ll also find A set of free and open source development and debugging tools you can download and put to use immediately Numerous examples woven throughout the book to illustrate the practical implementation of the ideas discussed within Practical tips on software design coding testing and debugging A one stop resource for aspiring and practicing Intel assembly programmers the latest edition of this celebrated text provides readers with an authoritative tutorial approach to x64 technology that s ideal for self paced instruction Please note the author s listings that accompany this book are available from the author website at [www.contrapositive.com](http://www.contrapositive.com) under his heading My Assembly Language Books

**Assembler Language Programming** George Struble, 1969      **Fundamentals of Assembler Language Programming for the IBM PC and IBM XT** Victor E. Broquard, J. William Westley, 1990      **Modern X86 Assembly Language Programming** Daniel Kusswurm, 2023 This book is an instructional text that will teach you how to code x86 64 assembly language functions It also explains how you can exploit the SIMD capabilities of an x86 64 processor using x86 64 assembly language and the AVX AVX2 and AVX 512 instruction sets This updated edition s content and organization are designed to help you quickly understand x86 64 assembly language programming and the unique computational capabilities of x86 processors The source code is structured to accelerate learning and comprehension of essential x86 64 assembly language programming constructs and data structures *Modern X86 Assembly Language Programming Third Edition* includes source code for both Windows and Linux The source code elucidates current x86 64 assembly language programming practices run time calling conventions and the latest generation of software development tools You will Understand important details of the x86 64 processor platform including its core architecture data types registers memory addressing modes and the basic instruction set Use the x86 64 instruction set to create assembly language functions that are callable from C Create assembly language code for both Windows and Linux using modern software development tools including MASM Windows and NASM Linux Employ x86 64 assembly language to efficiently manipulate common data types and programming constructs including integers text strings arrays matrices and user defined structures Explore indispensable

elements of x86 SIMD architectures register sets and data types Master x86 SIMD arithmetic and data operations using both integer and floating point operands Harness the AVX AVX2 and AVX 512 instruction sets to accelerate the performance of computationally intense calculations in machine learning image processing signal processing computer graphics statistics and matrix arithmetic applications Apply leading edge coding strategies to optimally exploit the AVX AVX2 and AVX 512 instruction sets for maximum possible performance

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