

Digital Logic Design - Asynchronous Counter

③ 状态转换表



① 输入方程

$$J_1 = K_1 = 1 \quad CP_1 \downarrow$$

$$J_2 = K_2 = 1 \quad CP_2 = Y_1 \downarrow$$

$$J_3 = K_3 = 1 \quad CP_3 = Y_2 \downarrow$$

异步Moor
型时序电路

异步模8加
法计数器

| 现态 | | | 次态 | | | 时钟 | | |
|---------|---------|---------|-------------|-------------|-------------|--------|--------|--------|
| Y_3^n | Y_2^n | Y_1^n | Y_3^{n+1} | Y_2^{n+1} | Y_1^{n+1} | CP_3 | CP_2 | CP_1 |
| 0 | 0 | 0 | 0 | 0 | 1 | 无 | 无 | ↓ |
| 0 | 0 | 1 | 0 | 1 | 0 | 无 | ↓ | ↓ |
| 0 | 1 | 0 | 0 | 1 | 1 | 无 | 无 | ↓ |
| 0 | 1 | 1 | 1 | 0 | 0 | ↓ | ↓ | ↓ |
| 1 | 0 | 0 | 1 | 0 | 1 | 无 | 无 | ↓ |
| 1 | 0 | 1 | 1 | 1 | 0 | 无 | ↓ | ↓ |
| 1 | 1 | 0 | 1 | 1 | 1 | 无 | 无 | ↓ |
| 1 | 1 | 1 | 0 | 0 | 0 | ↓ | ↓ | ↓ |

② 次态方程

$$Y_1^{n+1} = J_1 \bar{Q}_1 + \bar{K}_1 Q_1 = \bar{Y}_1 \quad CP_1 \downarrow$$

$$Y_2^{n+1} = J_2 \bar{Q}_2 + \bar{K}_2 Q_2 = \bar{Y}_2 \quad Y_1 \downarrow$$

$$Y_3^{n+1} = J_3 \bar{Q}_3 + \bar{K}_3 Q_3 = \bar{Y}_3 \quad Y_2 \downarrow$$

④ 波形图

二分频



Digital Logic Design Solution

Alexander Miczo



Digital Logic Design Solution:

Solutions manual Gideon Langholz, Abraham Kandel, Joe L. Mott, 1988 **Introduction to Digital Logic Design** John Patrick Hayes, 1993-01-01 **Solutions Manual for Digital Logic and State Machine Design** David J. Comer, 1984

Digital Logic Testing and Simulation Alexander Miczo, 1985-12 The new standard in the field presenting the latest design and testing methods for logic circuits and the development of a BASIC based simulation Offers designers and test engineers unique coverage of circuit design for testability stressing the incorporation of hardware into designs that facilitate testing and diagnosis by allowing greater access to internal circuits Examines various ways of representing a design as well as external testing methods that apply this information **Solutions Manual to Digital Logic and State Machine Design** David J. Comer, 1995 This book is intended for undergraduate students in Electrical Engineering **Solutions Manual to Accompany Digital Logic Testing and Simulation** Alexander Miczo, 1986 **DIGITAL ELECTRONICS AND LOGIC DESIGN** NAIR, B. SOMANATHAN, 2002-01-01 Designed as a textbook for undergraduate students in Electrical Engineering Electronics Computer Science and Information Technology this up to date well organized study gives an exhaustive treatment of the basic principles of Digital Electronics and Logic Design It aims at bridging the gap between these two subjects The many years of teaching undergraduate and postgraduate students of engineering that Professor Somanathan Nair has done is reflected in the in depth analysis and student friendly approach of this book Concepts are illustrated with the help of a large number of diagrams so that students can comprehend the subject with ease Worked out examples within the text illustrate the concepts discussed and questions at the end of each chapter drill the students in self study Digital Electronics Dr. S.K. Bhattacharya, Balaka Biswas, 2023-10-13 This book includes the following chapters 1 Number Systems and Codes 2 Logic Gates 3 Boolean algebra and logic simplification 4 Design of Combinational Logic Circuits 5 Arithmetic Circuits 6 Decoder Encoder Multiplexer Demultiplexer 7 Sequential Circuit Design 8 Shift Registers 9 Counters 10 A D and D A Converters 11 Logic Family **Basic Concepts in Digital Electronics and Logic Design** Er Jawad Ahmad Dar, 2017-10-08 This book on Basic Concepts in Digital Electronics and Logic Design has been specially written to meet the requirements of the Diploma Tech M Tech students and research scholar of all Indian universities The subject matter has been discussed in such a simple way that the students will find no difficulty to understand it This Book has been designed to understand the Basic Concepts in Digital Electronics and Logic Design to let students to understand the core concepts with examples The objective of the book are to provide a clear explanation of the operations of all logic devices in general use on today and to impart knowledge of digital electronics The text has been written in a style to enable students to self study The text of the book is simple and lucid Solved examples are provided throughout the book to assist the students to assimilate the material covered Highlights are given at the end of almost each chapter Digital Logic and Computer Design Thomas Richard McCalla, 1992 BIOMEDICAL ELECTRONICS: ENGINEERING HEALTH SOLUTIONS DR.

SUBHASHINI TATA ,S.HARIPRASATH ,DR. M. KAMARAJU ,2025-10-18 Combining biology and engineering biomedical engineering creates technological innovations in patient care medical procedures and gadgets To improve healthcare biomedical engineers work in labs medical facilities and even operating rooms Medical equipment such as prosthesis like hip replacements and implants like cardiac pacemakers are created by biomedical engineers In healthcare settings ranging from long term care homes to hospital emergency rooms they are at the forefront of significant advancements in healthcare such as wearable technology to monitor health issues and equipment utilised at patient bedsides *Instructor's Solutions Manual to Accompany Fundamentals of Digital Logic with Vhdl Design* Lord Brown,Stephen D. Brown,Zvonko G. Vranesic,1999-10-01

Solutions Manual to Accompany Digital Logic Applications and Design, John M. Yarbrough Bassam H.

Matar,1998 **Basic VLSI Design Technology** Cherry Bhargava,Gaurav Mani Khanal,2022-09-01 The current cutting edge VLSI circuit design technologies provide end users with many applications increased processing power and improved cost effectiveness This trend is accelerating with significant implications on future VLSI and systems design VLSI design engineers are always in demand for front end and back end design applications The book aims to give future and current VLSI design engineers a robust understanding of the underlying principles of the subject It not only focuses on circuit design processes obeying VLSI rules but also on technological aspects of fabrication The Hardware Description Language HDL Verilog is explained along with its modelling style The book also covers CMOS design from the digital systems level to the circuit level The book clearly explains fundamental principles and is a guide to good design practices The book is intended as a reference book for senior undergraduate first year post graduate students researchers as well as academicians in VLSI design electronics electrical engineering and materials science The basics and applications of VLSI design from digital system design to IC fabrication and FPGA Prototyping are each covered in a comprehensive manner At the end of each unit is a section with technical questions including solutions which will serve as an excellent teaching aid to all readers Technical topics discussed in the book include Digital System Design Design flow for IC fabrication and FPGA based prototyping Verilog HDL IC Fabrication Technology CMOS VLSI Design Miscellaneous It covers basics of Electronics and Reconfigurable computing PLDs Latest technology etc **Introduction to Logic Design, Second Edition** Sajjan G. Shiva,1998-01-20 The second edition of this text provides an introduction to the analysis and design of digital circuits at a logic instead of electronics level It covers a range of topics from number system theory to asynchronous logic design A solution manual is available to instructors only Requests must be made on official school stationery Digital Electronics Christopher E. Strangio,1980 Logic concepts Boolean algebra Combinational logic Binary number operations Flip flops Counter analysis and design Sequential circuits Digital circuit fault analysis Analog digital conversion Computers and microprocessors **Digital Electronics Through Project Analysis** Ronald A. Reis,1991 An introductory text to digital circuits for beginning electronics students which provides coverage of basic digital concepts and includes 46 actual digital projects that illustrate concrete

applications Coverage encompasses digital combinational and sequential logic circuits Solutions Manual to Accompany
Analysis and Design of Digital Integrated Circuits David A. Hodges, Yu Chen, Horace G. Jackson, 1983 **Solution Manual**
Digital Logic Jefferson C. Boyce, 1982 *Fundamentals of Logic Design* Charles H. Roth, 1992

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