

# Versal Series Overview

## Compute Engines

- Scalar Processors in every device
- Enhanced Programmable Logic
- New AI and enhanced DSP Engines

## NoC and Memory

- High BW Network-on-Chip
- Hardened LP/DDR4/5, and HBM

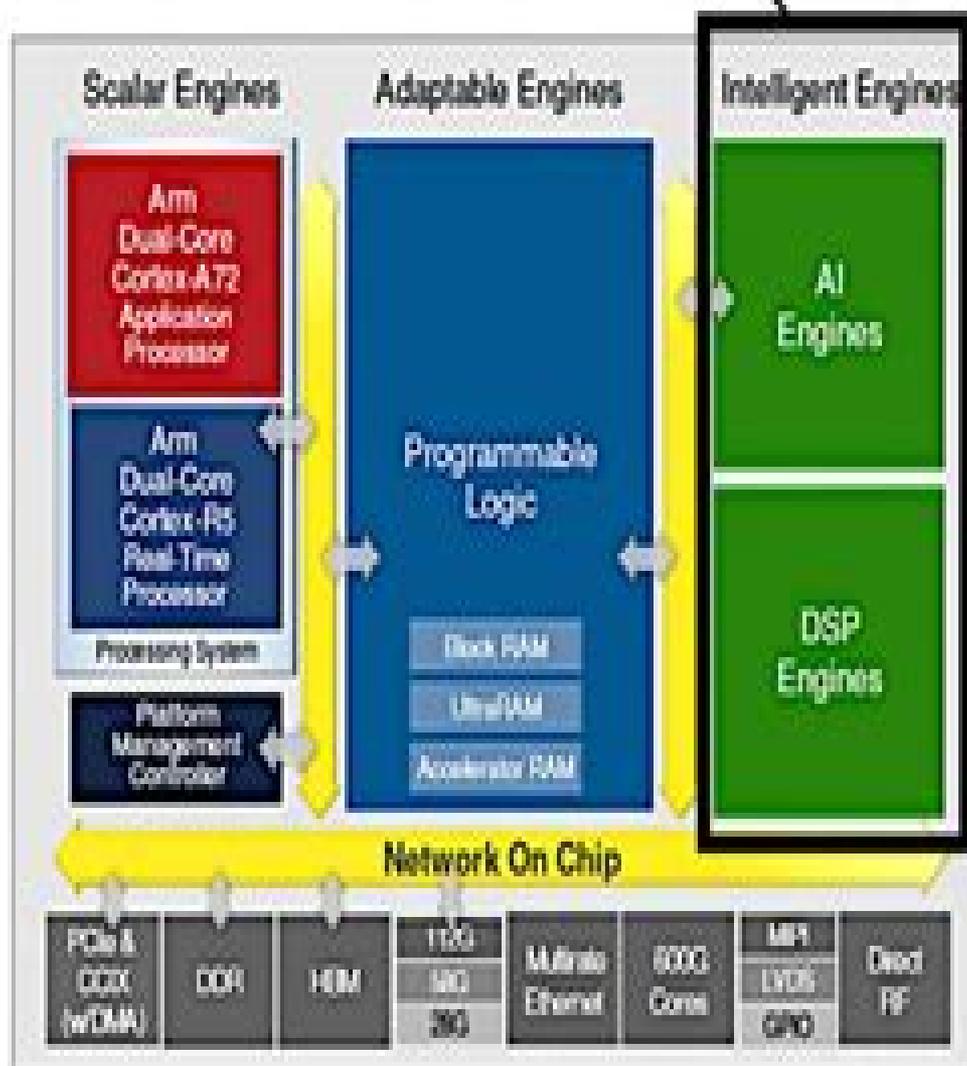
## High-Speed Interfaces

- PCIe & CCIX up to Gen5
- Ethernet MAC up to 600Gbps

## SerDes and RF

- SerDes Up to 112G PAM4
- Integrated ADC/DAC

Newly added  
Intelligent Engines



# Stack Overview Xilinx

**Mark Steyvers**



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