

# Challenge Tutorial 02: Using Design Variables and Parametric Analysis

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## Document Contents

- Introduction
- Using Design Variables
- Running a Parametric Analysis
- Running Parametrically Parametric
- Notes about Design Variables and Parametric Analysis

## Introduction

Design variables provide a method for specifying critical parameters just before a simulation. This allows you to make quick changes to parameter values across single or multiple circuit components without having to edit any schematic components. Design variables can also be used in simple parametric analysis, which can change a design variable across a range of values and display multiple simulation results on a single plot.

## Using Design Variables

This part of the tutorial will show you how to parametricize the width of the PCB and PTH on components of an circuit by using design variables. These variable values are then set using the **Running Through Environment** panel in simulation.

1. Make a copy of the circuitry with components you have previously designed, or create a new one.
2. Place the components of the circuit.
3. Edit each one of the PCB/PTH components to adjust it, and name the properties by using clicking on it and entering Properties. For you can press "q" as shortcut.
4. Define the size in the width box, and type  $var$ . This specifies that a variable called  $var$  will be used to set the width of the PCB/PTH. When you do this, observe that values for "Thermal/Electrical Difference Keys" change from a constant value to a formula that involves the variable  $var$ . Always check to make sure that formula is updated when you change the width to a variable otherwise, your simulation results will be incorrect. Click **OK** to apply the change.
5. Repeat this procedure to change the width of the PTHs to  $var$ .
6. Select **Check**  $\rightarrow$  **Check and name**. Verify that there are no errors.
7. Launch the **Analysis** **Running Through Environment**.
8. Set up the simulation environment as you normally would for simulating a schematic. This includes specifying a simulation file and transient run length, creating the SPICE model reader in the environment options, and selecting the outputs to be plotted on the schematic.
9. In **AN**, select **Variables**  $\rightarrow$  **Copy Hierarchical**. The design variables  $var$  and  $var$  should appear in the Design Variables area of the **Running Environment** window.
10. In **AN**, select **Variables**  $\rightarrow$  **Set**. In the main pane, type 1.0 in the **var** and then apply the value by clicking **Apply**. Change the value of  $var$  to 2.0 and click **Apply**. Check the values have been assigned, click **OK**. See Figure 1 and 2 below.
11. In **AN**, use the connection by selecting **Simulation**  $\rightarrow$  **Variable and Run**. After loading the the simulation to complete, the results window will pop-up. The results should be

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## **Table of Contents Cadence Tutorial D Using Design Variables And Parametric**

1. Understanding the eBook Cadence Tutorial D Using Design Variables And Parametric
  - The Rise of Digital Reading Cadence Tutorial D Using Design Variables And Parametric
  - Advantages of eBooks Over Traditional Books
2. Identifying Cadence Tutorial D Using Design Variables And Parametric
  - Exploring Different Genres
  - Considering Fiction vs. Non-Fiction
  - Determining Your Reading Goals
3. Choosing the Right eBook Platform
  - Popular eBook Platforms
  - Features to Look for in an Cadence Tutorial D Using Design Variables And Parametric
  - User-Friendly Interface
4. Exploring eBook Recommendations from Cadence Tutorial D Using Design Variables And Parametric
  - Personalized Recommendations
  - Cadence Tutorial D Using Design Variables And Parametric User Reviews and Ratings

- Cadence Tutorial D Using Design Variables And Parametric and Bestseller Lists
- 5. Accessing Cadence Tutorial D Using Design Variables And Parametric Free and Paid eBooks
  - Cadence Tutorial D Using Design Variables And Parametric Public Domain eBooks
  - Cadence Tutorial D Using Design Variables And Parametric eBook Subscription Services
  - Cadence Tutorial D Using Design Variables And Parametric Budget-Friendly Options
- 6. Navigating Cadence Tutorial D Using Design Variables And Parametric eBook Formats
  - ePub, PDF, MOBI, and More
  - Cadence Tutorial D Using Design Variables And Parametric Compatibility with Devices
  - Cadence Tutorial D Using Design Variables And Parametric Enhanced eBook Features
- 7. Enhancing Your Reading Experience
  - Adjustable Fonts and Text Sizes of Cadence Tutorial D Using Design Variables And Parametric
  - Highlighting and Note-Taking Cadence Tutorial D Using Design Variables And Parametric
  - Interactive Elements Cadence Tutorial D Using Design Variables And Parametric
- 8. Staying Engaged with Cadence Tutorial D Using Design Variables And Parametric
  - Joining Online Reading Communities
  - Participating in Virtual Book Clubs
  - Following Authors and Publishers Cadence Tutorial D Using Design Variables And Parametric
- 9. Balancing eBooks and Physical Books Cadence Tutorial D Using Design Variables And Parametric
  - Benefits of a Digital Library
  - Creating a Diverse Reading Collection Cadence Tutorial D Using Design Variables And Parametric
- 10. Overcoming Reading Challenges
  - Dealing with Digital Eye Strain
  - Minimizing Distractions
  - Managing Screen Time
- 11. Cultivating a Reading Routine Cadence Tutorial D Using Design Variables And Parametric
  - Setting Reading Goals Cadence Tutorial D Using Design Variables And Parametric
  - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Cadence Tutorial D Using Design Variables And Parametric
  - Fact-Checking eBook Content of Cadence Tutorial D Using Design Variables And Parametric
  - Distinguishing Credible Sources

13. Promoting Lifelong Learning
  - Utilizing eBooks for Skill Development
  - Exploring Educational eBooks
14. Embracing eBook Trends
  - Integration of Multimedia Elements
  - Interactive and Gamified eBooks

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