

64-Bit z/OS Assembler Coding

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elements of x86 SIMD architectures register sets and data types Master x86 SIMD arithmetic and data operations using both integer and floating point operands Harness the AVX AVX2 and AVX 512 instruction sets to accelerate the performance of computationally intense calculations in machine learning image processing signal processing computer graphics statistics and matrix arithmetic applications Apply leading edge coding strategies to optimally exploit the AVX AVX2 and AVX 512 instruction sets for maximum possible performance

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