



68000 Microprocessor

Andrew M. Veronis



68000 Microprocessor:

The 68000 Microprocessor Andrew M. Veronis, 2012-12-06 The Motorola MC68000 family of microprocessors is undoubtedly a revolutionary set of devices The MC68000 is the first advanced 16 bit microprocessor with a 32 bit internal architecture and the first with 16 megabyte nonsegmented direct memory addressing The processor's six basic addressing modes are equivalent to 14 when one considers all of the variations among these modes Combined with the device's data and instruction types the modes provide more than 1000 useful instructions The book you are about to study has been developed as an aid to the hardware designer and as a supplement to the Motorola seminars on the 68000 microprocessor The text includes a detailed description of the MC68000 and two complete systems that show how this processor can be interfaced to the outside world The book follows a top down approach A brief history of microprocessors is provided first Chapter 2 details the MC68000 by describing its registers control lines and capabilities Chapter 3 introduces a small MC68000 based system Although this system is characterized in the book as hypothetical it is indeed the Educational Computer Board used in the various Motorola seminars The addressing modes and instructions are explained in Chapter 4 which includes helpful hints on how instructions can be used Chapter 5 provides an in depth description of additional instructions and numerous examples Chapter 6 discusses exception handling and interrupts

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MC68010 Appendix V Comparison of 68000 Family Processors Answers to Selected Exercises Index The M68000 Microprocessor Family Yu-Cheng Liu, Youzheng Liu, 1991 In the past several years microprocessors have emerged as a major force in the computer industry and the Motorola MC68000 family is regarded as an industry standard The focus of this book is the Motorola MC68000 microprocessor family Many of the design practices and fundamental concepts can apply to other modern microprocessors as well This guide covers both the software and hardware of the M68000 family and is designed as a text for a one semester junior level microprocessor course that covers both programming and system design using the MC68000 microprocessor

The 68000 and 68020 Microprocessors Walter A. Triebel, Avtar Singh, 1991 M CREATED

Library of Congress Subject Headings Library of Congress. Cataloging Policy and Support Office, 2009 **Library of Congress Subject Headings** Library of Congress, 2002 MC68000 16-bit Microprocessor Motorola, Inc, 1982 For Design Engineers Software Architects Computer Designers a Guide to Completing Software Systems Using Motorola s MC68000 Family of Microprocessors Introduction to 6800/68000 Microprocessors Frederick F. Driscoll, 1987 **The Motorola Microprocessor Family** Barry B. Brey, 1992 **68000 Microprocessor Handbook** William D. Cramer, Gerry Kane, 1986

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