Digital Signal Processing

Chapter 1 : Discrete Time Signals

Q. 1 With the help of block schematic diagram explain basic elements of digital signal processing.

Ans. :

Basic elements of digital signal processing :

Fig. 1.1 shows that the basic elements of digital signal processing system.

Analog input $x(t)$ → Anti – Aliasing filter (LPF) → Sample and hold circuit (S/H circuit) → ADC → Digital signal processor → DAC → Reconstruction filter → Output

Fig. 1.1

The different blocks of this system are as follows :

1.   Input signal :

It is the signal generated from some transducer or from some communication system. It may be biomedical signal like ECG or EEG. Generally input signal is analog in nature. It is denoted by $x(t)$.

2.   Anti-aliasing filter :

Anti aliasing filter is basically a low pass filter. It is used for the following purposes :

(a)   It removes the high frequency noise contain in input signal.

(b)   As the name indicates; it avoids aliasing effect. That means it is used to band limit the signal.

3.   Sample and hold circuit :

This block takes the samples of input signal. It keeps the voltage level of input signal relatively constant. Sometimes amplifiers are required to bring the voltage level of input signal upto the required voltage level of ADC.

4.   Analog to Digital Converter (ADC) :

This block is used to convert analog signal into digital form. This is required, because digital signal processor accepts the signal which is digital in nature.

5.   Digital signal processor :

It processes input signal digitally. In a simple language processing of input signal means modifying the signal as per requirement. For this purpose DSP processors like ADSP 2100 or TMS 320 can be used.

6.   Digital to Analog Converter (DAC) :

The output of digital signal processor is digital in nature. But the required final output is analog in nature. So to convert digital signal into analog signal DAC is used.

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