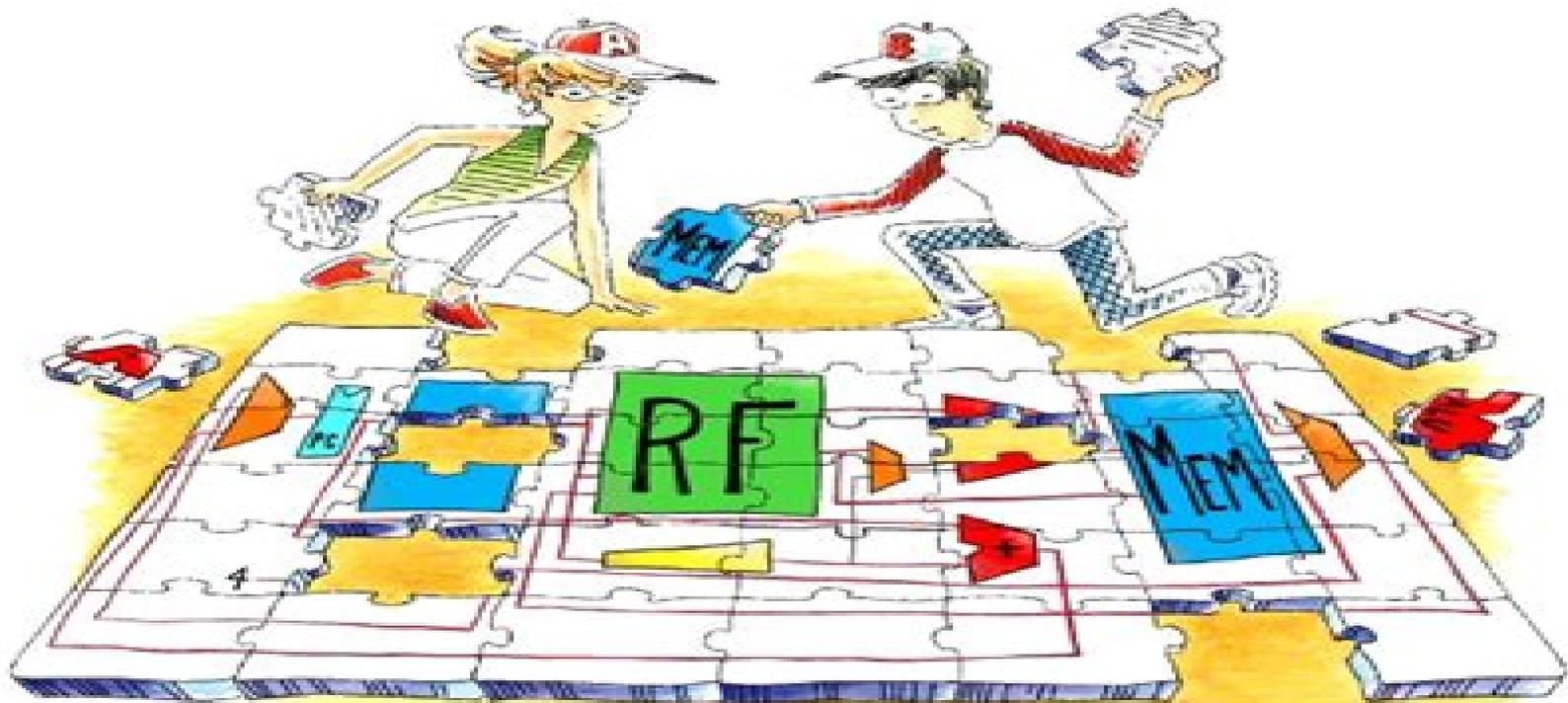


Digital Design and Computer Architecture

RISC-V Edition



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Digital Design and Computer Architecture(ARM Edition) Sarah L. Harris, 2016-12 *Digital Design and Computer Architecture* David Money Harris, Sarah L. Harris, 2013 Provides practical examples of how to interface with peripherals using RS232 SPI motor control interrupts wireless and analog to digital conversion This book covers the fundamentals of digital logic design and reinforces logic concepts through the design of a MIPS microprocessor

Digital Design and Computer Architecture Sarah Harris, David Harris, 2021-08-17 The newest addition to the Harris and Harris family of Digital Design and Computer Architecture books this RISC V Edition covers the fundamentals of digital logic design and reinforces logic concepts through the design of a RISC V microprocessor Combining an engaging and humorous writing style with an updated and hands on approach to digital design this book takes the reader from the fundamentals of digital logic to the actual design of a processor By the end of this book readers will be able to build their own RISC V microprocessor and will have a top to bottom understanding of how it works Beginning with digital logic gates and progressing to the design of combinational and

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Advanced Digital Logic Design Sunggu Lee,2006 This textbook is intended to serve as a practical guide for the design of complex digital logic circuits such as digital control circuits network interface circuits pipelined arithmetic units and RISC microprocessors It is an advanced digital logic design textbook that emphasizes the use of synthesizable Verilog code and provides numerous fully worked out practical design examples including a Universal Serial Bus interface a pipelined multiply accumulate unit and a pipelined microprocessor for the ARM THUMB architecture

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About the Author: Frank Vahid is a Professor of Computer Science, has worked and consulted for Hewlett Packard, AMCC, NEC, Motorola, and medical equipment makers, holds 3 U.S. patents, has received several teaching awards, helped setup UCR's Computer Engineering program, has authored two previous textbooks, and has published over 120 papers on digital design topics: automation, architecture, and low power.

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