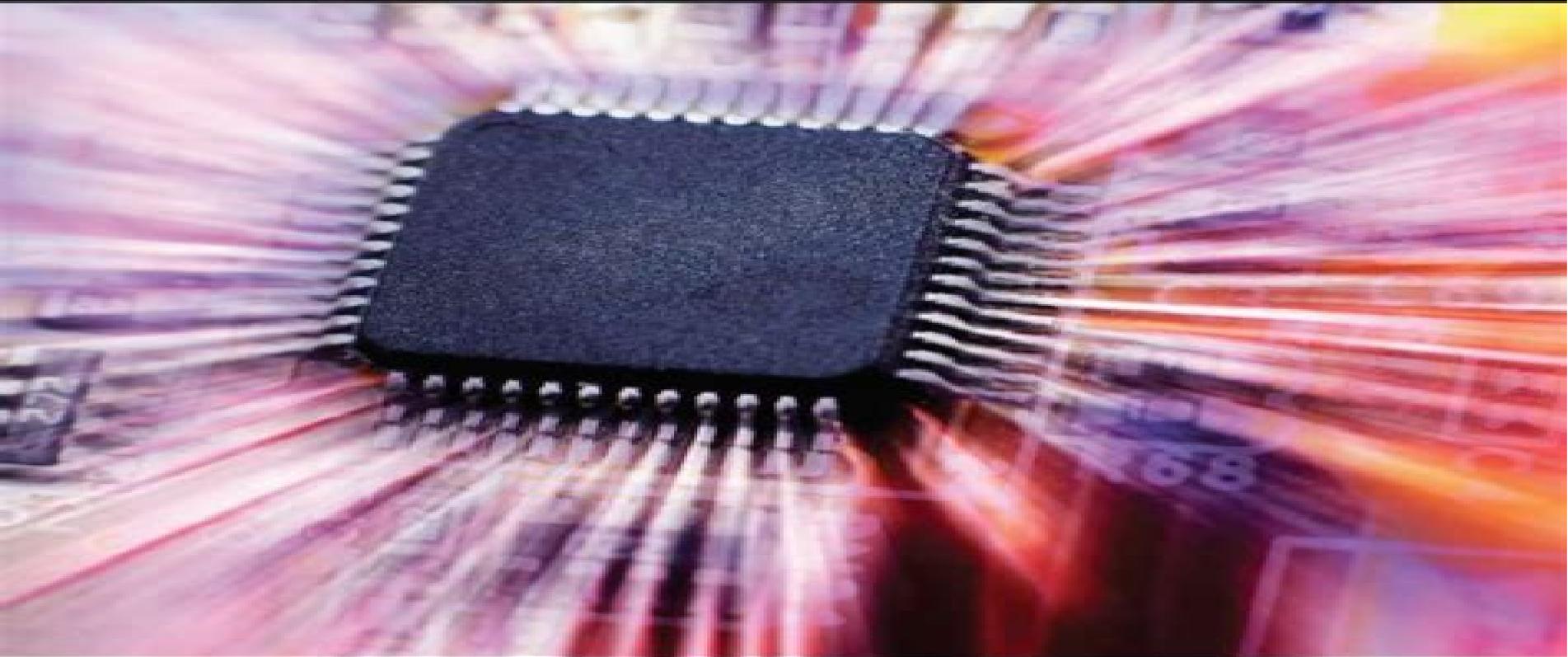


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*MOSFET Technologies for Double-Pole Four-Throw Radio-Frequency Switch* Viranjay M. Srivastava, Ghanshyam Singh, 2013-10-07 This book provides analysis and discusses the design of various MOSFET technologies which are used for the design of Double Pole Four Throw DP4T RF switches for next generation communication systems The authors discuss the design of the DP4T RF switch by using the Double Gate DG MOSFET as well as the Cylindrical Surrounding double gate CSDG MOSFET The effect of HFO2 high dielectric material in the design of DG MOSFET and CSDG MOSFET is also explored Coverage includes comparison of Single gate MOSFET and Double gate MOSFET switching parameters as well as testing of

MOSFETs parameters using image acquisition [2nd EAI International Conference on Big Data Innovation for Sustainable Cognitive Computing](#) Anandakumar Haldorai, Arulmurugan Ramu, Sudha Mohanram, Mu-Yen Chen, 2020-09-30 This proceeding features papers discussing big data innovation for sustainable cognitive computing. The papers feature details on cognitive computing and its self-learning systems that use data mining, pattern recognition, and natural language processing (NLP) to mirror the way the human brain works. This international conference focuses on cognitive computing technologies from knowledge representation techniques and natural language processing algorithms to dynamic learning approaches. Topics covered include Data Science for Cognitive Analysis, Real Time Ubiquitous Data Science Platform for Privacy Preserving Data Science, and Internet Based Cognitive Platform. The 2nd EAI International Conference on Big Data Innovation for Sustainable Cognitive Computing (BDCC) 2019 took place in Coimbatore, India, on December 12-13, 2019. It contains proceedings from the 2nd EAI International Conference on Big Data Innovation for Sustainable Cognitive Computing (BDCC) 2019, Coimbatore, India, December 12-13, 2019. Features topics ranging from Data Science for Cognitive Analysis to Internet Based Cognitive Platforms. Includes contributions from researchers, academics, and professionals from around the world. [Ultra Low Power Bioelectronics](#) Rahul Sarpeshkar, 2010-02-22 This book provides for the first time a broad and deep treatment of the fields of both ultra low power electronics and bioelectronics. It discusses fundamental principles and circuits for ultra low power electronic design and their applications in biomedical systems. It also discusses how ultra energy efficient cellular and neural systems in biology can inspire revolutionary low power architectures in mixed signal and RF electronics. The book presents a unique unifying view of ultra low power analog and digital electronics and emphasizes the use of the ultra energy efficient subthreshold regime of transistor operation in both. Chapters on batteries, energy harvesting, and the future of energy provide an understanding of fundamental relationships between energy use and energy generation at small scales and at large scales. A wealth of insights and examples from brain implants, cochlear implants, bio-molecular sensing, cardiac devices, and bio-inspired systems make the book useful and engaging for students and practicing engineers.

**CMOS Transceiver Design for Short Range Wireless Telemetry** Jonghae Kim, 2001 **Analysis, Simulation, and Applications of Passive Devices on Conductive Substrates** Ali M. Niknejad, 2000 **Memorandum**, 2000 *Proceedings*, 2005 **The Cumulative Book Index**, 1998 A world list of books in the English language [American Book Publishing Record](#), 1997-09 **Book Review Index** Gale Group, Thomson Gale Staff, 1999-09 Book Review Index provides quick access to reviews of books, periodicals, books on tape, and electronic media representing a wide range of popular, academic, and professional interests. Book Review Index is available in a three-issue subscription covering the current year or as an annual cumulation covering the past year. **5G and E-Band Communication Circuits in Deep-Scaled CMOS** Marco Vigilante, Patrick Reynaert, 2018-02-07 This book discusses design techniques, layout details, and measurements of several key analog building blocks that currently limit the performance of 5G and E-Band transceivers implemented in

deep scaled CMOS The authors present recent developments in low noise quadrature VCOs and tunable inductor less frequency dividers Moreover the design of low loss broadband transformer based filters that realize inter stage matching power division combining and impedance transformation is discussed in great detail The design and measurements of a low noise amplifier a downconverter and a highly linear power amplifier that leverage the proposed techniques are shown All the prototypes were realized in advanced nanometer scaled CMOS technologies without RF thick to metal option

**Books in Print Supplement** ,2002

**Design of CMOS Phase-Locked Loops** Behzad Razavi,2020-01-30 This modern pedagogic textbook from leading author Behzad Razavi provides a comprehensive and rigorous introduction to CMOS PLL design featuring intuitive presentation of theoretical concepts extensive circuit simulations over 200 worked examples and 250 end of chapter problems The perfect text for senior undergraduate and graduate students

**Fundamentals of Microelectronics, 2nd Edition** Razavi,2013-11-12

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