

$$\frac{V_0 - V_2}{R_2} + \frac{V_0}{R_0} = 0 \quad (\text{Eq. 4})$$

And finally:

$$V_2 = V_0 + 4 \quad (\text{Eq. 5})$$

Solving yields:

$$V_0 = 526.32 \text{ mV}$$

$$P_L = \frac{V_0^2}{R_0} = 69.25 \text{ mW}$$

Problem 3.13

(a) For the circuit of Figure P3.13, write the node equations necessary to find voltages V_1 , V_2 , and V_3 . Note that $G = 1/R = \text{conductance}$. From the results, note the interesting form that the matrices $[G]$ and $[I]$ have taken in the equation $[G][V] = [I]$ where

$$[G] = \begin{bmatrix} g_{11} & g_{12} & g_{13} & \cdots & g_{1n} \\ g_{21} & g_{22} & \cdots & \cdots & g_{2n} \\ g_{31} & \cdots & g_{33} & \cdots & g_{3n} \\ \vdots & \vdots & \vdots & \ddots & \vdots \\ g_{n1} & g_{n2} & \cdots & \cdots & g_{nn} \end{bmatrix} \quad \text{and} \quad [I] = \begin{bmatrix} I_1 \\ I_2 \\ \vdots \\ I_n \end{bmatrix}$$

(b) Write the matrix form of the node voltage equations again, using the following formulas:
 $g_{ii} = \Sigma$ conductances connected to node i
 $g_{ij} = -\Sigma$ conductances shared by nodes i and j
 $I_i = \Sigma$ all source currents into node i .

Solution:

Known quantities:

Circuit shown in Figure P3.13.

Find:

- Voltages
- Write down the equations in matrix form.

Analysis:

a) Using conductances, apply KCL at node 1:

$$(G_1 + G_{12} + G_{13})V_1 - G_{12}V_2 - G_{13}V_3 = I_s$$

Then apply KCL at node 2:

$$-G_{12}V_1 + (G_2 + G_{12} + G_{23})V_2 - G_{23}V_3 = 0$$

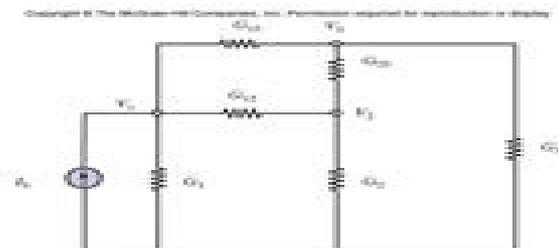
and at node 3:

$$-G_{13}V_1 - G_{23}V_2 + (G_3 + G_{13} + G_{23})V_3 = 0$$

Rewriting in the form

$$[G][V] = [I]$$

3.11



Rizzoni Electrical Engineering Chapter 4 Answer

Yijin Wang



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