

**FINAL EXAMINATION
COE 117 - LOGIC CIRCUIT 1
2nd Qtr. / 2010 - 2011**

- An expression is said to be in this form when all products are the products of only single variables.
 - POS
 - SOP**
 - Minterm
 - Maxterm
- The Boolean expression $X(X + Y) = X$ is an example of which Law/Theorem?
 - Simplification**
 - Absorption
 - De Morgan's
 - Commutative
- The signal in the most present day electronic digital systems use just two discrete values and are therefore said to be in _____.
 - Digital**
 - Decimal
 - Analog
 - Binary
- The Boolean expression $X + X' = 1$ is an example of which Law/Theorem?
 - Complementary**
 - Absorption
 - Idempotent
 - Inequality
- It is the process by which certain data or information is represented in a numerical pattern.
 - Digital System
 - Analog System
 - Number System**
 - None
- DEADFACE base 16 = _____
 - 1101111010101101.1111101011001010₂
 - 11041111010101101.1111100041001110₂
 - 11041111010101101.1001101011001110₂
 - 1101111010101101.1111101011001110₂**
- The given : 10110110_2 is equivalent to
 - 182₁₀**
 - 183₁₀
 - 183₁₆
 - 180₁₆
- $BABE_{16} + 1010011101_2$
 - A978
 - B1D5B**
 - BEBE
 - F23C
- The given: 16_{16} is equivalent to
 - 0100 0011₁₆₀₂
 - 0001 0001₂
 - 0001 1000₁₆₂
 - All of the above**
- The Boolean expression $X + X = X$ is an example of which Law/Theorem?
 - Complementary
 - Absorption
 - Idempotent**
 - Inequality
- This part of the design of digital system involves determining how to interconnect basic logic building blocks to perform a specific function.
 - System Design
 - Logic Design**
 - Switching Design
 - None
- Convert the decimal number 888 to binary
 - 1101111000₂**
 - 1101011000₂
 - 1101001000₂
 - 1001111000₂
- 0.625 base 10 = _____ base 16
 - 0.51
 - 50
 - 0.5**
 - 2.5
- What is the equivalent gate of two 1-input NOR gates connected in series?
 - AND
 - Buffer**
 - Inverter
 - None
- It is a form in which Boolean expression is expressed as sum of minterms or product of minterms.
 - Standard Form
 - Canonical Form
 - Boolean Expression
 - None of the above**
- What is the code use to label the cell of K-map?
 - Binary code
 - Gray code**
 - BCD
 - Excess-3 code
- What will happen if the function $v(w+xy)z$ would be implemented using the NOR gate?
 - Six NOR gates will be used.**
 - The given function must be implemented using OR and NAND gates
 - Not possible using NOR gates
 - None of the above
- Use K-map to find the following functions: (A,B,C,D,E)

$$F = \sum (1,3,5,7,8,12,13,18,19,22,23,24,27,28,31)$$
 - $ABE + CD'E + AB'D + ADE + BD'E$**
 - $A'B'E + CDE + AB'D + ADE + BD'E$
 - $ABE + CD'E + AB'D + A'DE + BD'E$
 - $A'B'E + CD'E + AB'D + ADE + BD'E$
- Use the K-map to find minimum sum of products of $F(A,B,C,D) = \prod (0,2,4,6,8,10,12,14)$
 - $F = D$**
 - $F = D'$
 - $F = A'D$
 - $F = ABCD$
- Simplify the following Boolean Expression using Boolean Algebra: $(wx + yz)(w'y + xz)$
 - $w'x'y'$
 - $wx'z$
 - $wxyz$**
 - $w'xz$
- Given the function $F(a,b,c,d) = \prod (1,3,5,9,12,14,15)$ determine its canonical form.
 - $F = M_1M_2M_3M_4M_5M_{12}M_{14}M_{15}$**
 - $F = m_1 + m_2 + m_3 + m_4 + m_5 + m_{12} + m_{14} + m_{15}$
 - $F = m_1m_2m_3m_4m_5m_{12}m_{14}m_{15}$
 - $F = M_1 + M_2 + M_3 + M_4 + M_{12} + M_{14} + M_{15}$
- A multiplexer is also called _____.
 - Comparator
 - Data Controller
 - Data Interpreter
 - Data Selector**
- The 74LS83 is an example of a 4-bit parallel adder. To expand this device to an 8-bit adder you must
 - Use 2 adders and connect the sum outputs of one to the bit inputs of the other
 - Use 8 adders with no interconnections
 - Use two adders with the carry output of the one connected to the carry input of the other**
 - Use four adders with no interconnections
- There are _____ 1-to-2 decoder to implement a 5-to-32 decoder using four 3-to-8 decoder
 - 2
 - 3**
 - 4
 - NONE
- How many 3-to-8 decoder is needed to implement a 6-to-64 Decoder?
 - 8
 - 9**
 - 10
 - 11
- How many inputs, outputs and selectors are needed to implement 16X1 MUX?
 - 8 Inputs, 16 outputs, 3 selectors
 - 16 Inputs, 3 outputs, 8 selectors
 - 16 outputs, 16 inputs, 4 selectors
 - 1 output, 16 inputs, 4 selectors**
- Simplify the given Boolean function.

$$F(A,B,C,D) = \sum m(0,6,8,13,14)$$

$$d(A,B,C,D) = \sum m(2,4,10)$$
 - $F = B'D' + AB'C'D + CD$
 - $F = C'D' + BD' + A'BCD$
 - $F = ABC'D + BD + CD$
 - $F = CD' + B'D' + ABC'D$**

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