

Application Hints for ATA5723/ATA5724/ATA5728

ATA5723/ATA5724/ATA5728

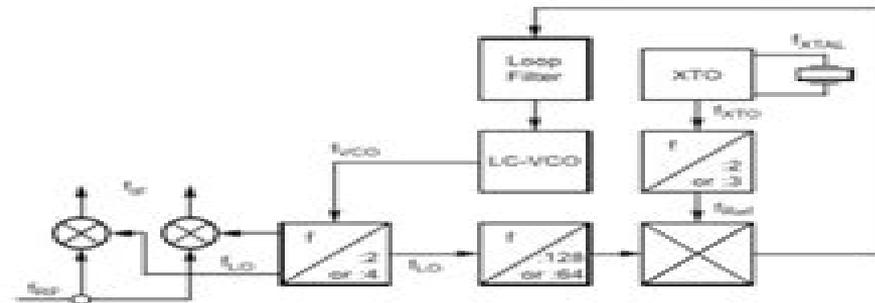
Introduction

The UHF receiver ATA5723, ATA5724, and ATA5728 are designed specially for automotive application like Remote Keyless Entry as well as Tire Pressure Monitoring System. These receivers are upgrades of ATA5743 and ATA5760. They have compatible pinning that allows the development of one layout for applications in three different ISM (Industrial Scientific Medical) frequency bands. The frequency ranges are 312.5MHz to 317.5MHz, 431.5MHz to 436.5MHz and 868MHz to 870MHz. Another benefit of the receiver is the reuse of the software from Atmel's older receiver, the ATA5743. Additional features include the RSSI output, the pierce crystal oscillator achieving a better oscillation margin, an improved sensitivity as well as the image rejection.

The purpose of this application note is to give a designer some hints how to start developing a receiver module with ATA5723/ATA5724/ATA5728.

Calculating the Required Crystal Frequency

Figure 1. System Block Diagram for the Receivers Containing the Internal Frequency Generation and Processing (Synthesizer)



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Dhananjay Gadre



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AVR RISC Microcontroller Handbook Claus Kuhnel, 1998-10-02 The AVR RISC Microcontroller Handbook is a comprehensive guide to designing with Atmel's new controller family which is designed to offer high speed and low power consumption at a lower cost. The main text is divided into three sections: hardware which covers all internal peripherals; software which covers programming and the instruction set; and tools which explains using Atmel's Assembler and Simulator available on the Web, as well as IAR's C compiler. Practical guide for advanced hobbyists or design professionals. Development tools and code available on the Web.

The Art of Hardware Architecture Mohit Arora, 2011-10-09 This book highlights the complex issues, tasks, and skills that must be mastered by an IP designer in order to design an optimized and robust digital circuit to solve a problem. The techniques and methodologies described can serve as a bridge between specifications that are known to the designer and RTL code that is final outcome, reducing significantly the time it takes to convert initial ideas and concepts into right first time silicon. Coverage focuses on real problems rather than theoretical concepts with an emphasis on design techniques across various aspects of chip design.

Advanced Data Acquisition and Intelligent Data Processing Vladimir Haasz, Kurosh Madani, 2022-09-01 DAQ and data processing is a basic part of all automated production systems, diagnostic systems, watching over quality of production, energy distribution, transport control, or in various other areas. Demands on the speed, accuracy, and reliability increase in general. It is possible to achieve not only using superior but also more expensive hardware but also applying advanced data acquisition and intelligent data processing. It deals e.g. optimal data fusion of a number of sensors, new stochastic methods for accuracy increasing, new algorithms for acceleration of data processing, etc. These are the grounds for publishing this book. Advanced Data Acquisition and Intelligent Data Processing offers 10 up-to-date examples of different applications of advanced data acquisition and intelligent data processing used in monitoring, measuring, and diagnostics systems. The book arose based on the most interesting papers from this area published at IDAACS 2013 conference. However, the individual chapters include not only designed solution in wider context but also relevant theoretical parts, achieved results, and possible future ways. Technical topics discussed in this book include advanced methods of data acquisition in application that are not routine, measured data fusion using up-to-date advanced data processing, nonlinear dynamical systems identification, multidimensional image processing. Advanced Data Acquisition and Intelligent Data Processing is ideal for personnel of firms, deals with advanced instrumentation, energy consumption monitoring, environment monitoring, non-destructive diagnostics, robotics, etc. as well as academic staff and postgraduate students in electrical control and computer engineering.

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Intelligent Science and Intelligent Data Engineering Jian Yang,Fang Fang,Changyin Sun,2013-02-11 This book constitutes the proceedings of the third Sino foreign interchange Workshop on Intelligence Science and Intelligent Data Engineering IScIDE 2012 held in Nanjing China in October 2012 The 105 papers presented were carefully peer reviewed and selected from 429 submissions Topics covered include pattern recognition computer vision and image processing machine learning and computational intelligence knowledge discovery data mining and web mining graphics and computer visualization and multimedia processing and applications

Complete Wireless Design, Second Edition Cotter W. Sayre,2008-07-01 Gain the Skill to Design Modern Wireless Circuits and Systems This fully updated and revised edition of the bestselling Complete Wireless Design takes a uniquely practical approach to designing complex receivers and transmitters found in advanced analog and digital wireless communication systems right down to the circuit level This authoritative book uses real life examples to provide a solid foundation in the subject and simple algebra to guide you through specific analysis and design processes In addition you ll find all the information you ll need for performing full circuit and electromagnetic software simulations to ensure the optimum performance of all completed projects Plus this in depth step by step guide comes with a CD ROM containing new simulation and design software Engineers and technicians will not find a more thorough practical book than Complete Wireless Design Updates include Fully worked out design samples complete with RF simulation results Special sections on power amplifier design and printed circuit board layout Brand new chapters covering antenna design and RF test and measurement Tips and techniques on performing accurate RF circuit simulations How to design for EMI control to pass FCC product testing The latest software for use in wireless design This COMPLETELY updated edition teaches you how to design Amplifiers Oscillators Frequency synthesizers Filters Mixers Antennas Support circuits Communication systems

Embedded Systems Design and Applications with the 68HC12 and HCS12 Steven Frank Barrett,Daniel J. Pack,2005 For a second microprocessor course for students enrolled in Electrical Computer Engineering Microcontroller courses Designed for a senior or graduate level embedded systems design course Embedded Systems Design and Applications with the 68HC12 introduces readers to unique issues associated with designing testing integrating and implementing microcontroller microprocessor based embedded systems

Handbook of Research on Mobility and Computing: Evolving Technologies and Ubiquitous Impacts Cruz-Cunha, Maria Manuela,Moreira, Fernando,2011-04-30 Discusses the main issues challenges opportunities and trends related to this explosive range of new developments and applications in constant evolution and impacting every organization and society as a whole This two volume handbook supports post graduate students teachers and researchers as well as IT professionals and managers

Field-programmable Logic and Applications, 2000 *Platform for Heading Measurement Using Magneto-resistive Sensors for Autonomous Flying Vehicle* Konstantin Klitenik, 2006 **Programming and Customizing the AVR Microcontroller** Dhananjay Gadre, 2000-10-09 Publisher's Note Products purchased from Third Party sellers are not guaranteed by the publisher for quality authenticity or access to any online entitlements included with the product How to take charge of the newest most versatile microcontrollers around Atmel's AVR RISC chip family with CD ROM This reader friendly guide shows you how to take charge of the newest most versatile microcontrollers around Atmel's AVR RISC chip family Inside Electronics World writer and astronomy instrumentation developer Dhananjay V Gadre walks you from first meeting these exciting new computers on a chip all the way through design and ready to launch products Personal Inertial Navigation System Lucas L. Delaney, Philip J. Sieh, 2004 *A Microcontroller Based Control System for a Split Parallel Hybrid Electric Vehicle* Tareq Nisar Aryne, 2001 **Circuit Cellar Ink**, 1997 *IEEE Symposium on FPGAs for Custom Computing Machines* Peter Athanas, Kenneth L. Pocek, 1995 *IC Master*, 2001 *Optical Engineering*, 2005 Publishes papers reporting on research and development in optical science and engineering and the practical applications of known optical science engineering and technology **EDN**, 2008 Computer Arithmetic Behrooz Parhami, 2010 Ideal for graduate and senior undergraduate courses in computer arithmetic and advanced digital design Computer Arithmetic Algorithms and Hardware Designs Second Edition provides a balanced comprehensive treatment of computer arithmetic It covers topics in arithmetic unit design and circuit implementation that complement the architectural and algorithmic speedup techniques used in high performance computer architecture and parallel processing Using a unified and consistent framework the text begins with number representation and proceeds through basic arithmetic operations floating point arithmetic and function evaluation methods Later chapters cover broad design and implementation topics including techniques for high throughput low power fault tolerant and reconfigurable arithmetic An appendix provides a historical view of the field and speculates on its future An indispensable resource for instruction professional development and research Computer Arithmetic Algorithms and Hardware Designs Second Edition combines broad coverage of the underlying theories of computer arithmetic with numerous examples of practical designs worked out examples and a large collection of meaningful problems This second edition includes a new chapter on reconfigurable arithmetic in order to address the fact that arithmetic functions are increasingly being implemented on field programmable gate arrays FPGAs and FPGA like configurable devices Updated and thoroughly revised the book offers new and expanded coverage of saturating adders and multipliers truncated multipliers fused multiply add units overlapped quotient digit selection bipartite and multipartite tables reversible logic dot notation modular arithmetic Montgomery modular reduction division by constants IEEE floating point standard formats and interval arithmetic *Embedded Systems Programming*, 1999-07 **Proceedings**, 2001

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