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Ltc3780 High Efficiency Synchronous 4 Switch Buck

Mark Cory



Ltc3780 High Efficiency Synchronous 4 Switch Buck:

Analog Circuit Design Volume Three Bob Dobkin, John Hamburger, 2014-11-29 Design Note Collection the third book in the Analog Circuit Design series is a comprehensive volume of applied circuit design solutions providing elegant and practical design techniques Design Notes in this volume are focused circuit explanations easily applied in your own designs This book includes an extensive power management section covering switching regulator design linear regulator design microprocessor power design battery management powering LED lighting automotive and industrial power design Other sections span a range of analog design topics including data conversion data acquisition communications interface design operational amplifier design techniques filter design and wireless RF communications and network design Whatever your application industrial medical security embedded systems instrumentation automotive communications infrastructure satellite and radar computers or networking this book will provide practical design techniques developed by experts for tackling the challenges of power management data conversion signal conditioning and wireless RF analog circuit design A rich collection of applied analog circuit design solutions for use in your own designs Each Design Note is presented in a concise two page format making it easy to read and assimilate Contributions from the leading lights in analog design including Bob Dobkin Jim Williams George Erdi and Carl Nelson among others Extensive sections covering power management data conversion signal conditioning and wireless RF

Embedded System Interfacing Marilyn Wolf, 2019-02-08 Embedded System Interfacing Design for the Internet of Things IoT and Cyber Physical Systems CPS takes a comprehensive approach to the interface between embedded systems and software It provides the principles needed to understand how digital and analog interfaces work and how to design new interfaces for specific applications The presentation is self contained and practical with discussions based on real world components Design examples are used throughout the book to illustrate important concepts This book is a complement to the author's Computers as Components now in its fourth edition which concentrates on software running on the CPU while Embedded System Interfacing explains the hardware surrounding the CPU Provides a comprehensive background in embedded system interfacing techniques Includes design examples to illustrate important concepts and serve as the basis for new designs Discusses well known widely available hardware components and computer aided design tools

EDN, 2005 **Improving the Control Strategy for a Four-switch Buck-boost Converter** Michael Peter Whitaker, 2007 The four switch buck boost converter such as implemented with the Linear Technology LTC3440 integrated circuit is useful in many applications While this topology can be highly efficient new strategies for controlling it could decrease power losses even more The strategy proposed for this thesis involves the use of level shifted triangle waves and high speed comparators to achieve a narrower buck boost region than achieved in previous control ICs such as the LTC3440 Reducing the amount of operation in this regime is desirable because it is the most inefficient mode of operation of the converter This potentially simpler solution will

allow for resources to be used to improve the performance of other elements of the circuit allowing for potential increases in efficiency

Fully Integrated Switched Capacitor Buck Converter with High Efficiency and Low Output Ripple Sudhir Reddy Gouni,2012

Conventional and ZVT Synchronous Buck Converter Design, Analysis, and Measurement Mark Cory,2010

The role played by power converting circuits is extremely important to almost any electronic system built today. Circuits that use converters of any type depend on power that is consistent in form and reliable in order to properly function. In addition today's demands require more efficient use of energy from large stationary systems such as power plants all the way down to small mobile devices such as laptops and cell phones. This places a need to reduce any losses to a minimum. The power conversion circuitry in a system is a very good place to reduce a large amount of unnecessary loss. This can be done using circuit topologies that are low loss in nature. For low loss and high performance soft switching topologies have offered solutions in some cases. Also limited study has been performed on device aging effects on switching mode power converting circuits. The impact of this effect on a converter's overall efficiency is theoretically known but with little experimental evidence in support. In this thesis non isolated buck type switching converters will be the main focus. This type of power conversion is widely used in many systems for DC to DC voltage step down. Newer methods and topologies to raise converter power efficiency are discussed including a new synchronous ZVT topology. Also a study has been performed on device aging effects on converter efficiency. Various scenarios of voltage conversion switching frequency and circuit components as well as other conditions have been considered. Experimental testing has been performed in both cases ZVT's benefits and device aging effects the results of which are discussed as well.

Fully-integrated CMOS Switched-capacitor Buck Converter Using Three Switching Networks for High Efficiency Over a Wide Range of Line Voltage Muhammed Junayet Hossain,2014

A fully integrated switched capacitor DC DC buck converter is designed in 0.5 μm CMOS process for low line regulation from 2.5 VDC to 4.5 VDC that converts to an output voltage of 1.2-0.15 V. This integrated system achieves a maximum efficiency of 70%. The total integrated capacitance is 1.6 nF and the ripple of the output voltage varies from 30 mV to 93 mV. The entire system is built up using transfer capacitors NMOS capacitor as the load capacitor CMOS switches clocked comparators various logic gates with driver cells a clock generator and voltage dividers. The load current is fixed at 3 mA. To achieve low line regulation three switching networks 2-1-5-2 and 3-1 are implemented in the system. A multi layer technique is applied to the layout of the transfer capacitors that helps to reduce parasitic capacitances thereby improving converter efficiency and achieve smaller size.

Modeling and Analysis of Four-switch Buck/boost Converter with Improved Peak Virtual Inductor Current Control [] [],2022

Ultra-efficient Cascaded Buck-boost Converter Anirudh Ashok Pise,2017

This thesis presents various techniques to achieve ultra high efficiency for Cascaded Buck Boost converter. A rigorous loss model with component nonlinearities is developed and validated experimentally. An adaptive switching frequency control is discussed to optimize weighted efficiency. Some soft switching techniques are discussed. A low profile planar nanocrystalline inductor is developed.

and various design aspects of core and copper design are discussed. Finite element method is used to examine and visualize the inductor design. By implementing the above, a peak efficiency of over 99.2% is achieved with a power density of 6 kW/L and a maximum profile height of 7 mm is reported. This converter finds many applications because of its versatility, allowing bidirectional power flow and the ability to step up or step down voltages in either direction.

A Dual Supply Buck Converter with Improved Light Load Efficiency Hui Chen, 2013. Power consumption is the primary concern in battery operated portable applications. Buck converters have gained popularity in powering portable devices due to their compact size, good current delivery capability, and high efficiency. However, portable devices are operating under light load condition for the most of the time. Conventional buck converters suffer from low light load efficiency, which severely limits battery lifetime. In this project, a novel technique for buck converter is proposed to reduce the switching loss by reducing the effective input supply voltage at light load. This is achieved by switching between two different input voltages, 3.3V and 1.65V, depending on the output current value. Experimental results show that this technique improves the efficiency at light loads by 18.07%. The buck converter possesses an output voltage of 0.9V and provides a maximum output current of 400mA. The buck converter operates at a switching frequency of 1MHz. The prototype was fabricated using 0.18 μm CMOS technology and occupies a total active area of 0.6039mm². The electronic version of this dissertation is accessible from <http://hdl.handle.net/1969.1/149497>.

A Dual-supply Buck Converter with Improved Light-load Efficiency Chao Zhang, 2012. Power consumption and device size have been placed at the primary concerns for battery operated portable applications. Switching converters gain popularity in powering portable devices due to their high efficiency, compact sizes, and high current delivery capability. However, portable devices usually operate at light loads most of the time and are only required to deliver high current in very short periods, while conventional buck converter suffers from low efficiency at light load due to the switching losses that do not scale with load current. In this research, a novel technique for buck converter is proposed to reduce the switching loss by reducing the effective voltage supply at light load. This buck converter implemented in TSMC 0.18 micrometers CMOS technology operates with an input voltage of 3.3V and generates an output voltage of 0.9V, delivers a load current from 1mA to 400mA, and achieves 54%–91% power efficiency. It is designed to work with a constant switching frequency of 3MHz. Without sacrificing output frequency spectrum or output ripple, an efficiency improvement of up to 20 percent is obtained at light load.

Digital Controlled Multi-phase Buck Converter with Accurate Voltage and Current Control Ming Sun, 2017. A 4-phase quasi-current mode hysteretic buck converter with digital frequency synchronization, online comparator offset calibration, and digital current sharing control is presented. The switching frequency of the hysteretic converter is digitally synchronized to the input clock reference with less than 1.5% error in the switching frequency range of 3.95MHz. The online offset calibration cancels the input-referred offset of the hysteretic comparator and enables 1.1% voltage regulation accuracy. Maximum current sharing error of 3.6% is achieved by a duty cycle calibrated

delay line based PWM generator without affecting the phase synchronization timing sequence In light load conditions individual converter phases can be disabled and the final stage power converter output stage is segmented for high efficiency The DC DC converter achieves 93% peak efficiency for V_i 2V and V_o 1.6V

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