



STEVE FURBER

**ARM SYSTEM-
ON-CHIP
ARCHITECTURE**

Arm System On Chip Architecture 2nd Edition

Yan Bai



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Low Power Design Automation David Graeme Chinnery,2006 **CASES 2004** Mary Jane Irwin,2004 *CODES+ISSS* ,2005 **Journal of VLSI Signal Processing Systems for Signal, Image, and Video Technology** ,1998 [Algorithm Engineering](#) ,2001 **The Symbian OS Architecture Sourcebook** Ben Morris,2007-07-16 Provides a technical introduction for the technical decision makers seeking to evaluate and understand Symbian OS The book will include a substantial reference section itemising the OS and its toolkit at component level and providing a reference entry for each component *Book Review Index* ,2003 Vols 8 10 of the 1965 1984 master cumulation constitute a title index **American Book Publishing Record** ,2000-07 *ACM SIGPLAN Notices* ,2006-07 [ARM System Developer's Guide](#) Andrew Sloss,Dominic Symes,Chris Wright,2004-05-10 Over the last ten years the ARM architecture has become one of the most pervasive architectures in the world with more than 2 billion ARM based processors embedded in products ranging from cell phones to automotive braking systems A world wide community of ARM developers in semiconductor and product design

companies includes software developers system designers and hardware engineers To date no book has directly addressed their need to develop the system and software for an ARM based system This text fills that gap This book provides a comprehensive description of the operation of the ARM core from a developer s perspective with a clear emphasis on software It demonstrates not only how to write efficient ARM software in C and assembly but also how to optimize code Example code throughout the book can be integrated into commercial products or used as templates to enable quick creation of productive software The book covers both the ARM and Thumb instruction sets covers Intel s XScale Processors outlines distinctions among the versions of the ARM architecture demonstrates how to implement DSP algorithms explains exception and interrupt handling describes the cache technologies that surround the ARM cores as well as the most efficient memory management techniques A final chapter looks forward to the future of the ARM architecture considering ARMv6 the latest change to the instruction set which has been designed to improve the DSP and media processing capabilities of the architecture No other book describes the ARM core from a system and software perspective Author team combines extensive ARM software engineering experience with an in depth knowledge of ARM developer needs Practical executable code is fully explained in the book and available on the publisher s Website Includes a simple embedded operating system **Dedicated Digital Processors** F. Mayer-Lindenberg,2004-02-13 Publisher Description **F&S Index United States Annual ,1999**
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