

**B.Sc. (Pass Course) 1st Semester Examination,
February-2022**

COMPUTER SCIENCE

Paper-1.2

Computer Architecture (2012-13)

Time allowed : 3 hours]

[Maximum marks : 40

Note : Attempt five questions in all, selecting one question from each unit. Question No. 1 is compulsory.

1. Write short note on the following :

निम्नलिखित पर संक्षिप्त टिप्पणी लिखिए :

(a) ~~Simplifying logic circuits ... sum of product.~~

तर्क सर्किट को सरल बनाना उत्पाद का योग।

(b) Multiplexers

मल्टीप्लेक्सर्स

(c) JK and D flip-flop with truth table

सत्य तालिका के साथ JK और D फ्लिप-फ्लॉप

(d) Micro-instructions

माइक्रो-निर्देश

Computer Architecture Exam Paper

Tobias Bleicker



Computer Architecture Exam Paper:

Exam Questions and Answers Kaplan AEC Education,2004 This is one of the most popular books we have ever published It consists of over 200 simulated examination questions covering every aspect of architecture and is arranged alphabetically by subject The questions are presented in the multiple choice format and a complete explanation and analysis of each answer is included Also included are a discussion of question types exam strategy and other helpful information

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These papers cover all core topics including meteorology atmospheric physics climatology weather systems and climate change Each paper includes clear explanations to strengthen concept clarity analytical skills and exam accuracy making this resource ideal for CUET PG Atmospheric Science entrance exam preparation *Architecture Exam Review* David Kent Ballast,1992 [Computer Architecture MCQs](#) Arshad Iqbal,2019-06-14 Computer Architecture Multiple Choice Questions and Answers MCQs Computer architecture quiz questions and answers with practice tests for online exam prep and job interview prep Computer architecture study guide with questions and answers about assessing computer performance computer architecture and organization computer arithmetic computer language and instructions computer memory review computer technology data level parallelism and GPU architecture embedded systems exploiting memory instruction level parallelism instruction set principles interconnection networks memory hierarchy design networks storage and peripherals pipe lining in computer architecture pipe lining performance processor datapath and control quantitative design and analysis request level and data level parallelism storage systems thread level parallelism Computer architecture trivia questions and answers to get prepare for career placement tests and job interview prep with answers key Practice exam questions and answers about computer science composed from computer architecture textbooks on chapters Assessing Computer Performance Practice Test 13 MCQs Computer Architecture and Organization Practice Test 19 MCQs Computer Arithmetic Practice Test 33 MCQs Computer Language and Instructions Practice Test 52 MCQs Computer Memory Review Practice Test 66 MCQs Computer Technology Practice Test 14 MCQs Data Level Parallelism and GPU Architecture Practice Test 38 MCQs Embedded Systems Practice Test 21 MCQs Exploiting Memory Practice Test 29 MCQs Instruction Level Parallelism Practice Test 52 MCQs Instruction Set Principles Practice Test 30 MCQs Interconnection Networks Practice Test 56 MCQs Memory Hierarchy Design Practice Test 37 MCQs Networks Storage and Peripherals Practice Test 20 MCQs Pipelining in Computer Architecture Practice Test 56 MCQs Pipelining Performance Practice Test 15 MCQs Processor Datapath and Control Practice Test 21 MCQs Quantitative Design and Analysis Practice Test 49 MCQs Request Level and Data Level Parallelism Practice Test 32 MCQs Storage Systems Practice Test 43 MCQs Thread Level Parallelism Practice Test 37 MCQs Computer architecture interview questions and answers on 32 bits MIPS addressing addition and subtraction advanced branch prediction advanced techniques and speculation architectural design vectors architecture and networks arrays and pointers basic cache optimization methods basic compiler techniques cache optimization techniques cache performance optimizations caches and cache types caches performance case study sanyo vpc sx500 camera Computer architecture test questions and answers on cloud computing compiler optimization computer architecture computer architecture memory hierarchy computer code computer hardware operands computer hardware operations computer hardware procedures computer instructions and languages computer instructions representations computer networking computer organization computer systems virtual memory computer types cost trends and analysis Computer architecture exam questions and answers on CPU

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Knowledge Science, Engineering and Management Cungeng Cao,Huajun Chen,Liang Zhao,Junaid Arshad,Taufiq Asyhari,Yonghao Wang,2024-07-26 The five volume set LNCS 14884 14885 14886 14887 14888 constitutes the refereed deadline proceedings of the 17th International Conference on Knowledge Science Engineering and Management KSEM 2024 held in Birmingham UK during August 16 18 2024 The 160 full papers presented in these proceedings were carefully reviewed and selected from 495 submissions The papers are organized in the following topical sections Volume I Knowledge Science with Learning and AI KSLA Volume II Knowledge Engineering Research and Applications KERA Volume III Knowledge Management with Optimization and Security KMOS Volume IV Emerging Technology Volume V Special Tracks

ECEL2003-Proceedings of the 2nd European Conference on eLearning - 2003 Roy Williams,2003 *Architecture Exam Review: Nonstructural topics* David Kent Ballast,1998 *Auditing & systems : exam questions and explanations* Irvin N. Gleim,William A. Hillison,2005 *Architecture Exam Review* Steven E. O'Hara,David Kent Ballast,2005 Volume II Nonstructural Topics has been updated to reflect recent exam changes and extensively revised including the addition of 41 new sample questions The book offers a thorough review of the Are nonstructural exam topics including pre design mechanical and electrical systems building design materials and methods energy efficiency and alternative energy sources and sustainable design More than 200 sample questions are provided with solutions along with a valuable checklist of items that should be considered in every design solution This volume also covers test taking strategy and is enhanced by illustrations figures and tables along with a detailed index

Top Productivity through Software Reuse Klaus Schmid,2011-06-16 This book constitutes the refereed proceedings of the 12th International Conference on Software Reuse ICSR 2011 held in Pohang South Korea in June 2011 The 16 revised full papers were carefully reviewed and selected from 43 submissions They are presented together with one keynote three workshop papers a doctoral symposium report and two tutorials Topics of interest are domain analysis and modeling asset search and retrieval architecture centric approaches to reuse component based reuse COTS based development generator based techniques domain specific languages testing in the context of software reuse aspect oriented techniques model driven development reuse of non code artifacts reengineering for reuse software product line techniques quality aspects of reuse economic models of reuse benefit and risk analysis scoping legal and managerial aspects of reuse transition to software reuse industrial experience with reuse light weight approaches software evolution and reuse

Architecture Exam Review Holly

Williams Leppo,2007 Realistic practice exams for the 4 nonstructural divisions of the ARE simulate the actual exams through the format number of questions time limit and topics covered

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