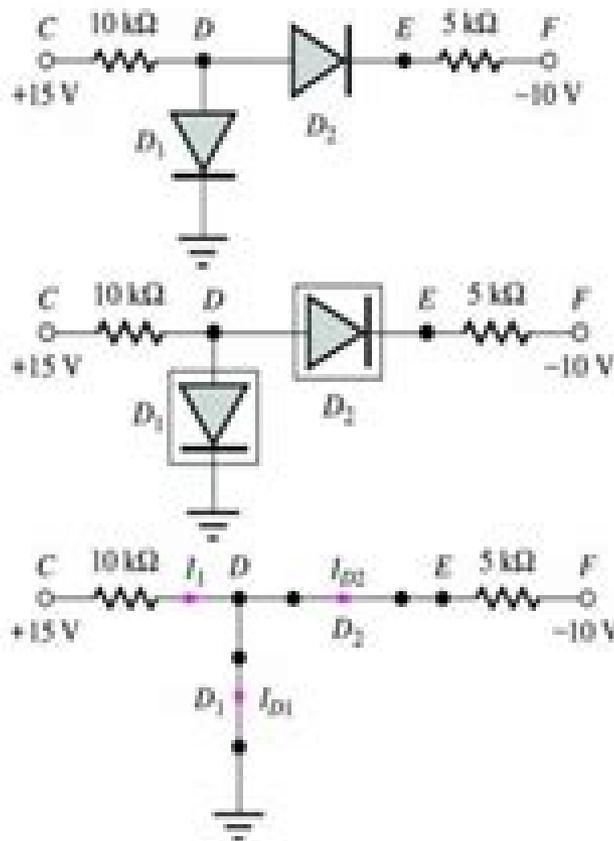


# Two-Diode Circuit Analysis



**Analysis:** Ideal diode model is chosen. Since 15V source is forcing positive current through  $D_1$  and  $D_2$  and -10V source is forcing positive current through  $D_2$ , assume both diodes are on.

Since voltage at node  $D$  is zero due to short circuit of ideal diode  $D_1$ ,

$$I_1 = \frac{(15 - 0)V}{10k\Omega} = 1.50\text{mA} \quad I_{D2} = \frac{0 - (-10)V}{5k\Omega} = 2.00\text{mA}$$

$$I_1 = I_{D1} + I_{D2} \quad \therefore I_{D1} = 1.5 - 2 = -0.50\text{mA}$$

Q-points are (-0.5 mA, 0 V) and (2.0 mA, 0 V)

But,  $I_{D1} < 0$  is not allowed by diode, so ?

# Diode Circuit Analysis

**Arturo Cuomo**



## Diode Circuit Analysis:

## Decoding **Diode Circuit Analysis**: Revealing the Captivating Potential of Verbal Expression

In a period characterized by interconnectedness and an insatiable thirst for knowledge, the captivating potential of verbal expression has emerged as a formidable force. Its power to evoke sentiments, stimulate introspection, and incite profound transformations is genuinely awe-inspiring. Within the pages of "**Diode Circuit Analysis**," a mesmerizing literary creation penned by a celebrated wordsmith, readers set about an enlightening odyssey, unraveling the intricate significance of language and its enduring effect on our lives. In this appraisal, we shall explore the book's central themes, evaluate its distinctive writing style, and gauge its pervasive influence on the hearts and minds of its readership.

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