

Digital Logic Design - Asynchronous Counter

③ 状态转换表



① 输入方程

$$J_1 = K_1 = 1 \quad CP_1 \downarrow$$

$$J_2 = K_2 = 1 \quad CP_2 = Y_1 \downarrow$$

$$J_3 = K_3 = 1 \quad CP_3 = Y_2 \downarrow$$

异步Moor
型时序电路

异步模8加
法计数器

现态			次态			时钟		
Y_3^n	Y_2^n	Y_1^n	Y_3^{n+1}	Y_2^{n+1}	Y_1^{n+1}	CP_3	CP_2	CP_1
0	0	0	0	0	1	无	无	↓
0	0	1	0	1	0	无	↓	↓
0	1	0	0	1	1	无	无	↓
0	1	1	1	0	0	↓	↓	↓
1	0	0	1	0	1	无	无	↓
1	0	1	1	1	0	无	↓	↓
1	1	0	1	1	1	无	无	↓
1	1	1	0	0	0	↓	↓	↓

② 次态方程

$$Y_1^{n+1} = J_1 \bar{Q}_1 + \bar{K}_1 Q_1 = \bar{Y}_1 \quad CP_1 \downarrow$$

$$Y_2^{n+1} = J_2 \bar{Q}_2 + \bar{K}_2 Q_2 = \bar{Y}_2 \quad Y_1 \downarrow$$

$$Y_3^{n+1} = J_3 \bar{Q}_3 + \bar{K}_3 Q_3 = \bar{Y}_3 \quad Y_2 \downarrow$$

④ 波形图

二分频



Digital Logic Design And Application

Albert A Gayle



Digital Logic Design And Application:

Digital Logic Susan A. R. Garrod, Robert J. Borns, 1991-01-01 **Digital Logic** John M. Yarbrough, 1997 DIGITAL LOGIC offers the right balance of classical and up to date treatment of combinational and sequential logic design for a first digital logic design class The author provides a thorough explanation of the design process including completely worked examples beginning with simple examples and going on to problems of increasing complexity This text contains PLD Programmable Logic Design coverage Chapter 9 develops complete worked EPROM PLA and EPLD design examples The problems are developed in Chapter 7 as standard designs using SSI and MSI devices so that your students can see the difference between the two approaches Digital Logic Susan A. Garrod, Robert J. Borns, 1991-01-01 Application Of Digital Electronics Edelmira Thimmesch, 2021-03-28 This book is your beginner s guide to simple logic programming Digital design is based on the binary principle where everything is either 0 or 1 either low or high Few people realize that digital logic existed before the advent of the computer Digital logic was used for control and communications systems even before semiconductors were invented They worked using switches relays and solenoids If you search the internet you will not find the phrase digital logic separate from computers It s like the only purpose for digital logic was to invent the computer

Digital Logic Design Guy Even, Moti Medina, 2012-10-08 This textbook based on the authors fifteen years of teaching is a complete teaching tool for turning students into logic designers in one semester Each chapter describes new concepts giving extensive applications and examples Assuming no prior knowledge of discrete mathematics the authors introduce all background in propositional logic asymptotics graphs hardware and electronics Important features of the presentation are All material is presented in full detail Every designed circuit is formally specified and implemented the correctness of the implementation is proved and the cost and delay are analyzed Algorithmic solutions are offered for logical simulation computation of propagation delay and minimum clock period Connections are drawn from the physical analog world to the digital abstraction The language of graphs is used to describe formulas and circuits Hundreds of figures examples and exercises enhance understanding The extensive website <http://www.eng.tau.ac.il/guyEven/Medina> includes teaching slides links to Logisim and a DLX assembly simulator *Digital Logic Design* Ming-Bo Lin, 2021-01-11 Digital Logic Design is a comprehensive textbook which aims to provide entrylevel readers a quick start to the field of digital logic design so as to facilitate them with the capability suitable for the versatility of social change and interdisciplinary learning This textbook can be used as a textbook for classroom use in the fields of electronics electrical computer science information engineering mechanical and soon The salient features of this textbook are as follows 1 Introduce incrementally the principles of digital logic design and exemplify each basic theme and concept with abundant illustrations 2 Detail design principles of various combinational modules including decoders encoders multiplexers demultiplexers arithmetic circuits and so on 3 Introduce design principles of various sequential modules including counters registers shift registers sequence generators etc 4

Address the structures features and applications of PLD FPGA devices 5 Exemplify applications of CPLD FPGA devices with Verilog HDL modules 6 Provide 20 basic and application experiments of digital logic to help readers verify the consistence of digital logic between principles and practice 7 Include an abundance of review questions in each section to help readers evaluate their understandings about the section 8 Deal with Verilog HDL concisely in relevant sections so as to make the reader understand how to describe a logic circuit in Verilog HDL precisely Digital Logic Design is an ideal textbook for the digital logic design course in the fields of electronics electrical computer science information engineering mechanical etc or serves as a valuable reference book for self study

Digital Logic: Applications And Design , **Digital Logic** Susan A. R. Garrod, Robert J. Borns, 1996-08-01

Digital Logic Design and Applications Lyle B. McCurdy, Albert L. McHenry, 1981-01-01

Digital Principles and Logic Design Arijit Saha, Nilotpal Manna, 2009-01-28 This text and reference provides students and practicing engineers with an introduction to the classical methods of designing electrical circuits but incorporates modern logic design techniques used in the latest microprocessors microcontrollers microcomputers and various LSI components The book provides a review of the classical methods e g the basic concepts of Boolean algebra combinational logic and sequential logic procedures before engaging in the practical design approach and the use of computer aided tools The book is enriched with numerous examples and their solutions over 500 illustrations and includes a CD ROM with simulations additional figures and third party software to illustrate the concepts discussed in the book

Digital Logic Design Brian Holdsworth, 1987 New updated and expanded topics in the fourth edition include EBCDIC Grey code practical applications of flip flops linear and shaft encoders memory elements and FPGAs The section on fault finding has been expanded A new chapter is dedicated to the interface between digital components and analog voltages A highly accessible comprehensive and fully up to date digital systems text A well known and respected text now revamped for current courses Part of the Newnes suite of texts for HND 1st year modules

Electrical and Computer Engineering Rajiv Kapadia, 2015-06-19 An excellent introduction to the digital world in engineering Introduction to Digital Logic Design explains the simple concepts behind digital logic design from logic gates all the way to the design of sequential machines Over the course of the eight chapters of the book students explore number systems and codes simple logic states boolean algebra working with logic equations and simplifying logic functions They also work with arithmetic in binary systems common combinational logic functions counters and sequential logic Each chapter includes practical problems that allow for immediate application of the skills and concepts All material is based on extensive class testing Simple yet rigorous Introduction to Digital Logic Design helps first semester students see the big picture in logic design and doesn't overwhelm them with extraneous details The text is suitable for first year engineering computer science and information science courses

Fundamentals of Digital Logic with VHDL Design Stephen Brown, Zvonko Vranesic, 2022 Fundamentals of Digital Logic with VHDL Design is intended for an introductory course in digital logic design which is a basic course in most electrical and computer engineering programs A successful

designer of digital logic circuits needs a good understanding of the classical methods of logic design and a firm grasp of the modern design approach that relies on computer aided design CAD tools The main goals of this book are to teach students the fundamental concepts of classical manual digital design and to illustrate clearly the way in which digital circuits are designed today using CAD tools This title will be available in Connect with the MHeBook but will not have SmartBook at this time

Foundations of Computer Science and Engineering: Theory, Design, and Applications Dr. Abhishek Kumar, 2025-07-20 *Digital Logic Design*, 2012 **Field-Programmable Logic: Architectures, Synthesis and Applications** Reiner W. Hartenstein, 1994-08-24 This volume contains the proceedings of the 4th International Workshop on Field Programmable Logic and Applications FPL 94 held in Prague Czech Republic in September 1994 The growing importance of field programmable devices is substantiated by the remarkably high number of 116 submissions for FPL 94 from them the revised versions of 40 full papers and 24 high quality poster presentations were accepted for inclusion in this volume Among the topics treated are testing layout synthesis tools compilation research and CAD trade offs and experience innovations and smart applications FPGA based computer architectures high level design prototyping and ASIC emulators commercial devices new tools CCMs and HW SW co design modelers educational experience and novel architectures

Application-Specific Hardware Architecture Design with VHDL Bogdan Belean, 2017-10-17 This book guides readers through the design of hardware architectures using VHDL for digital communication and image processing applications that require performance computing Further it includes the description of all the VHDL related notions such as language levels of abstraction combinational vs sequential logic structural and behavioral description digital circuit design and finite state machines It also includes numerous examples to make the concepts presented in text more easily understandable

Fundamentals of Digital Logic Design, with VLSI Applications Douglas A. Pucknell, 1990 Hardware Logic Design

Fundamentals of Digital Logic with VHDL Design with CD-ROM Stephen Brown, Zvonko Vranesic, 2008-04-14 Fundamentals of Digital Logic with VHDL Design teaches the basic design techniques for logic circuits The text provides a clear and easily understandable discussion of logic circuit design without the use of unnecessary formalism It emphasizes the synthesis of circuits and explains how circuits are implemented in real chips Fundamental concepts are illustrated by using small examples which are easy to understand Then a modular approach is used to show how larger circuits are designed VHDL is a complex language so it is introduced gradually in the book Each VHDL feature is presented as it becomes pertinent for the circuits being discussed While it includes a discussion of VHDL the book provides thorough coverage of the fundamental concepts of logic circuit design independent of the use of VHDL and CAD tools A CD ROM containing all of the VHDL design examples used in the book as well Altera's Quartus II CAD software is included free with every text **Smart Computing** Mohammad Ayoub Khan, Sanjay Gairola, Bhola Jha, Pushkar Praveen, 2021-06-22 The field of SMART technologies is an interdependent discipline It involves the latest burning issues ranging from machine learning cloud computing

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