

General Purpose Transistors

NPN Silicon

FEATURES

- High current capacity in compact package
- h_{FE} of 100
- Epitaxial power type
- JEDEC compliance: TO18
- Pin-Trim Package is available
- ICL: Suitable for Automotive and Other Applications Requiring Integer Size and Control Change Requirements. AEC-Q101 Qualified and PPAP Capable

DEVICE MARKING AND ORDERING INFORMATION

Device	Marking	Shipping
L8050H01TIG	S-L8050H01TIG	1000Tape and Reel
L8050H02TIG	S-L8050H02TIG	1000Tape and Reel
L8050H03TIG	S-L8050H03TIG	1000Tape and Reel
L8050H04TIG	S-L8050H04TIG	1000Tape and Reel
L8050H05TIG	S-L8050H05TIG	1000Tape and Reel
L8050H06TIG	S-L8050H06TIG	1000Tape and Reel
L8050H07TIG	S-L8050H07TIG	1000Tape and Reel
L8050H08TIG	S-L8050H08TIG	1000Tape and Reel

L8050H01TIG Series
S-L8050H01TIG Series



8050-01



MAXIMUM RATINGS

Rating	Symbol	Min	Max
Collector-Emitter Voltage	V _{CE}	25	V
Collector-Base Voltage	V _{CB}	40	V
Collector-Base Voltage	V _{EB}	5	V
Collector Current	I _C	1000	mA

TERMINAL CHARACTERISTICS

Characterization	Symbol	Min	Max
Total Device Dissipation (TA = 25°C) ¹ Ta=25°C Duty cycle above 25°C	P _D	200	400 mW
Thermal Resistance, Junction to Ambient	R _{θJA}	50	°C/W
Total Device Dissipation (Ambient Temperature, TA = 25°C) Duty cycle above 25°C	P _D	500	1000 mW
Thermal Resistance, Junction to Ambient (Ambient and Device Temperature)	R _{θJA}	41.1	°C/W
Junction and Storage Temperature	T _J , T _{stg}	-55 to +150	°C

1. P_D = I_C V_{CE} or I_C V_{BE} or I_C V_{CE}.

2. Ambient = 25°C ± 0.5°C ± 0.05°C or 25°C ± 0.5°C.

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Roman Wölfel



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