

Comprehensive USB 3.1 Test Suite



Key Features

- **Single vendor solution for the full range of USB 3.1 tests**
- **Fully automated Transmitter and Receiver testing**
- **Transmitter Testing**
 - Support for Gen1 and Gen2 DUTs
 - Automated compliance pattern (CP) change with PeRT³
 - Seamless SigTest integration
- **Receiver Testing**
 - Automated Rx calibration
 - True protocol handshake support
 - Jitter Tolerance Testing
 - USB 3.1 Compliance Patterns
- **Protocol Testing**
 - Multi-function analyzer system with integrated USB 3.1 exerciser
 - Support for Type-C, USB 3.1, and Power Delivery 2.0
 - T.A.P.3[™] probing for fast signal lock, seamless state change detection and accurate LFPS detection
- **Cable and Connector Testing**
 - S-parameters and TDR testing
 - Insertion loss, crosstalk, impedance measurements
 - Measures from DC up to 40 GHz

Teledyne LeCroy is the only vendor which offers a full line of USB 3.1 test solutions for compliance, characterization, and debug. Transmitter and receiver tests are covered in one automated physical layer test package, QPHY-USB3.1-Tx-Rx, enabling streamlined testing. The M310C protocol analyzer can test the link layer for protocol compliance. Cables and connectors can be quickly characterized using the SPARQ Series Network Analyzer for S-parameter and TDR testing. Teledyne LeCroy provides the right solution for every stage of USB 3.1 development.

Transmitter Testing

QPHY-USB3-Tx-Rx offers a fully automated compliance test package for USB 3.1 transmitter compliance testing. Tests can be performed using the latest SigTest software for official compliance results. Alternatively, tests can be run using SDAIII to provide the ideal interface for debugging any USB 3.1 failures.

Receiver Testing

The PeRT³ (Protocol-enabled Receiver and Transmitter Tolerance Tester) fills the space between physical layer test and protocol test, providing a new and more intelligent capability for performance testing of receivers and transmitters.

Protocol Testing

Supporting the Type-C, 3.1 and Power Delivery 2.0 specifications, the Voyager USB analyzer platform provides the industry's most accurate capture of the USB protocol. This multifunction validation platform is available with an integrated exerciser providing host and device emulation.

Cable and Connector Testing

The SPARQ Series Network Analyzer can easily make all frequency and time domain measurements required for USB 3.1 cable and connector testing. The SPARQ performs S-parameter measurements at a fraction of the cost of a VNA and utilizes a built-in calibration kit to make all measurements quickly and easily.

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Hussin A.Rothana



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Low-Power Processors and Systems on Chips Christian Piguet,2018-10-03 The power consumption of microprocessors is one of the most important challenges of high performance chips and portable devices In chapters drawn from Piguet s recently published *Low Power Electronics Design* this volume addresses the design of low power microprocessors in deep submicron technologies It provides a focused reference for specialists involved in systems on chips from low power microprocessors to DSP cores reconfigurable processors memories ad hoc networks and embedded software *Low Power Processors and Systems on Chips* is organized into three broad sections for convenient access The first section examines the design of digital signal processors for embedded applications and techniques for reducing dynamic and static power at the electrical and system levels The second part describes several aspects of low power systems on chips including hardware and embedded software aspects efficient data storage networks on chips and applications such as routing strategies in wireless RF sensing and actuating devices The final section discusses embedded software issues including details on compilers retargetable compilers and coverification tools Providing detailed examinations contributed by leading experts *Low Power Processors and Systems on Chips* supplies authoritative information on how to maintain high performance while lowering power consumption in modern processors and SoCs It is a must read for anyone designing modern computers or embedded systems

14th Symposium on Integrated Circuits and Systems Design Sociedade Brasileira de Computação,2001 Annotation Papers from a September 2001 symposium report on recent advances in areas of integrated circuits and systems design including embedded systems rapid prototyping formal methods codesign CAD and test analog digital and physical design and low power and low voltage Specific topics include communication architectures for system on chip using the CAN protocol and reconfigurable computing technology for Web based smart house automation and optimizing BDD based verification analyzing variable dependencies Other subjects include interconnection length estimation at logic level an environment to aid the synthesis of threephase analogue waveform using AHDL and extending sequencing graphs for reconfigurable applications modeling This work lacks a subject index c Book News Inc **HWM** ,2006-06

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USB Design by Example John Hyde,2001 This unique guide goes beyond all the USB specification overviews to provide designers with the expert knowledge and skills they need to design and implement USB I O devices HWM ,2007-09

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Syed A. Ahson,Mohammad Ilyas,2010-12-07 From basic concepts to research grade material *Mobile Web 2 0 Developing and Delivering Services to Mobile Devices* provides complete and up to date coverage of the range of technical topics related to

Mobile Web 2.0 It brings together the work of 51 pioneering experts from around the world who identify the major challenges in Mobile Web 2.0 appl *InfoWorld* ,1995-03-27 InfoWorld is targeted to Senior IT professionals Content is segmented into Channels and Topic Centers InfoWorld also celebrates people companies and projects **Government Reports Announcements & Index** ,1991 **Dataquest** ,1999-07 *The Photonics Directory* , **Electronic Design** ,2002 **The Wireless World** ,1980 **Jane's Military Communications** ,1987 Includes index Ham Radio ,1987

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Table of Contents Comprehensive Usb 3 1 Test Suite Datasheet

1. Understanding the eBook Comprehensive Usb 3 1 Test Suite Datasheet
 - The Rise of Digital Reading Comprehensive Usb 3 1 Test Suite Datasheet
 - Advantages of eBooks Over Traditional Books
2. Identifying Comprehensive Usb 3 1 Test Suite Datasheet
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Comprehensive Usb 3 1 Test Suite Datasheet
 - User-Friendly Interface
4. Exploring eBook Recommendations from Comprehensive Usb 3 1 Test Suite Datasheet

- Personalized Recommendations
 - Comprehensive Usb 3 1 Test Suite Datasheet User Reviews and Ratings
 - Comprehensive Usb 3 1 Test Suite Datasheet and Bestseller Lists
5. Accessing Comprehensive Usb 3 1 Test Suite Datasheet Free and Paid eBooks
- Comprehensive Usb 3 1 Test Suite Datasheet Public Domain eBooks
 - Comprehensive Usb 3 1 Test Suite Datasheet eBook Subscription Services
 - Comprehensive Usb 3 1 Test Suite Datasheet Budget-Friendly Options
6. Navigating Comprehensive Usb 3 1 Test Suite Datasheet eBook Formats
- ePub, PDF, MOBI, and More
 - Comprehensive Usb 3 1 Test Suite Datasheet Compatibility with Devices
 - Comprehensive Usb 3 1 Test Suite Datasheet Enhanced eBook Features
7. Enhancing Your Reading Experience
- Adjustable Fonts and Text Sizes of Comprehensive Usb 3 1 Test Suite Datasheet
 - Highlighting and Note-Taking Comprehensive Usb 3 1 Test Suite Datasheet
 - Interactive Elements Comprehensive Usb 3 1 Test Suite Datasheet
8. Staying Engaged with Comprehensive Usb 3 1 Test Suite Datasheet
- Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Comprehensive Usb 3 1 Test Suite Datasheet
9. Balancing eBooks and Physical Books Comprehensive Usb 3 1 Test Suite Datasheet
- Benefits of a Digital Library
 - Creating a Diverse Reading Collection Comprehensive Usb 3 1 Test Suite Datasheet
10. Overcoming Reading Challenges
- Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
11. Cultivating a Reading Routine Comprehensive Usb 3 1 Test Suite Datasheet
- Setting Reading Goals Comprehensive Usb 3 1 Test Suite Datasheet
 - Carving Out Dedicated Reading Time
12. Sourcing Reliable Information of Comprehensive Usb 3 1 Test Suite Datasheet

- Fact-Checking eBook Content of Comprehensive Usb 3 1 Test Suite Datasheet
 - Distinguishing Credible Sources
13. Promoting Lifelong Learning
- Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
14. Embracing eBook Trends
- Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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