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directly addressed their need to develop the system and software for an ARM based system This text fills that gap This book provides a comprehensive description of the operation of the ARM core from a developer s perspective with a clear emphasis on software It demonstrates not only how to write efficient ARM software in C and assembly but also how to optimize code Example code throughout the book can be integrated into commercial products or used as templates to enable quick creation of productive software The book covers both the ARM and Thumb instruction sets covers Intel s XScale Processors outlines distinctions among the versions of the ARM architecture demonstrates how to implement DSP algorithms explains exception and interrupt handling describes the cache technologies that surround the ARM cores as well as the most efficient memory management techniques A final chapter looks forward to the future of the ARM architecture considering ARMv6 the latest change to the instruction set which has been designed to improve the DSP and media processing capabilities of the architecture No other book describes the ARM core from a system and software perspective Author team combines extensive ARM software engineering experience with an in depth knowledge of ARM developer needs Practical executable code is fully explained in the book and available on the publisher s Website Includes a simple embedded operating system   *American Book Publishing Record* ,2000-07   ACM SIGPLAN Notices ,2006-07   **Dedicated Digital Processors** F. Mayer-Lindenberg,2004-02-13 Publisher Description   *ARM Architecture* Guruprasad PV,2015-05-21 ARM Architecture is designed in such a way that even readers with a basic understanding of microprocessors can learn and understand the concepts with ease This book addresses the concerns of software developers system designers and hardware engineers focusing on development of drivers software and hardware components in a closely knit fashion to avoid compatibility issues ARM Architecture guides engineers through the workings of an ARM in system on chip SOC via an example driven systematic process It also elucidates on data flow between peripherals and ARM in SOC drivers and the programming of hardware peripherals through multiple illustrations The book teaches the reader programming the ARM chip in both C and assembly language with the help of multiple examples It helps the reader learn peripheral programming which is necessary for firmware development for embedded systems and driver development It also teaches the reader how to program a system on chip ARM Architecture does far more than just outline the ARM features it explains the implementation of processor in real world designs through a step by step process and is beneficial to people who are migrating from 8 bit micro controllers to 32 bit micro controllers and covers the ARM family extensively elucidating on hardware blocks like SDRAM RAM etc   **F&S Index United States Annual** ,1999

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