

FPGA Simulation

A Complete
Step-by-Step
Guide

Functional Coverage

Automatic Stimulus

Self-Checking Test Bench

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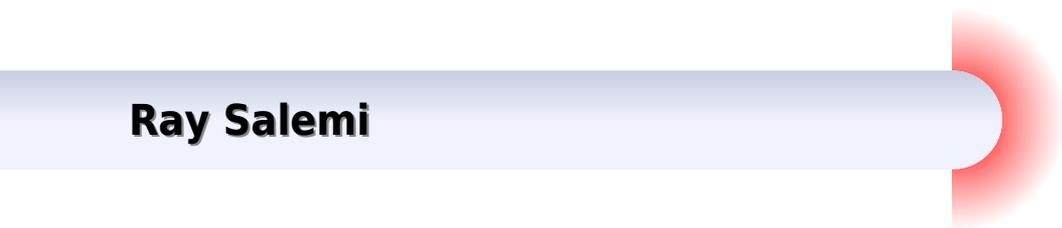
Test Planning

Code Coverage

Ray Salemi

Fpga Simulation A Complete Step By Step Guide By Ray Salemi

Ray Salemi



Fpga Simulation A Complete Step By Step Guide By Ray Salemi:

FPGA Simulation Ray Salemi,2009 FPGA Simulation A Complete Step by Step Guide shows FPGA design engineers how to avoid long lab debug sessions by simulating with SystemVerilog The book helps engineers to have never simulated their designs before by bringing them through seven steps that can be added incrementally to a design flow Engineers start with code coverage as the first step Succeeding steps introduce test planning assertions and SystemVerilog simulation techniques By the end of the process engineers who have never simulated before will know how to create complete self checking test benches that generate their own stimulus and demonstrate complete functional coverage This book is a must for engineers who are facing DO 254 certification requirements on their next FPGA project *A Practical Guide for Simulation and FPGA Implementation of Digital Design* Bekkay Hajji,Adel Mellit,Loubna Bouselham,2022-03-21 This book introduces the FPGA technology used in the laboratory sessions and provides a step by step guide for designing and simulation of digital circuits It utilizes the VHDL language which is one of the most common language used to describe the design of digital systems The Quartus II Xilinx ISE 14 7 and ModelSim software are used to process the VHDL code and make simulations and then the Altera and Xilinx FPGA platforms are employed to implement the simulated digital designs The book is composed of four parts The first part of this book has two chapters and covers various aspects FPGA architectures ASIC vs FPGA comparison FPGA design flow and basic VHDL concepts necessary to describe the design of digital systems The second part of the book includes three chapters that deal with the design of digital circuits such as combinational logic circuits sequential logic circuits and finite state machines The third part of the book is reserved for laboratory projects carried out on the FPGA platform It is a largely hands on lab class for design digital circuits and implementing their designs on the Altera FPGA platform Finally the fourth part of this work is devoted to recent applications carried out on FPGAs in particular advanced techniques in renewable energy systems The book is primarily intended for students scholars and industrial practitioners interested in the design of modern digital systems **ASIC and FPGA Verification** Richard Munden,2005 **FPGAs 101** Gina Smith,2010-01-16 FPGAs Field Programmable Gate Arrays can be found in applications such as smart phones mp3 players medical imaging devices and for aerospace and defense technology FPGAs consist of logic blocks and programmable interconnects This allows an engineer to start with a blank slate and program the FPGA for a specific task for instance digital signal processing or a specific device for example a software defined radio Due to the short time to market and ability to reprogram to fix bugs without having to respin FPGAs are in increasingly high demand This book is for the engineer that has not yet had any experience with this electrifying and growing field The complex issue of FPGA design is broken down into four distinct phases Design Synthesis Simulation Place Route Numerous step by step examples along with source code accompany the discussion A brief primer of one of the popular FPGA and hardware languages VHDL is incorporated for a simple yet comprehensive learning tool While a general technology background is assumed no direct hardware development

understanding is needed Also included are details on tool set up verification techniques and test benches Reference material consists of a quick reference guide reserved words and common VHDL FPGA terms Learn how to design and develop FPGAs no prior experience necessary Breaks down the complex design and development of FPGAs into easy to learn building blocks Contains examples helpful tips and step by step tutorials for synthesis implementation simulation and programming phases

Using FPGA Modules Dennis Bingham, 2020-03-12 This book is not intended to just be read In this book you are going to get your hands dirty It is intended to be a hands on book with real exercises You will be guided through a series of projects that use the Opal Kelly XEM 7001 FPGA Module You may choose to use a different FPGA module but you will need to modify the projects accordingly You will have to purchase a small set of electronic components that will be used in all the projects The book provides a list of them along with all the part numbers for ordering through www.digikey.com one of the most popular online electronic supply stores When you have completed this book you will have hands on real world experience in using FPGA modules that you can then apply to any project you like from robotics to home automation to just about anything else you can think of The programs used to communicate with the FPGA will be written in C Again the goal is not to teach C but to provide code for the projects All the code and VHDL firmware code is available online for the user to download A little bit of programming skills is required but again we do not use any of the advanced features of C All the projects use simple console based applications as I did not want to have to go into all the ways of creating windows based applications Chapter 1 is an introduction to FPGA s in general describing what they are how they are used and what they are used for Chapter 2 dives into the details of the Opal Kelly XEM7001 FPGA module Its features and how to communicate with it via a Personal Computer PC Chapter 3 dives into the language used to program FPGA s Specifically VHDL This book it not intended to teach VHDL and all the projects will be using the basics of VHDL not the more advanced features This chapter does not talk about it in theory alone but we actually will use it with Xilinx Vivado development tool to create VHDL code synthesize it and run simulations on it Unlike other books that just talk about VHDL without providing any actual integration of it with real world tools We will dive into it in great detail using very simple examples This will help the user learn VHDL within the Vivado tool set Because developing an FPGA application involves more than just VHDL it also involves other files called XDC files with their own mini language this is integrated into the exercises of the chapter You will come out of this chapter with a complete understanding of how to develop an FPGA project from start to finish In Chapter 4 we delve into creating FPGA projects for the XEM 7001 that will actually do something Starting with controlling the onboard LED s and moving to more complicated tasks Chapter 4 is the longest chapter in the book You will be breadboarding real circuits and then having the FPGA control and use those circuits while observing the results using various test instruments There is a small investment in various components that you will need The fun thing is you get to see the results of your efforts from temperature sensors and other interface chips You finally get a break at chapter 5 It just talks about communications protocols that FPGA s and

Micro controllers use to talk to other chips Chapter 6 we pickup on communicating with other chips ADC s Temperature Sensors EEPROM Memory Chips etc Again you are going to be wiring up a lot of circuits and observing the results of your labor Chapter 7 is probably one of the most fun chapters here we are interfacing to electro mechanical motors from standard motors with attached encoders to stepper motors ending with servo motors we get the world moving some We end this chapter implementing a soft core micro controller withing the FPGA Specifically the Xilinx Micro Blaze MCS Yes you can actually implement a microcontroller inside of an FPGA Chapter 8 completes the book with instructions on where you may go from here

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