

(T2) P1.0	<input type="checkbox"/>	1	40	<input type="checkbox"/>	VCC
(T2 EX) P1.1	<input type="checkbox"/>	2	39	<input type="checkbox"/>	P0.0 (AD0)
P1.2	<input type="checkbox"/>	3	38	<input type="checkbox"/>	P0.1 (AD1)
P1.3	<input type="checkbox"/>	4	37	<input type="checkbox"/>	P0.2 (AD2)
P1.4	<input type="checkbox"/>	5	36	<input type="checkbox"/>	P0.3 (AD3)
(MOSI) P1.5	<input type="checkbox"/>	6	35	<input type="checkbox"/>	P0.4 (AD4)
(MISO) P1.6	<input type="checkbox"/>	7	34	<input type="checkbox"/>	P0.5 (AD5)
(SCK) P1.7	<input type="checkbox"/>	8	33	<input type="checkbox"/>	P0.6 (AD6)
RST	<input type="checkbox"/>	9	32	<input type="checkbox"/>	P0.7 (AD7)
(RXD) P3.0	<input type="checkbox"/>	10	31	<input type="checkbox"/>	$\overline{EA}/VPP$
(TXD) P3.1	<input type="checkbox"/>	11	30	<input type="checkbox"/>	$\overline{ALE}/\overline{PROG}$
( $\overline{INT0}$ ) P3.2	<input type="checkbox"/>	12	29	<input type="checkbox"/>	$\overline{PSEN}$
( $\overline{INT1}$ ) P3.3	<input type="checkbox"/>	13	28	<input type="checkbox"/>	P2.7 (A15)
(T0) P3.4	<input type="checkbox"/>	14	27	<input type="checkbox"/>	P2.6 (A14)
(T1) P3.5	<input type="checkbox"/>	15	26	<input type="checkbox"/>	P2.5 (A13)
( $\overline{WR}$ ) P3.6	<input type="checkbox"/>	16	25	<input type="checkbox"/>	P2.4 (A12)
( $\overline{RD}$ ) P3.7	<input type="checkbox"/>	17	24	<input type="checkbox"/>	P2.3 (A11)
XTAL2	<input type="checkbox"/>	18	23	<input type="checkbox"/>	P2.2 (A10)
XTAL1	<input type="checkbox"/>	19	22	<input type="checkbox"/>	P2.1 (A9)
GND	<input type="checkbox"/>	20	21	<input type="checkbox"/>	P2.0 (A8)

# At89s52 Specification Microcontroller Datasheet

**Victor Bandur, Wolfram Kahl, Alan  
Wassyng**



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## **Table of Contents At89s52 Specification Microcontroller Datasheet**

1. Understanding the eBook At89s52 Specification Microcontroller Datasheet
  - The Rise of Digital Reading At89s52 Specification Microcontroller Datasheet
  - Advantages of eBooks Over Traditional Books
2. Identifying At89s52 Specification Microcontroller Datasheet
  - Exploring Different Genres
  - Considering Fiction vs. Non-Fiction
  - Determining Your Reading Goals
3. Choosing the Right eBook Platform
  - Popular eBook Platforms
  - Features to Look for in an At89s52 Specification Microcontroller Datasheet
  - User-Friendly Interface
4. Exploring eBook Recommendations from At89s52 Specification Microcontroller Datasheet
  - Personalized Recommendations
  - At89s52 Specification Microcontroller Datasheet User Reviews and Ratings
  - At89s52 Specification Microcontroller Datasheet and Bestseller Lists
5. Accessing At89s52 Specification Microcontroller Datasheet Free and Paid eBooks
  - At89s52 Specification Microcontroller Datasheet Public Domain eBooks
  - At89s52 Specification Microcontroller Datasheet eBook Subscription Services
  - At89s52 Specification Microcontroller Datasheet Budget-Friendly Options
6. Navigating At89s52 Specification Microcontroller Datasheet eBook Formats

- ePub, PDF, MOBI, and More
  - At89s52 Specification Microcontroller Datasheet Compatibility with Devices
  - At89s52 Specification Microcontroller Datasheet Enhanced eBook Features
7. Enhancing Your Reading Experience
    - Adjustable Fonts and Text Sizes of At89s52 Specification Microcontroller Datasheet
    - Highlighting and Note-Taking At89s52 Specification Microcontroller Datasheet
    - Interactive Elements At89s52 Specification Microcontroller Datasheet
  8. Staying Engaged with At89s52 Specification Microcontroller Datasheet
    - Joining Online Reading Communities
    - Participating in Virtual Book Clubs
    - Following Authors and Publishers At89s52 Specification Microcontroller Datasheet
  9. Balancing eBooks and Physical Books At89s52 Specification Microcontroller Datasheet
    - Benefits of a Digital Library
    - Creating a Diverse Reading Collection At89s52 Specification Microcontroller Datasheet
  10. Overcoming Reading Challenges
    - Dealing with Digital Eye Strain
    - Minimizing Distractions
    - Managing Screen Time
  11. Cultivating a Reading Routine At89s52 Specification Microcontroller Datasheet
    - Setting Reading Goals At89s52 Specification Microcontroller Datasheet
    - Carving Out Dedicated Reading Time
  12. Sourcing Reliable Information of At89s52 Specification Microcontroller Datasheet
    - Fact-Checking eBook Content of At89s52 Specification Microcontroller Datasheet
    - Distinguishing Credible Sources
  13. Promoting Lifelong Learning
    - Utilizing eBooks for Skill Development
    - Exploring Educational eBooks
  14. Embracing eBook Trends
    - Integration of Multimedia Elements
    - Interactive and Gamified eBooks

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