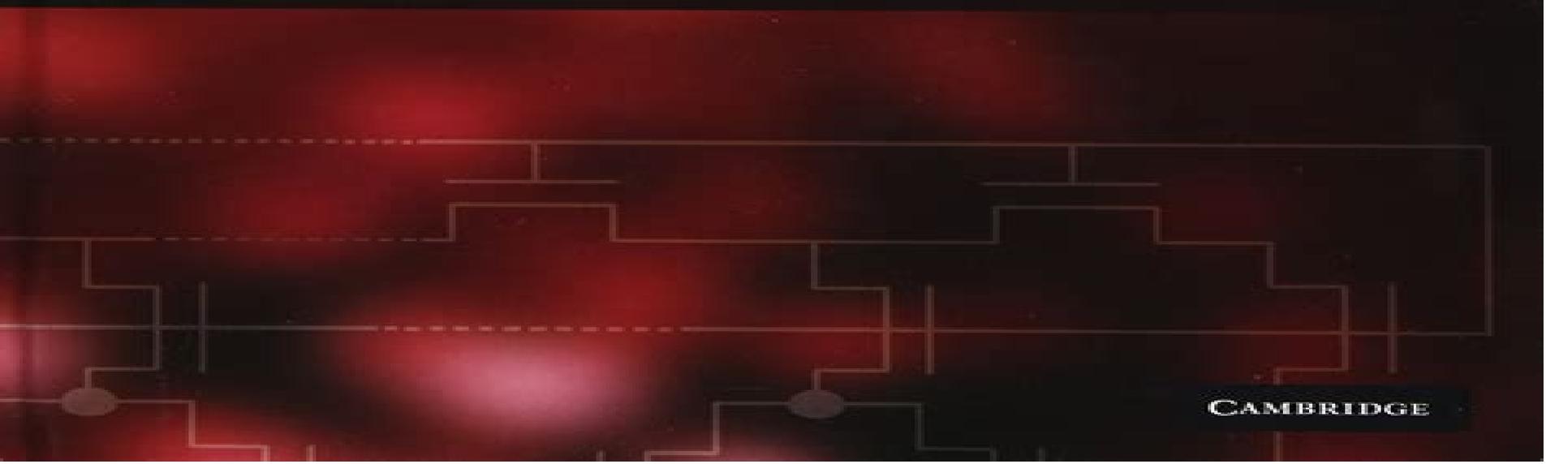
The top portion of the cover features a red background with white circuit diagrams. These diagrams include a complex network of lines at the top, followed by two distinct CMOS-like structures. Each structure consists of a PMOS transistor at the top and an NMOS transistor at the bottom, connected to a common node. The background also contains faint, larger-scale circuit patterns and arrows.

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Márcio Cherem Schneider and
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The bottom portion of the cover features a red background with white circuit diagrams, similar to the top section. It shows a network of lines and two CMOS-like structures with PMOS and NMOS transistors. The background also contains faint, larger-scale circuit patterns and arrows.

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