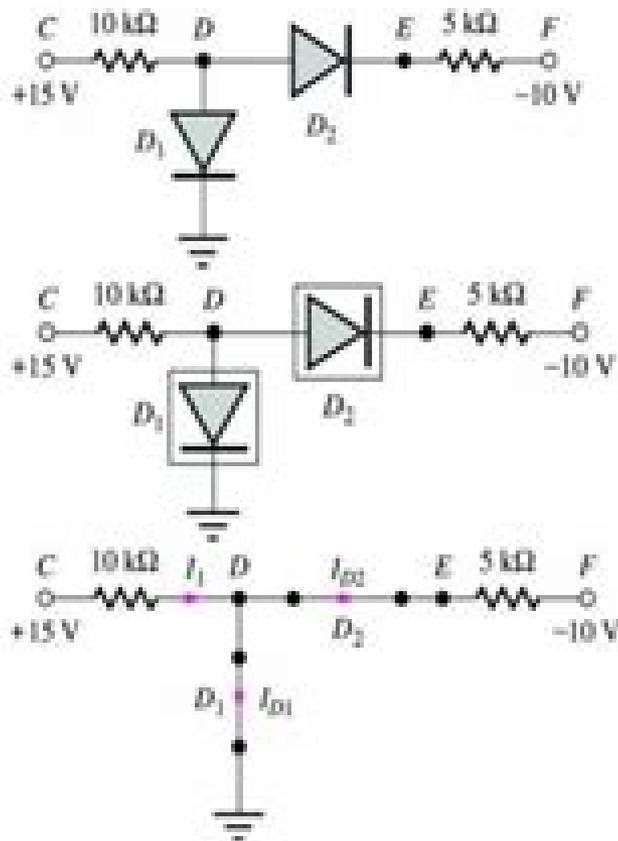


Two-Diode Circuit Analysis



Analysis: Ideal diode model is chosen. Since 15V source is forcing positive current through D_1 and D_2 and -10V source is forcing positive current through D_2 , assume both diodes are on.

Since voltage at node D is zero due to short circuit of ideal diode D_1 ,

$$I_1 = \frac{(15 - 0)V}{10k\Omega} = 1.50\text{mA} \quad I_{D2} = \frac{0 - (-10)V}{5k\Omega} = 2.00\text{mA}$$

$$I_1 = I_{D1} + I_{D2} \quad \therefore I_{D1} = 1.5 - 2 = -0.50\text{mA}$$

Q-points are (-0.5 mA, 0 V) and (2.0 mA, 0 V)

But, $I_{D1} < 0$ is not allowed by diode, so ?

Diode Circuit Analysis

Michal Rosen-Zvi



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