

# Digital Signals Integrity Tutorial

## Sonnet Microstrip Models



The College of New Jersey

Thanks to Sonnet's Application Engineers, Greg Kinoshita and the DCMA Spring 2014 EUC-443 Digital Engineering Systems Class, which included: Richard Moore, George Wang, Jason Miller, Michael O'Neil, Stephen Cook, Robert Nelson, Jeffrey Williams, Paul Jones, Steven Fisher, Andrew Hester, Andrew Roberts, Stephen Berman, David de Luca, Christopher Long, Matthew Casper, John Gough, Joseph Wang, Alexander J. Mariani, Matthew Miller, Michael Szymanski, Timothy Rogers, Jeffrey A. Pinsky, Robert Smith, John Hubert, Scott Manning, Andrew J. Richardson, Jr., Edward S. Sparrow, Andrew Hester, and George Kinoshita.  
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# Digital Signals Integrity Tutorial Sonnet Software

**Geoff Lawday, David Ireland, Greg  
Edlund**



## **Digital Signals Integrity Tutorial Sonnet Software:**

*The Design and Signal Integrity Analysis of a Digital Signal Processing System* Shawn William Thompson, 1996 *Signal Integrity Engineers* Geoff Lawday, David Ireland, Greg Edlund, 2013-02-06 A Signal Integrity Engineer's Companion Real Time Test and Measurement and Design Simulation Geoff Lawday David Ireland Greg Edlund Foreword by Chris Edwards Editor IET Electronics Systems and Software magazine Prentice Hall Modern Semiconductor Design Series Prentice Hall Signal Integrity Library Use Real World Test and Measurement Techniques to Systematically Eliminate Signal Integrity Problems This is the industry's most comprehensive authoritative and practical guide to modern Signal Integrity SI test and measurement for high speed digital designs Three of the field's leading experts guide you through systematically detecting observing analyzing and rectifying both modern logic signal defects and embedded system malfunctions The authors cover the entire life cycle of embedded system design from specification and simulation onward illuminating key techniques and concepts with easy to understand illustrations Writing for all electrical engineers signal integrity engineers and chip designers the authors show how to use real time test and measurement to address today's increasingly difficult interoperability and compliance requirements They also present detailed start to finish case studies that walk you through commonly encountered design challenges including ensuring that interfaces consistently operate with positive timing margins without incurring excessive cost calculating total jitter budgets and managing complex tradeoffs in high speed serial interface design Coverage includes Understanding the complex signal integrity issues that arise in today's high speed designs Learning how eye diagrams automated compliance tests and signal analysis measurements can help you identify and solve SI problems Reviewing the electrical characteristics of today's most widely used CMOS IO circuits Performing signal path analyses based on intuitive Time Domain Reflectometry TDR techniques Achieving more accurate real time signal measurements and avoiding probe problems and artifacts Utilizing digital oscilloscopes and logic analyzers to make accurate measurements in high frequency environments Simulating real world signals that stress digital circuits and expose SI faults Accurately measuring jitter and other RF parameters in wireless applications About the Authors Dr Geoff Lawday is Tektronix Professor in Measurement at Buckinghamshire New University England He delivers courses in signal integrity engineering and high performance bus systems at the University Tektronix laboratory and presents signal integrity seminars throughout Europe on behalf of Tektronix David Ireland European and Asian design and manufacturing marketing manager for Tektronix has more than 30 years of experience in test and measurement He writes regularly on signal integrity for leading technical journals Greg Edlund Senior Engineer IBM Global Engineering Solutions division has participated in development and testing for ten high performance computing platforms He authored *Timing Analysis and Simulation for Signal Integrity Engineers* Prentice Hall [Signal Integrity and Radiated Emission of High-Speed Digital Systems](#) Spartaco Caniggia, Francescaromana Maradei, 2008-12-15 Before putting digital systems for information technology or

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watashi wa des 私は私 i am kara kimashita 来ました i came

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