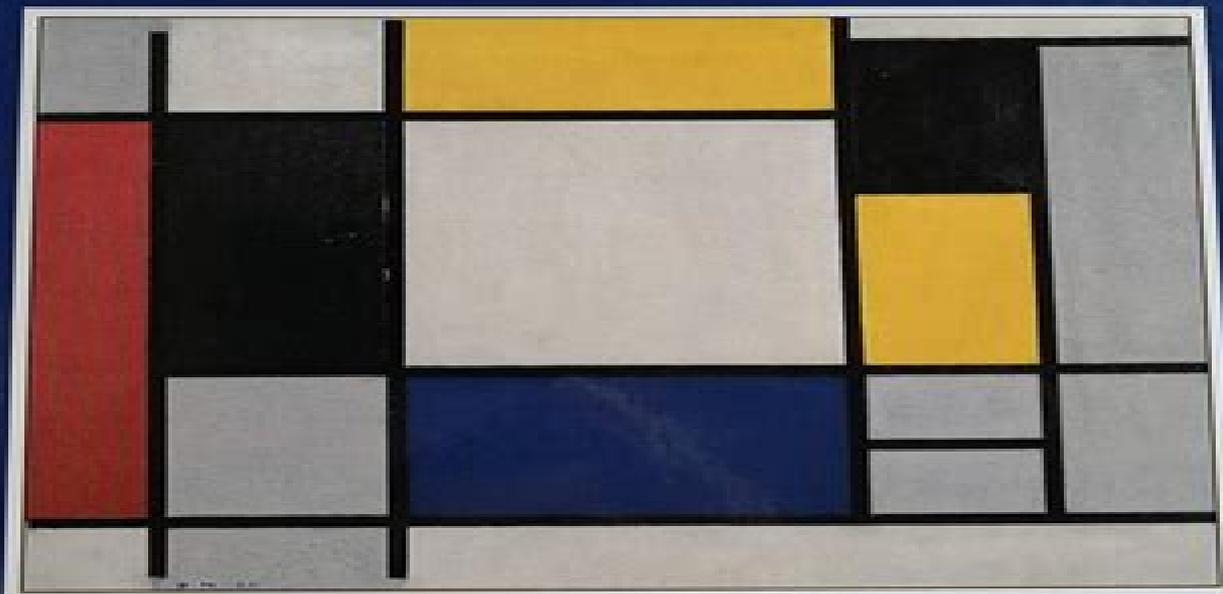


DIGITAL INTEGRATED CIRCUITS

A DESIGN PERSPECTIVE



JAN M. RABAEY

PRENTICE HALL ELECTRONICS AND VLSI SERIES
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integrated VLSI circuits and systems May also be used as a VLSI reference for professional VLSI design engineers VLSI design managers and VLSI CAD engineers Modern VLSI Design provides a comprehensive bottom up guide to the design of VLSI systems from the physical design of circuits through system architecture with focus on the latest solution for system on chip SOC design Because VLSI system designers face a variety of challenges that include high performance interconnect delays low power low cost and fast design turnaround time successful designers must understand the entire design process The Third Edition also provides a much more thorough discussion of hardware description languages with introduction to both Verilog and VHDL For that reason this book presents the entire VLSI design process in a single volume

Digital integrated Circuits, 1976 *Design of High Voltage xDSL Line Drivers in Standard CMOS* Bert Serneels, Michiel Steyaert, 2008-01-08 Design of high voltage xDSL line drivers in standard CMOS fits in the quest for highly efficient fully integrated xDSL modems for central office applications The book focusses on the line driver the most demanding building block of the xDSL modem for lowering power To reduce the cost the cheapest technology is selected standard CMOS without any extra process options to increase the nominal supply voltage The emphasis lies on the analysis design and implementation of high voltage highly efficient line drivers in mainstream CMOS Design of high voltage xDSL line drivers in standard CMOS covers the total design flow of monolithic CMOS high voltage circuits The book is essential reading for analog design engineers and researchers in the field and is also suitable as a text book for an advanced course on the subject

Design and Analysis of High Efficiency Line Drivers for xDSL Tim Piessens, Michiel Steyaert, 2005-12-30 Design and Analysis of High Efficiency Line Drivers for xDSL covers the most important building block of an xDSL ADSL VDSL system the line driver Traditional Class AB line drivers consume more than 70% of the total power budget of state of the art ADSL modems This book describes the main difficulties in designing line drivers for xDSL The most important specifications are elaborated starting from the main properties of the channel and the signal properties The traditional class AB state of the art class G and future technologies class K are discussed The main part of Design and Analysis of High Efficiency Line Drivers for xDSL describes the design of a novel architecture the Self Oscillating Power Amplifier or SOPA

Digital Integrated Circuits Jan M. Rabaey, 1996 Contemporary in content and form this practical book successfully bridges the gap between the circuit perspective and system perspective of digital integrated circuit design Beginning with a solid foundation of the operation of electronic devices and an in depth analysis of the nucleus of digital design it maintains a consistent logical flow of subject matter throughout addressing today's most significant and compelling industry topics the effect of interconnect design for low power issues in timing and clocking design methodologies and the tremendous impact of design automation on the digital design perspective Discusses state of the art topics in design such as complex gates adders multipliers registers controllers and memories Focuses on practical design issues with examples design problems and case studies creating practical knowledge readers can readily apply in industrial design Offers perspectives on the future evolution of design

practice at the end of each chapter to put issues discussed in a broader vision Includes many illustrations and reality based design problems to foster comprehension For professional engineers Computer Vision and Information Technology K. V. Kale,S. C. Mehrotra,R. R. Manza,2010 Spread in 133 articles divided in 20 sections the present treatises broadly discusses Part 1 Image Processing Part 2 Radar and Satellite Image Processing Part 3 Image Filtering Part 4 Content Based Image Retrieval Part 5 Color Image Processing and Video Processing Part 6 Medical Image Processing Part 7 Biometric Part 8 Network Part 9 Mobile Computing Part 10 Pattern Recognition Part 11 Pattern Classification Part 12 Genetic Algorithm Part 13 Data Warehousing and Mining Part 14 Embedded System Part 15 Wavelet Part 16 Signal Processing Part 17 Neural Network Part 18 Nanotechnology and Quantum Computing Part 19 Image Analysis Part 20 Human Computer Interaction

Digital Integrated Circuits Jan M. Rabaey,Anantha P. Chandrakasan,Borivoje Nikolić,2008 Millimeter-wave CMOS Power Amplifiers Design Mounir Youssef Bohsali,2008 Optical Information Systems ,2003 **Bibliographic Index** ,1997 *Digital Integrated Circuits : Design Perspective(2nd)(Paperback)* Jan M. Rabaey,2012-01-01 **Electric Circuits** James William Nilsson,Susan A. Riedel,2005 **IEEE Circuits & Devices** ,2006 Proceedings ,2005

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