

[4218] – 6

F. Y. B. Sc. (Computer Science) Examination, October – 2012

ELECTRONICS

(Paper - II)

11312 – Fundamentals of Digital Electronics

(2008 Pattern)

Time : Three Hours

Total Marks : 80

- Note :**
- (i) *All questions are compulsory.*
 - (ii) *Use of non-programmable electronic calculator is allowed*
 - (iii) *Draw neat diagrams wherever necessary.*

Q. 1. Attempt all of the following questions : [8×2=16]

- (a) What is parity? What is the use of parity bit?
- (b) State two de Morgan's theorems.
- (c) Define modulo of a counter. Give number of flipflops required for MOD 5 counter.
- (d) Write the formulae for output frequency and duty cycle for Astable multivibrator using IC 555.
- (e) Write truth table of 1 : 2 demultiplexer.
- (f) Give the full form of EPROM and EEPROM.
- (g) For 4 bit R-2R type DAC with binary input 0101, find output voltage if $V_{ref} = 5V$.
- (h) What is the use of Assembler?

Digital Electronics Question Paper With Answer

Terry L. M. Bartelt



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Table of Contents Digital Electronics Question Paper With Answer

1. Understanding the eBook Digital Electronics Question Paper With Answer
 - The Rise of Digital Reading Digital Electronics Question Paper With Answer
 - Advantages of eBooks Over Traditional Books
2. Identifying Digital Electronics Question Paper With Answer
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in a Digital Electronics Question Paper With Answer
 - User-Friendly Interface
4. Exploring eBook Recommendations from Digital Electronics Question Paper With Answer
 - Personalized Recommendations
 - Digital Electronics Question Paper With Answer User Reviews and Ratings
 - Digital Electronics Question Paper With Answer and Bestseller Lists
5. Accessing Digital Electronics Question Paper With Answer Free and Paid eBooks
 - Digital Electronics Question Paper With Answer Public Domain eBooks
 - Digital Electronics Question Paper With Answer eBook Subscription Services
 - Digital Electronics Question Paper With Answer Budget-Friendly Options
6. Navigating Digital Electronics Question Paper With Answer eBook Formats
 - ePub, PDF, MOBI, and More
 - Digital Electronics Question Paper With Answer Compatibility with Devices
 - Digital Electronics Question Paper With Answer Enhanced eBook Features
7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Digital Electronics Question Paper With Answer
 - Highlighting and Note-Taking Digital Electronics Question Paper With Answer
 - Interactive Elements Digital Electronics Question Paper With Answer
8. Staying Engaged with Digital Electronics Question Paper With Answer

- Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Digital Electronics Question Paper With Answer
9. Balancing eBooks and Physical Books Digital Electronics Question Paper With Answer
- Benefits of a Digital Library
 - Creating a Diverse Reading Collection Digital Electronics Question Paper With Answer
10. Overcoming Reading Challenges
- Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
11. Cultivating a Reading Routine Digital Electronics Question Paper With Answer
- Setting Reading Goals Digital Electronics Question Paper With Answer
 - Carving Out Dedicated Reading Time
12. Sourcing Reliable Information of Digital Electronics Question Paper With Answer
- Fact-Checking eBook Content of Digital Electronics Question Paper With Answer
 - Distinguishing Credible Sources
13. Promoting Lifelong Learning
- Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
14. Embracing eBook Trends
- Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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