

Department of Electronics & Communication(University Polytechnic)

Jamia Millia Islamia

First Semestral Test (April 2019)

Total Marks-25

Time: 1 hour

Subject -DIGITAL ELECTRONICS

Subject Code: DEL-101

Attempt all questions.

Unit-1

- Q.1) Convert the following numbers to their equivalents as asked: (6)
- (a)  $(11074)_{10} = ( )_{16}$       (b)  $1101111.101 = ( )_{10}$       (c)  $11.101101 = ( )_{10}$       (d)  $(1723)_{10} = ( )_{16}$   
(e)  $(ACD)_{16} = ( )_{10}$       (f)  $3018 = ( )_{16}$
- Q.2) Convert: (a)  $10001110101$  to gray code      (b) Gray code  $111011001$  to binary (3)
- Q.3) Perform calculation as asked: (a)  $1110.1011 + 1001.1110$  (b)  $10000 - 11000$  (3)
- Q.4) (a) Write the BCD code for 93.17      (b) Write the Excess -3 code for 951 (2)

Unit-2

- Q.5) Reduce the given Boolean expressions. (5)
- (a)  $AD + DA + AC$       (b)  $[(A + B + C + D)' + AB'CD]'$
- Q.6) Explain half adder circuit with the help of truth table, logic diagram and boolean expression. (3)

# Digital Electronics Diploma First Sem Question Paper

**Banani Ghosh**



## **Digital Electronics Diploma First Sem Question Paper:**

**FUNDAMENTALS OF DIGITAL ELECTRONICS (2 Credits) Electronic Science P B Prof (Dr ) Buchade,P. B. (Dr. Buchade,M L Prof (Dr ) Dongare,M. L. (Dr. Dongare,J. A. Bangali,2019-11** Electronics have made tremendous revolution in last decade The majority of this revolution is in the digital world Students entering in the field of Electronics should have understanding of basic fundamentals of digital electronics This text book has been prepared keeping in mind the need of subject and syllabus specified by SPPU The First Chapter describes basics of digital electronics which includes number system logic gates and Boolean algebra

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magnitude comparator encoder decoder code converters Multiplexer and Demultiplexer CHAPTER 4 Describes with Latches Flip Flops Registers and Counters CHAPTER 5 Concentrates on the Analysis as well as design of synchronous sequential circuits Design of synchronous counters sequence generator and Sequence detector CHAPTER 6 Concentrates the Design as well as Analysis of Fundamental Mode circuits Pulse mode Circuits Hazard Free Circuits ASM Chart and Design of Asynchronous counters CHAPTER 7 Discussion on memory devices which includes ROM RAM PLA PAL Sequential logic devices and ASIC CHAPTER 8 Concentrate on the comparison operation and characteristics of RTL DTL TTL ECL and MOS families We have taken enough care to present the definitions and statements of basic laws and theorems problems with simple steps to make the students familiar with the fundamentals of Digital Design     *Digital Electronics : a Practical Approach* William Kleitz,1999     *Introductory Digital Electronics* ,1983

This book delves into Digital Electronics Diploma First Sem Question Paper. Digital Electronics Diploma First Sem Question Paper is a crucial topic that must be grasped by everyone, ranging from students and scholars to the general public. This book will furnish comprehensive and in-depth insights into Digital Electronics Diploma First Sem Question Paper, encompassing both the fundamentals and more intricate discussions.

1. The book is structured into several chapters, namely:
    - Chapter 1: Introduction to Digital Electronics Diploma First Sem Question Paper
    - Chapter 2: Essential Elements of Digital Electronics Diploma First Sem Question Paper
    - Chapter 3: Digital Electronics Diploma First Sem Question Paper in Everyday Life
    - Chapter 4: Digital Electronics Diploma First Sem Question Paper in Specific Contexts
    - Chapter 5: Conclusion
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- The book is crafted in an easy-to-understand language and is complemented by engaging illustrations. It is highly recommended for anyone seeking to gain a comprehensive understanding of Digital Electronics Diploma First Sem Question Paper.

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