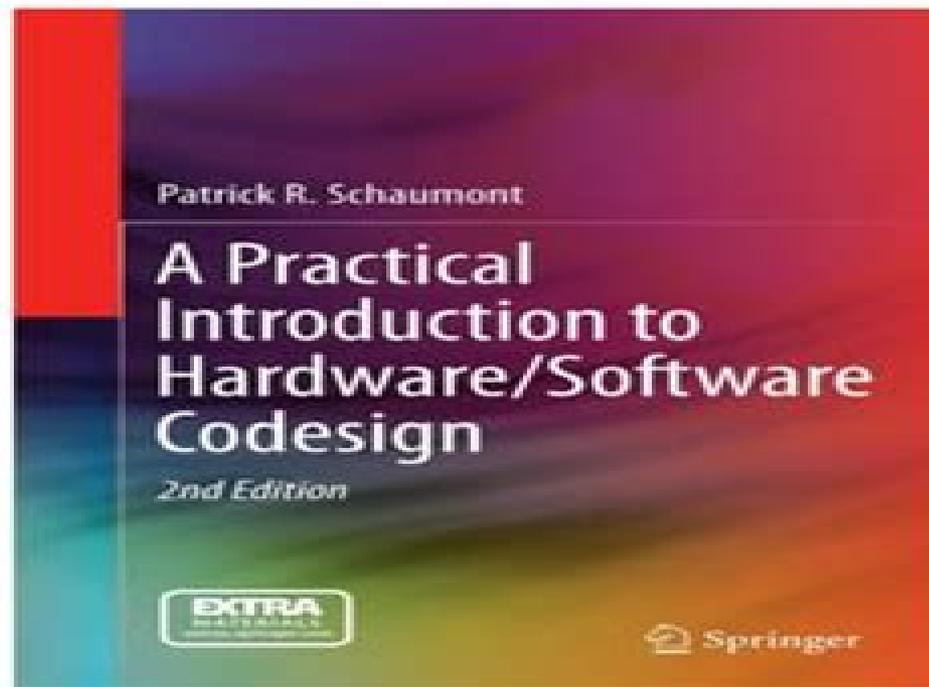


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A Practical Introduction to Hardware/Software Codesign Patrick R. Schaumont, 2012-11-27 This textbook serves as an introduction to the subject of embedded systems design with emphasis on integration of custom hardware components with software. The key problem addressed in the book is the following: how can an embedded systems designer strike a balance between flexibility and efficiency? The book describes how combining hardware design with software design leads to a solution to this important computer engineering problem. The book covers four topics in hardware software codesign: fundamentals, the design space of custom architectures, the hardware software interface, and application examples. The book comes with an associated design environment that helps the reader to perform experiments in hardware software codesign. Each chapter also includes exercises and further reading suggestions. Improvements in this second edition include labs and examples using modern FPGA environments from Xilinx and Altera which will make the material in this book applicable to a greater number of courses where these tools are already in use. More examples and exercises have been added throughout the book. If I were teaching a course on this subject I would use this as a resource and text. If I were a student who wanted to learn codesign I would look for a course that at least used a similar approach. If I were an engineer or engineering manager who wanted to learn more about codesign from a very practical perspective I would read this book first before any other. When I first started learning about codesign as a practitioner a book like this would have been the perfect introduction. Grant Martin Tensilica *A Practical Introduction to Hardware/Software Codesign* Springer, 2012-11-26

A Practical Introduction to Hardware/Software Codesign Patrick Schaumont, 2011-03-02 This is a practical book for computer engineers who want to understand or implement hardware software systems. It focuses on problems that require one to combine hardware design with software design such problems can be solved with hardware software codesign. When used properly hardware software codesign works better than hardware design or software design alone it can improve the overall performance of digital systems and it can shorten their design time. Hardware software codesign can help a designer to make trade offs between the flexibility and the performance of a digital system. To achieve this a designer needs to combine two radically different ways of design: the sequential way of decomposition in time using software with the parallel way of decomposition in space using hardware. Intended Audience: This book assumes that you have a basic understanding of hardware that you are familiar with standard digital hardware components such as registers, logic gates, and components such as multiplexers and arithmetic operators. The book also assumes that you know how to write a program in C. These topics are usually covered in an introductory course on computer engineering or in a combination of courses on digital design and software engineering. **Embedded Computing for High Performance** João Manuel Paiva Cardoso, José Gabriel de Figueiredo Coutinho, Pedro C. Diniz, 2017-06-13 *Embedded Computing for High Performance Design Exploration and Customization Using High level Compilation and Synthesis Tools* provides a set of real life example implementations that

migrate traditional desktop systems to embedded systems Working with popular hardware including Xilinx and ARM the book offers a comprehensive description of techniques for mapping computations expressed in programming languages such as C or MATLAB to high performance embedded architectures consisting of multiple CPUs GPUs and reconfigurable hardware FPGAs The authors demonstrate a domain specific language LARA that facilitates retargeting to multiple computing systems using the same source code In this way users can decouple original application code from transformed code and enhance productivity and program portability After reading this book engineers will understand the processes methodologies and best practices needed for the development of applications for high performance embedded computing systems Focuses on maximizing performance while managing energy consumption in embedded systems Explains how to retarget code for heterogeneous systems with GPUs and FPGAs Demonstrates a domain specific language that facilitates migrating and retargeting existing applications to modern systems Includes downloadable slides tools and tutorials [FPGA Prototyping by SystemVerilog Examples](#) Pong P. Chu, 2018-04-18 A hands on introduction to FPGA prototyping and SoC design This is the successor edition of the popular FPGA Prototyping by Verilog Examples text It follows the same learning by doing approach to teach the fundamentals and practices of HDL synthesis and FPGA prototyping The new edition uses a coherent series of examples to demonstrate the process to develop sophisticated digital circuits and IP intellectual property cores integrate them into an SoC system on a chip framework realize the system on an FPGA prototyping board and verify the hardware and software operation The examples start with simple gate level circuits progress gradually through the RT register transfer level modules and lead to a functional embedded system with custom I O peripherals and hardware accelerators Although it is an introductory text the examples are developed in a rigorous manner and the derivations follow the strict design guidelines and coding practices used for large complex digital systems The book is completely updated and uses the SystemVerilog language which absorbs the Verilog language It presents the hardware design in the SoC context and introduces the hardware software co design concept Instead of treating examples as isolated entities the book integrates them into a single coherent SoC platform that allows readers to explore both hardware and software programmability and develop complex and interesting embedded system projects The new edition Adds four general purpose IP cores which are multi channel PWM pulse width modulation controller I2C controller SPI controller and XADC Xilinx analog to digital converter controller Introduces a music synthesizer constructed with a DDFS direct digital frequency synthesis module and an ADSR attack decay sustain release envelope generator Expands the original video controller into a complete stream based video subsystem that incorporates a video synchronization circuit a test pattern generator an OSD on screen display controller a sprite generator and a frame buffer Provides a detailed discussion on blocking and nonblocking statements and coding styles Describes basic concepts of software hardware co design with Xilinx MicroBlaze MCS soft core processor Provides an overview of bus interconnect and interface circuit Presents basic embedded system software development

Suggests additional modules and peripherals for interesting and challenging projects

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FPGA Prototyping by VHDL Examples Pong P. Chu, 2018-01-25 A hands on introduction to FPGA prototyping and SoC design This Second Edition of the popular book follows the same learning by doing approach to teach the fundamentals and practices of VHDL synthesis and FPGA prototyping It uses a coherent series of examples to demonstrate the process to develop sophisticated digital circuits and IP intellectual property cores integrate them into an SoC system on a chip framework realize the system on an FPGA prototyping board and verify the hardware and software operation The examples start with simple gate level circuits progress gradually through the RT register transfer level modules and lead to a functional embedded system with custom I O peripherals and hardware accelerators Although it is an introductory text the examples are developed in a rigorous manner and the derivations follow strict design guidelines and coding practices used for large complex digital systems The new edition is completely updated It presents the hardware design in the SoC context and introduces the hardware software co design concept Instead of treating examples as isolated entities the book integrates them into a single coherent SoC platform that allows readers to explore both hardware and software programmability and develop complex and interesting embedded system projects The revised edition Adds four general purpose IP cores which are multi channel PWM pulse width modulation controller I2C controller SPI controller and XADC Xilinx analog to digital converter controller Introduces a music synthesizer constructed with a DDFS direct digital frequency synthesis module and an ADSR attack decay sustain release envelop generator Expands the original video controller into a complete stream based video subsystem that incorporates a video synchronization circuit a test pattern generator an OSD on screen display controller a sprite generator and a frame buffer Introduces basic concepts of software hardware co design with Xilinx MicroBlaze MCS soft core processor Provides an overview of bus interconnect and interface circuit Introduces basic embedded system software development Suggests additional modules and peripherals for interesting and challenging projects

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Embedded SoPC Design with Nios II Processor and Verilog Examples Pong P. Chu, 2012-05-14 Explores the unique hardware programmability of FPGA based embedded systems using a learn by doing approach to introduce the concepts and techniques for embedded SoPC design with Verilog An SoPC system on a programmable chip integrates a processor memory modules I O peripherals and custom hardware accelerators into a single FPGA field programmable gate array device In addition to the customized software customized hardware can be developed and incorporated into the embedded system as well allowing us to configure

the soft core processor create tailored I O interfaces and develop specialized hardware accelerators for computation intensive tasks Utilizing an Altera FPGA prototyping board and its Nios II soft core processor Embedded SoPC Design with Nios II Processor and Verilog Examples takes a learn by doing approach to illustrate the hardware and software design and development process by including realistic projects that can be implemented and tested on the board Emphasizing hardware design and integration throughout the book is divided into four major parts Part I covers HDL and synthesis of custom hardware Part II introduces the Nios II processor and provides an overview of embedded software development Part III demonstrates the design and development of hardware and software of several complex I O peripherals including a PS2 keyboard and mouse a graphic video controller an audio codec and an SD secure digital card Part IV provides several case studies of the integration of hardware accelerators including a custom GCD greatest common divisor circuit a Mandelbrot set fractal circuit and an audio synthesizer based on DDFS direct digital frequency synthesis methodology While designing and developing an embedded SoPC can be rewarding the learning can be a long and winding journey This book shows the trail ahead and guides readers through the initial steps to exploit the full potential of this emerging methodology

Embedded SoPC Design with Nios II Processor and VHDL Examples Pong P. Chu,2011-09-26 The book is divided into four major parts Part I covers HDL constructs and synthesis of basic digital circuits Part II provides an overview of embedded software development with the emphasis on low level I O access and drivers Part III demonstrates the design and development of hardware and software for several complex I O peripherals including PS2 keyboard and mouse a graphic video controller an audio codec and an SD secure digital card Part IV provides three case studies of the integration of hardware accelerators including a custom GCD greatest common divisor circuit a Mandelbrot set fractal circuit and an audio synthesizer based on DDFS direct digital frequency synthesis methodology The book utilizes FPGA devices Nios II soft core processor and development platform from Altera Co which is one of the two main FPGA manufactures Altera has a generous university program that provides free software and discounted prototyping boards for educational institutions details at www.altera.com university The two main educational prototyping boards are known as DE1 99 and DE2 269 All experiments can be implemented and tested with these boards A board combined with this book becomes a turn key solution for the SoPC design experiments and projects Most HDL and C codes in the book are device independent and can be adapted by other prototyping boards as long as a board has similar I O configuration

Encyclopedia of Computer Science and Technology Allen Kent,James G. Williams,2000-04-28 Combining Artificial Neural Networks to Symbolic and Algebraic computation

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A Practical Approach to Real-time Systems Phillip A. Laplante,2000 Under the same cover this volume offers both modern and classic papers focusing on real time systems design and analysis Rather than focusing in theoretical observations of real time systems it is intended for the practical professional who is building real real

time systems The editor himself the author of a course on real time systems has selected articles to provide a deep exploration of issues raised in his other works In particular emphasis is placed on applying practical but theoretically sound approaches in software engineering rate monotonic design and analysis testing and architecting systems for real time applications

OOIS 2001 Xingxu Wang, Shushma Patel, Ronald Johnston, 2001-07-23 This volume contains the papers presented at the 7th International Conference on Object Oriented Information Systems OOIS 2001 The conference was hosted by the University of Calgary Calgary Canada on 27-29 August 2001 The theme of OOIS1 was Object Oriented and Web Based Frameworks for Information Systems The papers published in this volume highlight the contributions of leading researchers and practitioners in the field of Object Technology and Information Systems The topics covered include OO foundations OO modeling and analysis OOIS processes XML based IS OO based reuse OO frameworks OO and web testing Use case for requirement analysis OO CASE tools OO virtual environments and real time systems IT process assessment and improvement Industrial experience and case studies Web based IS Component based OOIS Software engineering metrics and analysis Production line and requirements engineering GRIDs the next generation technologies for the Internet E Business Enterprise Frameworks and Perspectives on future development

Proceedings of the ... ACM/USENIX International Conference on Virtual Execution Environments, 2005

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System on Package Rao Tummala, 2007-07-22 System on Package SOP is an emerging microelectronic technology that places an entire system on a single chip size package Where systems used to be bulky boxes housing hundreds of components SOP saves interconnection time and heat generation by keep a full system with computing communications and consumer functions all in a single chip Written by the Georgia Tech developers of the technology this book explains the basic parameters design functions and manufacturing issues showing electronic designers how this radical new packaging technology can be used to solve pressing electronics design challenges

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Forthcoming Books Rose Army, 1997

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