

Efficient

C++

*Performance
Programming
Techniques*



 CodeSourcery
PRACTICE

Dov Bulka
David Mayhew

Efficient C Performance Programming Techniques

Zhe Xu

A decorative graphic element consisting of a light blue horizontal bar with a rounded right end, and a red-to-white gradient semi-circle overlapping the right side of the bar.

Efficient C Performance Programming Techniques:

Efficient C++ Dov Bulka, David Mayhew, 2000 Far too many programmers and software designers consider efficient C to be an oxymoron They regard C as inherently slow and inappropriate for performance critical applications Consequently C has had little success penetrating domains such as networking operating system kernels device drivers and others Efficient C explodes that myth Written by two authors with first hand experience wringing the last ounce of performance from commercial C applications this book demonstrates the potential of C to produce highly efficient programs The book reveals practical everyday object oriented design principles and C coding techniques that can yield large performance improvements It points out common pitfalls in both design and code that generate hidden operating costs This book focuses on combining C's power and flexibility with high performance and scalability resulting in the best of both worlds Specific topics include temporary objects memory management templates inheritance virtual functions inlining reference counting STL and much more With this book you will have a valuable compendium of the best performance techniques at your fingertips

0201379503B04062001 **Java Performance and Scalability: Server-side programming techniques** Dov Bulka, 2000

PLEASE PROVIDE DESCRIPTION **The C++ Report**, 2000 [Dr. Dobb's Journal](#), 2004 **C++ and C Efficiency**

David Spuler, 1992 Text for the professional programmer familiar with C or C Examines techniques for speeding up progress and improving efficiency in terms of space and time Includes exercises answers a bibliography and an index **C++**

Footprint and Performance Optimization Rene Alexander, Graham Bensley, 2000 This guide fills in the knowledge gaps for intermediate advanced programmers who have been trained without regard to software performance from a program level Information on advanced tips and cautions can prepare programmers for cross platform or multilanguage projects *The*

Art of Writing Efficient Programs Fedor G. Pikus, 2021-10-22 Become a better programmer with performance improvement techniques such as concurrency lock free programming atomic operations parallelism and memory management Key

Features Learn proven techniques from a heavyweight and recognized expert in C and high performance computing Understand the limitations of modern CPUs and their performance impact Find out how you can avoid writing inefficient code and get the best optimizations from the compiler Learn the tradeoffs and costs of writing high performance programs

Book Description The great free lunch of performance taking care of itself is over Until recently programs got faster by themselves as CPUs were upgraded but that doesn't happen anymore The clock frequency of new processors has almost peaked and while new architectures provide small improvements to existing programs this only helps slightly To write

efficient software you now have to know how to program by making good use of the available computing resources and this book will teach you how to do that The Art of Efficient Programming covers all the major aspects of writing efficient programs such as using CPU resources and memory efficiently avoiding unnecessary computations measuring performance and how to put concurrency and multithreading to good use You'll also learn about compiler optimizations and how to use the

programming language C more efficiently Finally you ll understand how design decisions impact performance By the end of this book you ll not only have enough knowledge of processors and compilers to write efficient programs but you ll also be able to understand which techniques to use and what to measure while improving performance At its core this book is about learning how to learn What you will learn Discover how to use the hardware computing resources in your programs effectively Understand the relationship between memory order and memory barriers Familiarize yourself with the performance implications of different data structures and organizations Assess the performance impact of concurrent memory accessed and how to minimize it Discover when to use and when not to use lock free programming techniques Explore different ways to improve the effectiveness of compiler optimizations Design APIs for concurrent data structures and high performance data structures to avoid inefficiencies Who this book is for This book is for experienced developers and programmers who work on performance critical projects and want to learn new techniques to improve the performance of their code Programmers in algorithmic trading gaming bioinformatics computational genomics or computational fluid dynamics communities will get the most out of the examples in this book but the techniques are fairly universal Although this book uses the C language the concepts demonstrated in the book can be easily transferred or applied to other compiled languages such as C Java Rust Go and more

Journal of Object-oriented Programming ,2000 **Parallel Logic Programming Techniques** Stephen Taylor,1989 **Bibliography of Scientific and Industrial Reports** ,1965-07
U.S. Government Research & Development Reports ,1965 ACM SIGPLAN Notices ,2006 **JGI '02 ACM Special Interest Group on Programming Languages**,2002 *Electronic Design* ,1997 Proceedings of the ... International Workshop on Software and Performance ,2005 **Railway Age** ,1936 **Optimization and Tuning Guide for Fortran, C, and C++** ,1994 *The Sixth Distributed Memory Computing Conference Proceedings* Quentin F. Stout,Michael Joseph Wolfe,1991 **Electrical & Electronics Abstracts** ,1994 High-Performance System Design Vojin G. Oklobdzija,1999-07-12 This comprehensive collection of papers offers you practical information that can be used to develop high performance digital system design Specially written introductions by editor Vojin G Oklobdzija precede each chapter to aid your understanding of the most relevant topics in this advanced area of circuit design Featured topics include Differential pass transistor logic High speed circuits and design of high performance systems Advanced deep submicron circuits used in high speed computers and digital circuits Clocking and latch design essential to high performance systems Relationships between VLSI algorithms and implementation techniques HIGH PERFORMANCE SYSTEM DESIGN Circuits and Logic is indispensable reading for circuit designers practicing engineers and students who want to master the basic principles underlying high performance system design This handy single volume provides a useful reference to a collection of accumulated experience necessary for good successful designs Professors To request an examination copy simply e mail collegeadoption iee org Sponsored by IEEE Solid State Circuits Council Society

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