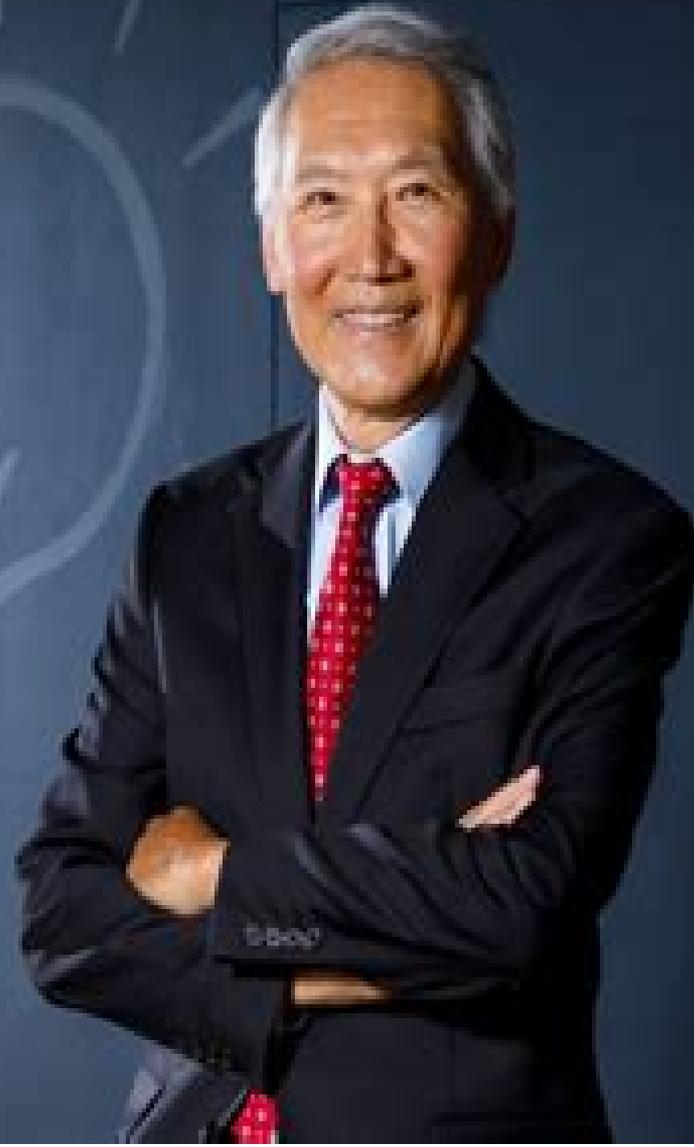


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**Modern Semiconductor Devices for Integrated Circuits** Chenming Hu, 2010 For courses in semiconductor devices Prepare your students for the semiconductor device technologies of today and tomorrow Modern Semiconductor Devices for Integrated Circuits First Edition introduces students to the world of modern semiconductor devices with an emphasis on integrated circuit applications Written by an experienced teacher researcher and expert in industry practices this succinct and forward looking text is appropriate for both undergraduate and graduate students and serves as a suitable reference text for practicing engineers

**Toward Quantum FinFET** Weihua Han, Zhiming M. Wang, 2013-11-23 This book reviews a range of quantum phenomena in novel nanoscale transistors called FinFETs including quantized conductance of 1D transport single electron effect tunneling transport etc The goal is to create a fundamental bridge between quantum FinFET and nanotechnology to stimulate readers interest in developing new types of semiconductor technology Although the rapid development of micro nano fabrication is driving the MOSFET downscaling trend that is evolving from planar channel to nonplanar FinFET silicon based CMOS technology is expected to face fundamental limits in the near future Therefore new types of nanoscale devices are being investigated aggressively to take advantage of the quantum effect in carrier transport The quantum confinement effect of FinFET at room temperatures was reported following the breakthrough to sub 10nm scale technology in silicon nanowires With chapters written by leading scientists throughout the world Toward Quantum FinFET provides a comprehensive introduction to the field as well as a platform for knowledge sharing and dissemination of the latest advances As a roadmap to guide further research in an area of increasing importance for the future development of materials science nanofabrication technology and nano electronic devices the book can be recommended for Physics Electrical Engineering and Materials Science departments and as a reference on micro nano electronic science and device design Offers comprehensive coverage of novel nanoscale transistors with quantum confinement effect Provides the keys to understanding the emerging area of the quantum FinFET Written by leading experts in each research area Describes a key enabling technology for research and development of nanofabrication and nanoelectronic devices

**FinFETs and Other Multi-Gate Transistors** J.-P. Colinge, 2008 This book explains the physics and properties of multi gate field effect transistors MuGFETs how they are made and how circuit designers can use them to improve the performances of integrated circuits It covers the emergence of quantum effects due to the reduced size of the devices and describes the evolution of the MOS transistor from classical structures to SOI silicon on insulator and then to MuGFETs

**Engineering Education**, 1982

**Mathematical Reviews**, 2006 *Mosfet Modeling For Circuit Analysis And Design* Carlos Galup-montoro, Marcio Cherem Schneider, 2007-02-27 This is the first book dedicated to the next generation of MOSFET models Addressed to circuit designers with an in depth treatment that appeals to device specialists the book presents a fresh view of compact modeling having completely abandoned the regional modeling approach Both an overview of the basic physics theory required to build

compact MOSFET models and a unified treatment of inversion charge and surface potential models are provided The needs of digital analog and RF designers as regards the availability of simple equations for circuit designs are taken into account Compact expressions for hand analysis or for automatic synthesis valid in all operating regions are presented throughout the book All the main expressions for computer simulation used in the new generation compact models are derived Since designers in advanced technologies are increasingly concerned with fluctuations the modeling of fluctuations is strongly emphasized A unified approach for both space matching and time noise fluctuations is introduced

**Official Gazette of the United States Patent and Trademark Office** United States. Patent and Trademark Office,1998 A Physical, Scalable and Efficient Deep-submicrometer MOSFET Model for VLSI Digital/analog Circuit Simulation Jian-hui Huang,1994 *1996 2nd International Conference on ASIC* Qian-Ling Zhang,1996 *Dissertation Abstracts International* ,2003 **Scientific and Technical Aerospace Reports** ,1984 Lists citations with abstracts for aerospace related reports obtained from world wide sources and announces documents that have recently been entered into the NASA Scientific and Technical Information Database Dr. Dobb's Journal ,2000 *1997 International Symposium on VLSI Technology, Systems, and Applications* ,1997 **Portable Design** ,2000 Solar and Switching Materials Carl M. Lampert,Claes G. Granqvist,Keith L. Lewis,2001 **Journal of Partial Differential Equations** ,1995 **IEEE, ACM International Conference on Computer Aided Design** International Conference on Computer Aided Design. Institute of Electrical and Electronics Engineers, 2000, San José, Calif.,2000 **Government Reports Annual Index** ,1986 **The Chinese Nation** ,1930 **China Monthly Review** ,1926

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