

Seat No.	
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S.Y.B.Sc. (Computer Science) (Semester – I) Examination, 2013
ELECTRONICS (Paper – I)
ELC – 211 : Microprocessor Architecture and Programming
(2008 Pattern)

Time : 2 Hours

Max. Marks : 40

- Instructions :** 1) *All questions are compulsory.*
2) *Figures to the right indicate full marks.*
3) *Neat diagram must be drawn whenever necessary.*

1. Answer the following questions in **one** or **two** sentences : (10×1=10)
- a) In memory stack where is the stack pointer initialised ?
 - b) Which memories can be directly accessed by the CPU ?
 - c) Why two floating point instructions are not possible at the same time ?
 - d) What will be the result of XOR CX, CX ?
 - e) What is a device driver ?
 - f) Give the location and size of IVT.
 - g) State any two advantages of a flow chart.
 - h) Identify the type of instruction for the following :
 - i) DAA
 - ii) CMC
 - i) What will be the contents of AL & BL register after executing the following ?
MOV AL, 62 H
MOV BL, AL
SHR AL, 4
AND BL, 0FH
 - j) If AH = 08 H and INT 21 H is executed, what is the processor going to do ?

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Decoding **Microprocessor Question Papers**: Revealing the Captivating Potential of Verbal Expression

In a period characterized by interconnectedness and an insatiable thirst for knowledge, the captivating potential of verbal expression has emerged as a formidable force. Its ability to evoke sentiments, stimulate introspection, and incite profound transformations is genuinely awe-inspiring. Within the pages of "**Microprocessor Question Papers**," a mesmerizing literary creation penned by way of a celebrated wordsmith, readers set about an enlightening odyssey, unraveling the intricate significance of language and its enduring effect on our lives. In this appraisal, we shall explore the book's central themes, evaluate its distinctive writing style, and gauge its pervasive influence on the hearts and minds of its readership.

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