

An Efficient Digit-by-Digit Decimal Square Root Algorithm Using Non-restoring Pseudo-Division

by C. Bond, ©2003

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Abstract

A square root algorithm optimized for hand held calculators has been previously disclosed in an article by Egbert [1]. The algorithm is similar to other digit-by-digit decimal algorithms published elsewhere, but with a number of improvements to better adapt the method to a class of BCD calculators. In this paper, a further improvement is disclosed which brings the advantages of non-restoring division, also described in the literature, to the pseudo-division process used in taking square roots.

1 BCD Square Root Algorithm

The basic algorithm we will use is completely described in the article by Egbert and the reader is advised to consult the original paper for a detailed explanation.

Egbert shows that the method for computing square roots closely follows the pencil and paper methods taught in school, but includes modifications to reduce the number of extraneous computations. He also shows how to exploit certain properties of the electronic storage medium. For example, a decimal digit can be easily multiplied by ten by simply shifting it to the adjacent position.

1.1 Pseudo-division

The central computation in the algorithm involves calculating each result digit successively, from highest to lowest. The method used for each digit is the same, and requires subtracting an iteratively modified quantity from a suitable starting value until underflow occurs.¹ This process resembles division, except for the repeated updates to the subtractor, and is referred to as *pseudo-division* in the literature.

When underflow occurs, according to Egbert, the last subtractor is added back in, thus restoring the least positive value of the result for the next operation. Note that when this strategy is applied to

¹Egbert refers to *underflow as overflow* in the article.

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Maximilian Krahn



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Division and Square Root Milos Ercegovac, Tomas Lang, 1994-03-31 Division and Square Root Digit Recurrence Algorithms and Implementations is intended for researchers into division and square root and related operations as well as for designers of the corresponding arithmetic units either for general purpose processors or for special purpose components of systems for applications such as signal and image processing The book can also be used in graduate courses on arithmetic algorithms and processors As the capabilities of IC technologies improve hardware implementation of all basic arithmetic operations is becoming common in the design of processors While the design of fast and efficient adders and multipliers is well understood division and square root remain a serious design challenge The reasons are the intrinsic dependence among the iteration steps and the complexity of the result digit generation function To limit the effect of these on the execution time an extensive theory has been developed based on concepts such as redundant number representations prediction of result digits and operand scaling The authors give a unified presentation of the most relevant aspects of this theory This can serve as the basis of specific implementations as well as the foundations for further research Division and Square Root Digit Recurrence Algorithms and Implementations integrates a vast amount of research The authors have drawn on results of many researchers as well as on their own work A comprehensive bibliography is provided as well as bibliographical notes after each chapter

Secrets of Root Algorithms Maximilian Krahn, 2021-02-06 What s the quickest way to calculate the square root of a number What about the cube root What is a root algorithm How is it used for mental calculation How can logarithms simplify the process of taking a root Secrets of root algorithms gives the reader a broad selection of algorithms used to master mentally calculating any root of any power The algorithms do not only vary on the kind of root to solve but also on what strategy one wants to use Learning such algorithms improves the understanding and estimating the magnitude of numbers in real life Maximilian Krahn born in Sydney Australia and now living in Switzerland has achieved podium positions at several World and European Junior Championships of Mental Calculation as well as other mathematics competitions He currently studies physics at the ETH Zurich and is an active member of Mensa Switzerland *Improved Algorithms for Non-restoring Division and Square Root* Kihwan Jun, 2012 This dissertation focuses on improving the non restoring division and square root algorithm Although the non restoring division algorithm is the fastest and has less complexity among other radix 2 digit recurrence division algorithms there are some possibilities to enhance its performance To improve its performance two new approaches are proposed here In addition the research scope is extended to seek an efficient algorithm for implementing non restoring square root which has similar steps to non restoring division For the first proposed approach the non restoring divider with a modified algorithm is presented The new algorithm changes the order of the flowchart which reduces one unit delay of the multiplexer per every iteration In addition a new method to find a correct quotient is presented and it removes an error that the quotient is always odd number after a digit conversion from a digit

converter from the quotient with digits 1 and 1 to conventional binary number The second proposed approach is a novel method to find a quotient bit for every iteration which hides the total delay of the multiplexer with dual path calculation The proposed method uses a Most Significant Carry MSC generator which determines the sign of each remainder faster than the conventional carry lookahead adder and it eventually reduces the total delay by almost 22% compared to the conventional non restoring division algorithm Finally an improved algorithm for non restoring square root is proposed The two concepts already applied to non restoring division are adopted for improving the performance of a non restoring square root since it has similar process to that of non restoring division for finding square root Additionally a new method to find intermediate quotients is presented that removes an adder per an iteration to reduce the total area and power consumption The non restoring square root with MSC generator reduces total delay area and power consumption significantly

A Transparent Square Root Algorithm to Beat Brute Force for Sufficiently Large Primes of the Form $P = 4n + 3$ Michael R. Spink, 2018 Finding square roots in the modular integers is a well known problem that is the basis for many modern cryptosystems For primes of the form $p = 4n + 3$ given C in \mathbb{Z}_p times finding solutions to $x^2 \equiv C \pmod{p}$ is deterministic For primes of the form $p = 4n + 1$ no known deterministic computation exists for determining x given C Tonelli later improved by Shanks Cipolla and Pocklington among others found sophisticated algorithms to perform this task Brute force is a transparent approach but offers no insights into the problem In this thesis we produce a transparent approach to this problem visualized using a model built on Symplectic Geometry One of the insights from viewing the problem in this way is a conjecture on the distribution of quadratic residues which we exploit in our algorithm Even though the conjecture is not essential to the workings of the algorithm it gives it an edge over brute force for large enough primes Finally we follow this with examples of the algorithm's execution

Abstract **Efficient Initial Approximation and Fast Converging Algorithms for Division and Square Root** Kyōto Daigaku. Joho Kogaku Kyoshitsu, Masayuki Ito, Naofumi Takagi, Shuzo Yajima, 1995 Abstract With the increasing availability of high speed multiplication units convergence type algorithms have become advantageous to the fast calculation of division and square root Efficient initial approximation methods and fast converging algorithms are important to achieve the desired precision with a small number of operations and with small look up tables In this paper a new initial approximation method for division and accelerated higher order converging division algorithm and a new square rooting algorithm are proposed They are suitable for implementation on an arithmetic unit where one multiply accumulate operation can be executed in one cycle In the case of double precision division the combination of our initial approximation method and our converging algorithm requires only 1/5 to 1/50 the size of look up tables as the Newton Raphson based method Our new square rooting algorithm can form a double precision square root faster using smaller look up tables than the Newton Raphson method

Two square root algorithms utilizing multiplication as the iterative operator James Richard Goodman, 1969 An Algorithm for Inverse Square-roots J. J. Modi, J. S. Rollett, 1984

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