

4-bit Synchronous Counter (74LS161)

```
// 4-bit counter

module counter_4bit(clk, nclr, nload, ent, enp, rco, count, parallel_in) :

    input  clk ;
    input  nclr ;
    input  nload ;
    input  ent ;
    input  enp ;
    output rco ;
    output [3:0] count ;
    input  [3:0] parallel_in ;

    reg    [3:0] count ;
    reg    rco ;

    always @ (count or ent)
    if ((count == 15) && (ent == 1'b1))
        rco = 1'b1 ;
    else
        rco = 1'b0 ;

    always @ (posedge clk or negedge nclr)
    begin
        if (nclr == 1'b0)
            count <= 4'd0 ;
        else if (nload == 1'b0)
            count <= parallel_in ;
        else if ((ent == 1'b1) && (enp == 1'b1))
            count <= (count + 1) & 16 ;
    end
endmodule
```

4 Bit Counter Verilog Code Davefc

Rachel Sandford



4 Bit Counter Verilog Code Davefc:

This book delves into 4 Bit Counter Verilog Code Davefc. 4 Bit Counter Verilog Code Davefc is a crucial topic that must be grasped by everyone, from students and scholars to the general public. The book will furnish comprehensive and in-depth insights into 4 Bit Counter Verilog Code Davefc, encompassing both the fundamentals and more intricate discussions.

1. This book is structured into several chapters, namely:
 - Chapter 1: Introduction to 4 Bit Counter Verilog Code Davefc
 - Chapter 2: Essential Elements of 4 Bit Counter Verilog Code Davefc
 - Chapter 3: 4 Bit Counter Verilog Code Davefc in Everyday Life
 - Chapter 4: 4 Bit Counter Verilog Code Davefc in Specific Contexts
 - Chapter 5: Conclusion
 2. In chapter 1, the author will provide an overview of 4 Bit Counter Verilog Code Davefc. The first chapter will explore what 4 Bit Counter Verilog Code Davefc is, why 4 Bit Counter Verilog Code Davefc is vital, and how to effectively learn about 4 Bit Counter Verilog Code Davefc.
 3. In chapter 2, the author will delve into the foundational concepts of 4 Bit Counter Verilog Code Davefc. This chapter will elucidate the essential principles that must be understood to grasp 4 Bit Counter Verilog Code Davefc in its entirety.
 4. In chapter 3, the author will examine the practical applications of 4 Bit Counter Verilog Code Davefc in daily life. This chapter will showcase real-world examples of how 4 Bit Counter Verilog Code Davefc can be effectively utilized in everyday scenarios.
 5. In chapter 4, this book will scrutinize the relevance of 4 Bit Counter Verilog Code Davefc in specific contexts. This chapter will explore how 4 Bit Counter Verilog Code Davefc is applied in specialized fields, such as education, business, and technology.
 6. In chapter 5, the author will draw a conclusion about 4 Bit Counter Verilog Code Davefc. The final chapter will summarize the key points that have been discussed throughout the book.
- The book is crafted in an easy-to-understand language and is complemented by engaging illustrations. It is highly recommended for anyone seeking to gain a comprehensive understanding of 4 Bit Counter Verilog Code Davefc.

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