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Paul Cockshott, Kenneth Renfrew



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The Business Of Book Publishing Elizabeth Geiser, Arnold Dolin, Gladys Topkis, 2019-06-04 When the first University of Denver Publishing Institute came to a dose in August 1976 all of us involved in its launching knew that we had a real success on our hands And we knew it was due in great measure to an outstanding faculty of more than forty top publishing executives who had come to Denver during those four weeks to teach our students How regrettable it seemed that their knowledge and expertise were available only to the eighty students handpicked for that first class Fred Praeger publisher of Westview Press suggested a solution Do a book he invited and let Westview publish the curriculum for others to share

The Complete Diagnosis Coding Book Shelley Safian, 2011-01-14 The Complete Diagnosis Coding Book introduces students to the coding process and uses case studies examples and a lighter writing style to sit side by side with the student and help them through learning to code This book speaks directly to students with respect and helps them to overcome the intimidation of learning such an important new skill Case studies examples and exercises are salted generously throughout the book to connect the reader with real life scenarios and to help them relate to the issues at hand

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[SIMD Programming Manual for Linux and Windows](#) Paul Cockshott, Kenneth Renfrew, 2013-03-09 A number of widely used contemporary processors have instruction set extensions for improved performance in multi media applications The aim is to allow operations to proceed on multiple pixels each clock cycle Such instruction sets have been incorporated both in specialist DSPchips such as the Texas C62xx Texas Instruments 1998 and in general purpose CPU chips like the Intel IA32 Intel 2000 or the AMD K6 Advanced Micro Devices 1999 These instruction set extensions are typically based on the Single Instruction stream Multiple Data stream SIMD model in which a single instruction causes the same mathematical operation to be carried out on several operands or pairs of operands at the same time The level of parallelism supported ranges from two floating point operations at a time on the AMD K6 architecture to 16 byte operations at a time on the Intel P4 architecture Whereas processor architectures are

moving towards greater levels of parallelism the most widely used programming languages such as C Java and Delphi are structured around a model of computation in which operations take place on a single value at a time This was appropriate when processors worked this way but has become an impediment to programmers seeking to make use of the performance offered by multi media instruction sets The introduction of SIMD instruction sets Peleg et al

The Stephen Cobb User's Guide to FileMaker Stephen Cobb, Chey Romfo, 1991 Expert author Cobb has teamed with veteran computer user Romfo to provide a comprehensive course in tapping the power of Filemaker II the 1 bestselling database product for the Macintosh The authors worked directly with the software publisher on the book

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American Horticulturist, 1995

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